



BMS COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

INSTRUMENTATION TECHNOLOGY

Scheme & Syllabus

2010-2014

Core & Cluster Electives

(III - VIII Semesters)



BMS COLLEGE OF ENGINEERING, BANGALORE-19

Autonomous College under VTU

Department: INSTRUMENTATION TECHNOLOGY

Program: Electrical Cluster (EE/EC/TC/ML/IT)

Semester: III

Course Code											CourseTitle	L	T	P	Credits	CIE	SEE	Total
1	1	M	A	3	I	C	M	A	T	Engineering Mathematics-III	3	2	0	4	50	50	100	
1	0	E	S	3	G	C	N	A	L	Network Analysis	4	0	0	4	50	50	100	
1	1	E	S	3	G	C	A	E	C	Analog Electronic Circuits	4	0	1	5	50	50	100	
1	1	E	S	3	G	C	D	E	C	Digital Electronics	4	0	1	5	50	50	100	
1	1	E	S	3	G	C	S	A	S	Signals & Systems (EC/EE/IT)	4	0	0	4	50	50	100	
1	0	E	S	3	G	C	M	S	T	Measurement Techniques (EE/IT/ML)	3	0	0	3	50	50	100	
Total											22	2	2	25	300	300	600	

GE- Group Elective L – Lecture Hours / week; T- Tutorial Lecture Hours / week; P-Practical Lecture Hours / week. CIE- Continuous Internal Evaluation; SEE- Semester End Examination (of 3 Hours duration)



BMS COLLEGE OF ENGINEERING, BANGALORE-19

Autonomous College under VTU

Department: INSTRUMENTATION TECHNOLOGY

Program: Electrical Cluster (EE/EC/TC/ML/IT)

Semester: IV

Course Code										Course Title	Hours/Week			Credits	CIE	SEE	Total
											L	T	P				
1	1	M	A	4	I	C	M	A	T	Engineering Mathematics-IV	3	2	0	4	50	50	100
1	1	E	S	4	G	C	L	I	C	Op-amps and Linear ICs	4	0	1	5	50	50	100
1	1	E	S	4	G	C	M	C	S	Microcontrollers	3	0	1	4	50	50	100
1	1	E	S	4	G	C	F	T	H	Field Theory (EC/EE/IT/TC)	4	0	0	4	50	50	100
0	9	E	S	4	G	C	C	S	T	Control Systems	4	0	0	4	50	50	100
0	9	E	S	4	G	C	H	D	L	Fundamentals of HDL (EC/TC/ML/IT)	3	0	1	4	50	50	100
Total										21	2	3	25	300	300	600	

GE- Group Elective L – Lecture Hours / week; T- Tutorial Lecture Hours / week; P-Practical Lecture Hours / week. CIE- Continuous Internal Evaluation; SEE- Semester End Examination (of 3 Hours duration)



BMS COLLEGE OF ENGINEERING, BANGALORE-19

Autonomous College under VTU

Department: INSTRUMENTATION TECHNOLOGY

Program: IT

Semester: V

Course Code											CourseTitle	Hours/Week			Credits	Marks		
												L	T	P		CIE	SEE	Total
1	0	I	T	5	D	C	T	D	R		Transducers	4	0	1	5	50	50	100
1	0	E	S	5	G	C	D	S	P		Digital Signal Processing	4	0	1	5	50	50	100
1	2	I	T	5	D	C	P	C	S		Process control	4	0	1	5	50	50	100
1	0	I	T	5	D	C	A	N	I		Analytical Instrumentation	3	0	0	3	50	50	100
1	0	I	T	5	D	C	C	S	T		Communication Systems	4	0	0	4	50	50	100
1	0	X	X	5	G	E	1	X	X		Elective I	4	0	0	4	50	50	100
Total														26	300	300	600	

DC- Department Core , L Lecture Hours / week; T- Tutorial Lecture Hours / week; P-Practical Lecture Hours / Week. CIE- Continuous Internal Evaluation; SEE- Semester End Examination (of 3 Hours duration)



BMS COLLEGE OF ENGINEERING, BANGALORE-19

Autonomous College under VTU

Department: INSTRUMENTATION TECHNOLOGY

Program: IT

Semester: VI

Course Code											CourseTitle	Hours/Week			Credits	Marks		
												L	T	P		CIE	SEE	Total
1	2	I	T	6	D	C	M	C	T		Modern control theory	4	0	0	4	50	50	100
1	0	I	T	6	D	C	D	V	I		Data Acquisition & Virtual Instrumentation	3	0	1	4	50	50	100
1	2	I	T	6	D	C	B	M	I		Biomedical Instrumentation	4	0	1	5	50	50	100
1	0	I	T	6	D	C	D	S	A		DSP algorithms and Architecture	3	0	1	4	50	50	100
1	0	X	X	6	G	E	2	X	X		Elective II	4	0	0	4	50	50	100
1	0	X	X	6	G	E	3	X	X		Elective III	4	0	0	4	50	50	100
Total														25	300	300	600	

DC- Department Core , L Lecture Hours / week; T- Tutorial Lecture Hours / week; P-Practical Lecture Hours / Week. CIE- Continuous Internal Evaluation; SEE- Semester End Examination (of 3 Hours duration)



BMS COLLEGE OF ENGINEERING, BANGALORE-19

Autonomous College under VTU

Department: INSTRUMENTATION TECHNOLOGY

Program: IT

Semester: VII

Course Code											CourseTitle	Hours/Week			Credits	Marks		
												L	T	P		CIE	SEE	Total
1	2	I	T	7	D	C	V	L	I		VLSI	4	0	1	5	50	50	100
1	2	I	T	7	D	C	A	P	C		Automation in Process Control	3	0	1	4	50	50	100
1	1	X	X	7	D	E	4	X	X		Elective - IV	4	0	0	4	50	50	100
1	1	X	X	7	D	E	5	X	X		Elective - V	4	0	0	4	50	50	100
1	1	X	X	7	I	E	1	X	X		Institutional Elective I	4	0	0	4	50	50	100
1	2	I	T	7	D	C	P	R	J		Project Work - I	0	0	6	4	50	50	100
Total														25			550	

DC- Department Core , L Lecture Hours / week; T- Tutorial Lecture Hours / week; P-Practical Lecture Hours / Week. CIE- Continuous Internal Evaluation; SEE- Semester End Examination (of 3 Hours duration)



BMS COLLEGE OF ENGINEERING, BANGALORE-19

Autonomous College under VTU

Department: INSTRUMENTATION TECHNOLOGY

Department/Cluster: Electrical Sciences

Program: I.T.

Semester: VIII

Course Code	CourseTitle	L	T	P	Credits	Marks		
						CIE	SEE	Total
11HS8GCIPR	Intellectual Property Rights	2	0	0	2	50	50	100
11HS8GCPRM	Project Management	2	0	0	2	50	50	100
11XX8IE2XX	Institutional Elective II	4	0	0	4	50	50	100
12IT8DCPRJ	Project	0	0	21	14	150	150	300
11IT8DCSMR	Technical Seminar	2	0	0	2	50	50	100
Total					24			700

L – Lecture Hours / week; T- Tutorial Lecture Hours / week; P-Practical Lecture Hours / week.
CIE- Continuous Internal Evaluation; SEE- Semester End Examination (of 3 Hours duration)



BMS COLLEGE OF ENGINEERING, BANGALORE-19

Autonomous College under VTU

Department: INSTRUMENTATION TECHNOLOGY

Group I Electrical Cluster Electives (Programs: EC/TC/IT/EE/ML)

Semester: V

Course Code										CourseTitle	Hours/Week			Credits	CIE	SEE	Total
											L	T	P				
1	0	E	S	5	G	E	1	0	P	Objected oriented programming using C++	4	0	0	4	50	50	100
1	2	E	S	5	G	E	1	D	D	Digital System Design using VHDL (Except EE)	3	0	2	4	50	50	100
1	2	E	S	5	G	E	1	I	T	Essentials of Information Technology	3	0	2	4	50	50	100
1	2	E	C	5	G	E	1	D	C	Digital Control of Dynamic Systems (Except IT)	4	0	0	4	50	50	100
1	2	M	L	5	G	E	1	M	P	Medical Physics	4	0	0	4	50	50	100
1	0	M	L	5	G	E	1	D	S	Data Structures with C++ (Except ML)	3	0	2	4	50	50	100
1	0	E	E	5	G	E	1	C	S	Communication Systems (EE only)	4	0	0	4	50	50	100
1	2	E	S	5	G	E	1	H	D	Fundamentals of HD L (EE only)	3	0	2	4	50	50	100



BMS COLLEGE OF ENGINEERING, BANGALORE-19
Autonomous College under VTU

Group II Electrical Cluster Electives (Programs: EC/TC/IT/EE/ML)

Semester: VI

Course Code										CourseTitle	Hours/Week			Credits	CIE	SEE	Total
											L	L	L				
1	0	E	E	6	G	E	2	F	V	Fundamentals of VLSI (EE only)	4	0	0	4	50	50	100
1	0	E	E	6	G	E	2	U	P	Utilization of Electrical Power	4	0	0	4	50	50	100
1	2	E	S	6	G	E	2	O	S	Operating Systems Concepts	4	0	0	4	50	50	100
1	2	E	S	6	G	E	2	M	C	Advanced Microcontroller and Applications	3	0	2	4	50	50	100
1	0	T	C	6	G	E	2	S	A	Introduction to Speech and Audio Processing	3	0	2	4	50	50	100
1	2	T	C	6	G	E	2	C	J	Objected oriented programming using C++ & Java (Not for those who have taken 12ES5GE1OP)	4	0	0	4	50	50	100
1	0	M	L	6	G	E	2	S	N	Bio Sensors	4	0	0	4	50	50	100
1	0	M	L	6	G	E	2	B	S	Bio Statistics	4	0	0	4	50	50	100
1	0	I	T	6	G	E	2	M	D	Biomedical DSP (Except ML)	3	0	2	4	50	50	100
1	2	I	T	6	G	E	2	M	T	MEMS	4	0	0	4	50	50	100



BMS COLLEGE OF ENGINEERING, BANGALORE-19

Autonomous College under VTU

Group III Electrical Cluster Electives (Programs: EC/TC/IT/EE/ML)

Semester: VI

Course Code										Course Title	Hours/Week			Credits	CIE	SEE	Total
											L	L	L				
1	2	E	E	6	G	E	3	E	D	Embedded System Design	4	0	0	4	50	50	100
1	0	E	E	6	G	E	3	E	I	Electronic Instrumentation (EE only)	4	0	0	4	50	50	100
1	0	E	E	6	G	E	3	R	E	Renewable Energy Resources	4	0	0	4	50	50	100
1	0	T	C	6	G	E	3	R	T	Real Time Embedded Systems	4	0	0	4	50	50	100
1	2	E	C	6	G	E	3	A	E	Automotive Embedded Systems Development Technology	4	0	0	4	50	50	100
1	2	E	S	6	G	E	3	I	P	Fundamentals of Image processing (Except ML)	3	0	2	4	50	50	100
1	0	T	C	6	G	E	3	M	M	Design of Analog and Mixed mode VLSI circuits (Only TC)	4	0	0	4	50	50	100
1	0	M	L	6	G	E	3	B	C	Biomedical circuits with VLSI	4	0	0	4	50	50	100
1	0	M	L	6	G	E	3	R	E	Rehabilitation Engineering	4	0	0	4	50	50	100
1	0	I	T	6	G	E	3	R	B	Robotics	4	0	0	4	50	50	100
1	2	I	T	6	G	E	3	O	I	Optical Instrumentation I	4	0	0	4	50	50	100



BMS COLLEGE OF ENGINEERING, BANGALORE-19
Autonomous College under VTU

Group IV Electrical Cluster Electives (Programs: EC/TC/IT/EE/ML)

Semester: VII

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Course Code										Course Title	Hours/Week			Credits	CIE	SEE	Total
											L	L	L				
1	1	E	E	7	G	E	4	P	S	Power Systems Operation and Control	4	0	0	4	50	50	100
1	1	E	E	7	G	E	4	I	D	Industrial Drives and Applications	4	0	0	4	50	50	100
1	1	T	C	7	G	E	4	M	C	Low power Microcontroller (Except ML)	3	0	2	4	50	50	100
1	1	T	C	7	G	E	4	S	R	Software Defined Radio (only EC, TC)	4	0	0	4	50	50	100
1	2	E	S	7	G	E	4	M	C	Multimedia Communication	4	0	0	4	50	50	100
1	2	M	L	7	G	E	4	B	M	Biometrics	4	0	0	4	50	50	100
1	2	M	L	7	G	E	4	A	V	Introduction to Audio & Video processing	4	0	0	4	50	50	100
1	1	E	C	7	G	E	4	W	C	Wireless communication (Except TC)	4	0	0	4	50	50	100
1	1	E	C	7	G	E	4	E	S	Embedded Systems Design	4	0	0	4	50	50	100
1	1	I	T	7	G	E	4	D	C	Distributed Computing	4	0	0	4	50	50	100
1	1	I	T	7	G	E	4	M	I	Medical Imaging Systems (Except ML)	4	0	0	4	50	50	100



BMS COLLEGE OF ENGINEERING, BANGALORE-19
Autonomous College under VTU

Group V Electrical Cluster Electives (Programs: EC/TC/IT/EE/ML)

Semester: VII

Course Code										CourseTitle	Hours/Week			Credits	CIE	SEE	Total
											L	L	L				
1	2	E	E	7	G	E	5	P	Q	Electrical Power Quality	4	0	0	4	50	50	100
1	1	E	E	7	G	E	5	S	P	Switch Mode Power Supplies	4	0	0	4	50	50	100
1	1	T	C	7	G	E	5	E	M	EMC -EMI	4	0	0	4	50	50	100
1	1	T	C	7	G	E	5	S	C	Satellite Communication	4	0	0	4	50	50	100
1	1	E	S	7	G	E	5	A	D	ASIC Design	4	0	0	4	50	50	100
1	1	M	L	7	G	E	5	I	P	Advanced Medical Image Processing	4	0	0	4	50	50	100
1	1	M	L	7	G	E	5	S	P	Advanced Biomedical Digital Signal Processing	4	0	0	4	50	50	100
1	1	E	S	7	G	E	5	N	S	Network Security	4	0	0	4	50	50	100
1	1	E	C	7	G	E	5	L	P	Low Power VLSI design	4	0	0	4	50	50	100
1	1	I	T	7	G	E	5	E	S	Embedded System and RTOS	4	0	0	4	50	50	100
1	1	I	T	7	G	E	5	C	N	Computer Communication Networks (Except TC & EC)	4	0	0	4	50	50	100

B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

III SEMESTER

Subject	ENGINEERING MATHEMATICS III	Sub.Code	11MA3ICMAT
Credits	04	L-T-P	3-1-0

COURSE OBJECTIVES

CO1: Express given functions to form Fourier series.

CO2: Demonstrate an understanding of Fourier transforms techniques.

CO3: Employ analytical techniques to solve partial differential equations with appropriate boundary conditions.

CO4: Compute interpolating polynomials, derivatives, integrals for a given function from a given data.

CO5: Apply Z- transforms techniques to solve difference equations.

CO6: Use calculus of variations to find the external of a functional.

UNIT I

FOURIER SERIES

[09 hours]

Infinite series, convergence and divergence of infinite series of positive terms, power series, periodic function, Dirichlet's conditions, statement of Fourier Theorem, Fourier series of periodic function of period 2 and arbitrary period, half range Fourier series, practical harmonic analysis.

[7 L + 2 T]

UNIT II

FOURIER TRANSFORMS

[09hours]

Infinite Fourier transform, Fourier Sine and Cosine transforms, properties, Inverse transforms, Convolution theorem (statement only), Parseval's identities for Fourier transform. Fourier transforms of the derivatives of a function.

[7L+2 T]



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT III

PARTIAL DIFFERENTIAL EQUATIONS [12 hours]

Formation of Partial differential equations-elimination of arbitrary constants, elimination of arbitrary functions. Equations of first order- The linear equation $P p + Q q = R$ (Lagrange's partial differential equation). Method of separation of variables. [5L+2T]

APPLICATIONS OF PARTIAL DIFFERENTIAL EQUATIONS

One-dimensional heat equation and wave equation (without proof), various possible solutions of these by the method of separation of variables, D'Alembert's solution of wave equation. [4L+1T]

UNIT IV

NUMERICAL METHODS [11 hours]

Finite Differences and interpolation: Forward differences, Back ward differences. Interpolation: Newton-Gregory forward interpolation formula, Newton-Gregory backward interpolation formula, Lagrange's interpolation formula, Lagrange's inverse interpolation. Numerical differentiation: Numerical differentiation using Newton-Gregory forward and backward interpolation formula. [4L+2T]

Numerical integration: Trapezoidal rule, Simpson's 1/3rd rule, Simpson's 3/8th rule, Weddle's rule. Solution of algebraic and transcendental equations: Newton-Raphson method, Numerical solution of ordinary differential equations: Euler's modified method, Runge - Kutta method of fourth order. [4L+1T]

UNIT V

Z -TRANSFORMS [11 hours]

Definition, Properties, Transforms of standard functions, Inverse transforms. APPLICATIONS OF Z-TRANSFORMS

Solution of difference equations using Z- transforms. [5L+1T]



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

CALCULUS OF VARIATIONS

Variation of function and functional, Euler's equation, variational problem.

APPLICATIONS OF CALCULUS OF VARIATIONS

Geodesics of a right circular cylinder, minimal surface of revolution, hanging chain, Brachistochrone problem. [4L+1T]

TEXT BOOKS:

1. Advanced Engineering Mathematics, Erwin Kreyszig, 8th edition, 2007, Wiley-India
2. Higher Engineering Mathematics, B.S. Grewal, 40th edition, 2007, Khanna Publishers.
3. Introductory methods of Numerical Analysis, S. S. Sastry, 3rd edition, 1999, Prentice-Hall of India.

REFERENCE BOOKS

1. Advanced Modern Engineering Mathematics, Glyn James, 3rd edition, 2004, Pearson Education.
2. Higher Engineering Mathematics, B.V. Ramana, 2007, Tata Mc. Graw Hill.
3. Advanced Engineering Mathematics, P. V. O'Neil, 5th Indian reprint, 2009, Cengage learning India Pvt. Ltd.
4. Advanced Engineering Mathematics, 3rd edition by Dennis G. Zill and Cullen, Jones and Bartlett India Pvt. Ltd..



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

III SEMESTER ELECTRICAL CLUSTER

Subject	NETWORK ANALYSIS	Sub.Code	10ES3GCNAL
Credits	04	L-T-P	4-0-0

COURSE OBJECTIVES

CO1: Describe about forming various types of electrical network configuration

CO2: Analyze the solutions of different types of electrical networks

CO3: Solve the transient behaviour of networks for different types of networks.

CO4: Apply the knowledge of transforms in solving the network problems.

UNIT I

Basic Concepts: [08 hours]

Practical sources, Source transformations, Network reduction using Star Delta transformation, Loop and node analysis with linearly dependent and independent sources for DC and AC networks, Concepts of super node and super mesh.

UNIT II

Network Topology: [10 hours]

Graph of a network, Concept of tree and co-tree, incidence matrix, tie-set, tie-set & cut-set schedules, Formulation of equilibrium equations, Principle of duality.

Resonant Circuits: Series and parallel resonance, frequency response of series and Parallel circuits, Q factor, Bandwidth

UNIT III

Network Theorems : [12 hours]

Superposition, Reciprocity, Millman's, Thevinin's and Norton's theorems; Maximum Power transfer theorem



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT IV

Transient behavior and initial conditions: [14 hours]
Behavior of circuit elements under switching condition and their representation, evaluation of initial and final conditions in RL, RC and RLC circuits

Laplace Transformation & Applications

Review of Laplace transforms, waveform Synthesis, initial and final value theorems, step, ramp and impulse responses, convolution theorem, solution of simple R-L, R-C, R-L-C networks for AC and DC excitations using Laplace transforms.

UNIT V

Two port network parameters and State Variable analysis: [08 hours]
Definition of z , y , h and transmission parameters, modeling with these parameters, relationship between parameters sets. Writing stage equations and solution using Laplace transforms.

TEXT BOOKS:

1. "Network Analysis", M. E. Van Valkenburg, PHI / Pearson Education, 3rd Edition. Reprint 2002.
2. "Networks and systems", Roy Choudhury, 2nd edition, 2006 re-print, New Age International Publications.
3. Theory and Problems of Electric Circuits (Schaum Series), 2nd Edition Mc Graw Hill

REFERENCE BOOKS:

1. "Engineering Circuit Analysis", Hayt, Kemmerly and Durbin, TMH 6th Edition, 2002
2. "Network analysis and Synthesis", Franklin F. Kuo, Wiley
3. "Analysis of Linear Systems", David K. Cheng, Narosa Publishing House, 11th reprint, 2002
4. "Circuits", Bruce Carlson, Thomson Learning, 2000. Reprint 2002



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

III SEMESTER ELECTRICAL CLUSTER

Subject	ANALOG ELECTRONIC CIRCUITS	Sub.Code	11ES3GCAEC
Credits	05	L-T-P	4-0-1

COURSE OBJECTIVES

CO1: Identify, describe models and transfer characteristic of analog circuits using circuit elements like resistor, capacitors, BJT's, FET's.

CO2: Design and evaluate basic multistage and power amplifiers for the required specification.

CO3: Describe and design feedback and oscillator circuits for practical applications.

CO4: Design and analysis of amplifier circuits using FET.

UNIT I

Semiconductor Diodes [10 hours]

Semiconductor diode, ideal versus practical, resistance levels, diode equivalent circuits, transition and diffusion capacitance, reverse recovery time, diode specification sheets, semiconductor diode notation, diode testing.

Diode Applications Introduction, load line analysis, series diode configurations, parallel and series parallel configurations, clippers, clampers, voltage multipliers.

UNIT II

DC biasing of BJTS [12 hours]

Introduction, operating point, fixed bias circuit, emitter bias, voltage divider bias, dc bias with voltage feedback, miscellaneous bias configurations, design operations, transistor switching networks, troubleshooting techniques, bias stabilization.

BJT AC Analysis Introduction, amplification in the ac domain, BJT transistor modeling, r_e transistor model, the hybrid equivalent model, CE fixed bias, voltage divider bias, CE emitter bias, Determining the current gain, Effect of R_L and R_s , two-port system approach, summary tables, complete hybrid equivalent model, problems on h parameters(only CE configuration)



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT III

[10 hours]

Cascaded systems, Darlington connections, BJT frequency response : Introduction, logarithms, decibels, general frequency considerations, low frequency analysis bode plot, BJT low frequency response, miller effect capacitance, BJT high frequency response

UNIT IV

[10 hours]

Feedback Circuits (BJT version only) Feedback concepts, feedback connection types, practical feedback circuits, Power amplifiers Introduction definitions and amplifier types, series fed class A amplifier, transformer coupled class A amplifier, class B amplifier operation, class B amplifier circuits. Amplifier distortion, class C and class D amplifiers.

UNIT V

[10 hours]

FETs Introduction, construction and characteristics of JFETs, transfer characteristics, important relationships, JFET small signal model, JFET fixed bias, self bias, voltage divider configuration, JFET source follower, Depletion and Enhancement type MOSFETS. UJT principle and characteristics

LAB Experiments: Clipping, clamping, Bridge rectifiers, RC coupled amplifiers, Darlington emitter follower, RC phase shift & crystal oscillator, voltage series feedback amplifier, JFET static characteristics, UJT relaxation oscillator, Simulation experiments using Multisim/P-Spice

TEXT BOOK:

Electronic Devices and Circuit Theory- Boylestad and Louis Nashelsky, 9th edition, Pearson

REFERENCE BOOKS:

1. Electronic Devices and Circuits- Millman and Halkias, TMH
2. Electronic Devices and Circuits- David A Bell - PHI 4th edition



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

III SEMESTER ELECTRICAL CLUSTER

Subject	Digital Electronics	Sub.Code	11ES3GCDEC
Credits	05	L-T-P	4-0-1

COURSE OBJECTIVES

JCO1 : Ability to design, simplify, analyze and implement Boolean functions using basic gates and universal logic gates.

JCO2: Design, analyze and implement Combinational circuits with MSI and LSI Integrated Circuits.

JCO3: Design, analyze and implement simple sequential circuits using Flip-flops and complex sequential circuits using state machines.

UNIT I

[11 hours]

Introduction :Review of Boolean algebra, logic gates.

Simplification of Boolean functions : The Map Method, Two and Three Variable Maps, Four Variable Map, Five and Six variable Maps, Product of sums simplification, NAND and NOR implementation, Other Two level implementations, Don't care conditions, The Tabulation Method, Determination of Prime Implicants, Selection of prime implicants , Concluding Remarks

UNIT II

[11 hours]

Combinational Logic Circuits: Introduction, Design Procedure, Adders, Subtractors, Code conversion

Combinational Logic with MSI and LSI:

Introduction, Binary Parallel Adder, Decimal Adder, Magnitude Comparator, Decoders, Multiplexers, Programmable Logic Devices, Programmable Read Only memories (PROMs), Programmable Logic Arrays(PLAs), Programmable array logic (PAL)



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT III

[10 hours]

Flip-Flops and Simple Flip Flops Applications:

The Basic Bistable Element, Latches, Timing Considerations, Master Slave Flip-Flops(pulse-Triggered Flip-flops), Edge Triggered Flip Flops, Characteristic Equations

UNIT IV

[09 hours]

Sequential Logic Circuits:

Registers, Counters, Design of Synchronous Counters

UNIT V

Synchronous Sequential circuits:

[11hours]

Structure and Operation of Clocked Synchronous sequential Networks, Analysis of clocked synchronous sequential networks, Modeling clocked synchronous sequential network behavior, state table reduction, The state assignment, Completing the design of clocked synchronous sequential networks.

LAB experiments: Verification of gates, implementation using basic gates and universal gates, Code conversion (Binary to gray, BCD to Excess 3), verify adders, subtractors, multiplexers, demultiplexers, comparators & code converter, verification of Flip-flops, counters, shift registers

TEXT BOOKS:

1. Digital logic and computer design- Morris Mano, Prentice Hall.
2. Digital Principles and Design- DonaldGivone, Tata McGraw Hill.

REFERENCE BOOKS:

1. Fundamental of Logic Design- Charles Roth Jr., Thomas Learning.
2. Digital Logic Applications and principles- John Yarbrough, Pearson Education



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

III SEMESTER ELECTRICAL CLUSTER

Subject	SIGNALS AND SYSTEMS (EE/EC/IT/ML)	Sub.Code	11ES3GCSAS
Credits	04	L-T-P	4-0-0

COURSE OBJECTIVES

CO1: Define and classify various signals and systems.

CO2: Describe and examine the behaviour of a typical Linear Time Invariant system based on its mathematical model.

CO3: Sketch and relate the time and frequency domain response of an LTI system.

CO4: Evaluate the behavior of LTI system using Fourier transform and Z-transform.

Unit -1:

Introduction: Definitions of a signal and a system, classification of signals, basic Operations on signals, elementary signals, Systems viewed as Interconnections of operations, properties of systems. [10 hours]

UNIT -2:

TIME-DOMAIN REPRESENTATIONS FOR LTI SYSTEMS

Convolution, impulse response representation, Convolution Sum and Convolution Integral. Properties of impulse response representation, Differential and difference equation Representations, Block diagram representations. [12 hours]

UNIT-3:

FOURIER REPRESENTATION FOR SIGNALS - 1 : Introduction, Discrete time and continuous time Fourier series (derivation of series excluded) and their properties .

FOURIER REPRESENTATION FOR SIGNALS 2: Discrete and continuous Fourier transforms(derivations of transforms are excluded) and their properties.

[12 hours]



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT-4:

APPLICATIONS OF FOURIER REPRESENTATIONS: Introduction, Frequency response of LTI systems, Fourier transform representation of periodic signals, Fourier transform representation of discrete time signals. [8 hours]

UNIT-5:

Z-TRANSFORMS 1: Introduction, Z transform, properties of ROC, properties of Z transforms, inversion of Z transforms.

Z-TRANSFORMS 2: Transform analysis of LTI Systems, unilateral Z- Transform and its application to solve difference equations. [10 hours]

Text Books:

Signals and Systems-Simon Haykin and Barry Van Veen, John Wiley & Sons, 2nd edition

Reference Books:

1. Alan V Oppenheim, Alan S, Willsky and A Hamid Nawab, "Signals and Systems" Pearson Education Asia / PHI, 2nd edition, 1997. Indian Reprint 2002
2. Signals and systems: ANALYSIS OF SIGNALS THROUGH LINEAR SYSTEMS-Michel j Roberts, TMH, 2003
3. Ganesh rao and Sathishtunga. "signals and systems", sanguine technical publishers, 2004
4. H. P Hsu, R. Ranjan, "Signals and Systems", Scham's outlines, TMH, 2006
5. B. P. Lathi, "Linear Systems and Signals", Oxford University Press, 2005



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

III SEMESTER ELECTRICAL CLUSTER

Subject	MEASUREMENT TECHNIQUES	Sub.Code	10ES3GCMST
Credit	03	L-T-P	3-0-0

COURSE OBJECTIVES

CO1: Able to distinguish & understand the concepts of analog & digital Instruments for the measurement of electrical quantity.

CO2: To identify and select the suitable bridge for the measurement of electrical parameters.

CO3: To identify the operation for measuring the electrical and non-electrical quantities using transducers, display devices & graphical recorders.

UNIT I [07 hours]

Fundamentals of Measurement

Introduction, Static Characteristics, Dynamic Characteristics, Errors in measurement, Types of errors, Sources of error.

Electrical Measuring Instruments: Types of Instruments, Principle of Operation, Constructional features of PMMC instrument, shunts & multipliers, universal shunt, multi range voltmeters

UNIT II [8 hours]

Electronic Measuring Instruments:

Need for electronic measuring instruments, True RMS Responding voltmeter, digital voltmeters-Ramp type, integrating type, successive approximation type, q meter, digital multimeter -Block diagram description

UNIT III [8 hours]

Measurement of Resistance, inductance and capacitance:

Wheatstone bridge-sensitivity analysis, limitations, kelvin's double bridge, Maxwells Bridge, Schering Bridge, source and detectors, minimization of AC bridge errors, problems.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT IV

[8 hours]

TRANSDUCER-1

classification and selection ,principle of operation of thermocouples, Resistance temperature detectors, thermistors, LVDT, capacitive transducers, piezoelectric transducers.

UNIT V [8 hours]

Transducer-II

Strain gauges-types, expression for gauge factor, photosensitive devices, display devices and recorders: method of measuring amplitude, phase, frequency and period using cro. use of lissajous patterns. LCD and led display, strip chart and x-y records. Introduction to printers.

TEXT BOOKS:

1. Electronic Instrumentation and Measurements, David a Bell, phi/ pearson Education, 2006.
2. Electronic Instrumentation, h. skalsi, TMH,2004

Reference books:

1. electronics and electrical Measurements, A.ksawhney,dhanpatrai and sons,9th edition



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

IV SEMESTER ELECTRICAL CLUSTER

Subject	ENGINEERING MATHEMATICS – IV	Sub.Code	1MA41CMAT
Credits	04	L-T-P	3-1-0

COURSE OBJECTIVES

-) CO1: Estimate the relation between two variables and perform regression analysis.
-) CO2: Apply the basic principles of probability and probability distributions.
-) CO3: Calculate solutions of algebraic and transcendental equations, ordinary differential equations numerically.
-) CO4: Construct analytic functions and evaluate real and complex integrals.
-) CO5: Discuss series solution of ordinary differential equations.

UNIT-1

STATISTICS

[10 hours]

Curve fitting – Fitting a straight line, fitting of a parabola, fitting of curves of the form $y = a + bx$, $y = ax^2 + bx + c$, $y = a e^{bx}$, $y = a \log x$, $y = a/x$. Correlation and regression. (4L+1T)

PROBABILITY 1

Probability of an event, axiomatic definition, addition theorem, conditional probability, multiplication theorem, Bayes' theorem. (4L+1T)

UNIT-2

PROBABILITY 2

[10 hours]

Probability distributions: Random variables, Discrete probability distributions, continuous probability distributions, Some standard distributions: Binomial distribution, Poisson distribution, exponential distribution, normal distribution. (8L+2T)



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT-3

COMPLEX ANALYSIS 1 [10 hours]

Function of a complex variable, Analytic functions, Cauchy - Riemann equations, construction of analytic functions, Cauchy - Riemann equations in Polar form.

Transformations $w = \frac{az + b}{cz + d}$ and $w = \frac{a^2}{z}$ Bilinear transformations.
(8L+2T)

UNIT-4

COMPLEX ANALYSIS 2 [12 hours]

Complex integration-Cauchy's theorem, Cauchy's integral formula, Taylor's and Laurent's series, Singular points, poles, residues, the residue theorem. (5L+2T)

SERIES SOLUTION OF DIFFERENTIAL EQUATIONS

Series solution-Frobenius method, series solution of Bessel's differential equation leading to Bessel function of first kind, equations reducible to Bessel's differential equation, series solution of Legendre's differential equation leading to Legendre polynomials, Rodrigue's formula. (4L+1T)

UNIT-5

PROBABILITY 3 [10 hours]

Joint Probability distributions: Case of discrete random variables, mathematical expectation, correlation, covariance.

Markov Chain: Probability vectors, stochastic matrices, fixed points, regular stochastic matrices. Markov chains, higher transition probabilities, stationary distribution of regular Markov chains and absorbing states. (7L+3T)

Text Books

1. Advanced Engineering Mathematics, Erwin Kreyszig, 8th edition, 2007, Wiley-India
2. Higher Engineering Mathematics, B.S. Grewal, 40th edition, 2007, Khanna Publishers.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

Reference Books:

1. Advanced Modern Engineering Mathematics, Glyn James, 3rd edition, 2004, Pearson Education.
2. Higher Engineering Mathematics, B.V. Ramana, 2007, Tata Mc. Graw Hill.
3. Advanced Engineering Mathematics, P. V. O' Neil, 5th Indian reprint, 2009, Cengage learning India Pvt. Ltd.

Question Paper Pattern

1. Each unit consists of one full question.
2. Each full question consists of three or four subdivisions.
3. Five full questions to be answered.
4. Internal Choice in Unit 2 and Unit 4



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

IV SEMESTER ELECTRICAL CLUSTER

Subject	OP-AMPS & LINEAR ICS	Sub.Code	11ES4GCLIC
Credits	05	L-T-P	4-0-1

COURSE OBJECTIVES

-) CO1: Identify basic building blocks, characteristic of op-amp and design basic amplifier circuits.]
- |) CO2: Discuss performance and design of linear and non-linear circuits using op-amp.]
- |) CO3: Design and develop analog circuits for regulation and timing using Op-amps and other linear IC's.]
- |) CO4: Describe and select A/D and D/A converter.]

UNIT I [10 hours]

a. Circuit configurations for linear ICs: Introduction, Current sources- current mirror, basic current source circuit

Differential amplifiers: Differential amplifiers using BJT(DC analysis only), CMRR, Input impedance R_i , output impedance R_o

b. Operational amplifier characteristics : Introduction, ideal op-amp, practical op-amp

IC 741 bi-polar opamp-bias circuit, input stage, gain stage, output stage, DC performance characteristics of opamp, AC performance characteristics of opamp, slew rate, noise, open-loop configurations, closed loop op-amp configurations, differential amplifiers, general description, power supply connections

UNIT II [10 hours]

a. Applications of op-amps: Introduction, sign changer, scale changer, phase shift circuits, voltage followers, voltage controlled voltage source, current sources, inverting current amplifier, current controlled current source, V-to-I , I-to-V converters, adder, subtractor, adder-subtractor, instrumentation amplifier, integrator, differentiator



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

b. Op-amp non-linear circuits: Introduction, opamp comparator, zero-cross detector, Schmitt trigger, Precision rectifiers, peak Detectors, sample -and hold circuit, clippers, clampers

UNIT III [12 hours]

- a. Active Filters: Introduction, comparison between passive and active network design, Design of low pass filters, high pass filters, all-pass filters
- b. Waveform generators: Introduction, sine-wave generators- Weinbridge, RC phase shift, Multivibrators- astable, monostable, triangular waveform generators

UNIT IV [10 hours]

- a. 555 timer: General description of 555, Monostable operation, Astable Operation,
- b. Voltage regulators: Introduction, Basics of voltage regulator, linear voltage regulator using op amp- single polarity linear voltage regulator, IC voltage Regulators, IC723 General purpose Regulator. SMPS Introduction only

UNIT V [10 hours]

A/D and D/A convertors: Introduction, Analog and digital data conversions, specifications of DAC, basic D/A conversion techniques- weighted resistor DAC, R-2R DAC, A/D converters, specifications of ADC, classification of ADC, different types of ADC

LAB Experiments :

Inverting amplifier, non-inverting amplifier, summing amplifier and voltage follower, precision half wave, and full wave rectifier, clipping circuits, clamping circuits, differentiator and integrator, Schmitt trigger and zero crossing detector, Wien bridge oscillator, first order low-pass and high pass filter, IC 723 low voltage and high voltage regulator, A/D and D/A converters.

TEXT BOOKS:

Linear Integrated circuits - S. Salivahanan, V S KanchanaBhaaskaran, TMH ,



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

REFERENCE BOOK:

1. Op-Amps and Linear Integrated Circuits-Ramakanth A.Gayakwad,4thed,PHI.
2. Linear Integrated circuits; D.Roy Choudhury and Shail B.Jain,2nd ed,
Reprint 2006, New Age International
3. Op-Amps and Linear ICs: David A Bell, 2nd Edition, PHI

IV SEMESTER ELECTRICAL CLUSTER

Subject	MICROCONTROLLERS	Sub.Code	11ES4GCMCS
Credits	04	L-T-P	3-0-1

COURSE OBJECTIVES

- |) CO1: Ability to design, build, and debug simple microcontroller based systems by applying suitable algorithms.|
- |) CO2: Ability to use a development environment that includes simulators, debuggers, cross compilers etc. in the development of control applications.|
- |) CO3: Ability to develop applications of microcontrollers for serial communication, data acquisition system, etc.|

UNIT I

[08 hours]

INTRODUCTION TO MICRO CONTROLLERS: Microprocessors and micro controller, Introduction, Difference between Microprocessors and Micro controllers, RISC & CISC CPU Architectures, Harvard & Von-Neumann CPU architecture, Embedded Electronic Systems and Micro controllers, comparison of Different micro controllers and applications.

The 8051 Architecture: Introduction, 8051 Micro controller Hardware, Input / Output Pins, Ports and Circuits, External Memory.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT II [08 hours]

ASSEMBLY LANGUAGE PROGRAMMING IN 8051: Addressing Modes and Instruction set: Introduction, Addressing modes, Data transfer instructions, Example Problems, Arithmetic instructions, Logical instructions, Example Problems, JUMP and CALL Program range, Jumps, calls and Subroutines, Returns, Example Problems.

UNIT III [08 hours]

EMBEDDED 'C' PROGRAMMING: 8051 programming in C: Data types and time delays in 8051 C, I/O programming, logic operations, data conversion programs, accessing code ROM space, data serialization.

Timer / Counter Programming in 8051: Counters and timers programming 8051 Timers, Counter Programming, programming timers 0 and 1 in 8051 C.

UNIT IV [08 hours]

8051 Serial Communication: Basics of Serial Communication- Serial data input/output, 8051 connections to RS-232, 8051 Serial communication Programming, Interrupts Programming:, 8051 Interrupts, Programming Timer Interrupts, Programming External Hardware Interrupts, Programming the Serial Communication Interrupts, Interrupt programming in C

UNIT V [07 hours]

8051 INTERFACING AND APPLICATIONS:

Interfacing 8051 to LCD, Keyboard, DAC, ADC Stepper motor interfacing.

LABORATORY EXPERIMENTS:

Part A: Data Transfer, Logical-Byte/Bit manipulations, Jump and Subroutine Calls using Assembly language, counters and delay generation using timers, Embedded C programs

Part B: Interfacing: LCD Display, Stepper motor control using interrupt, Elevator interface and & 7 segment interface, DAC, keyboard.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

The Experiments will be implemented using 'Keil' software with Embedded IDE. For interface, 8051 hardware kit is used.

TEXT BOOKS:

1. **“The 8051 Micro controller Architecture, Programming & Applications”**, Kenneth J. Ayala 2e, Thomson Learning 2005
2. **“The 8051 Micro controller and Embedded Systems using assembly and C ”**, Muhammad Ali Mazidi and Janice Gillespie Mazidi and Rollin D. McKinlay; PHI,2006

REFERENCE BOOKS:

1. **“Programming and Customizing the 8051 Micro controller”**, Predko ;, TMH year
2. **“Micro controllers: Architecture, Programming, Interfacing and System Design”**, Raj Kamal, Pearson Education, 2005
3. **“PIC Micro controllers”**, J.B. Peatman;” PHI, 2006



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

IV SEMESTER ELECTRICAL CLUSTER

Subject	FIELD THEORY	Sub.Code	11ES4GCFTH
Credits	04	L-T-P	4-0-0

COURSE OBJECTIVES

- |) CO1: Apply the knowledge of electrostatic fields|
- |) CO2: Apply the knowledge of Magneto static fields.|
- |) CO3: Combine the knowledge of electromagnet statics for time varying fields.|

UNIT I [10 hours]

Coulomb's Law, Electric Field Intensity (EFI): Experimental Law, EFI, due to Line Charge, Surface and Volume Charge

Electric Flux Density (EFD), Gauss' Law, Divergence: Electric Flux Density (EFD), Gauss' Law, Application, Divergence and Divergence Theorem.

UNIT II [10 hours]

Energy and Potential: Energy spent in moving charge, Definition of Potential Difference (PD), PD due to Point Charge and System of Charge, Energy Density

Current and current density: Current and Current Density, Continuity Equation
Current, Conductor, Properties, and Boundary Conditions

UNIT III [10 hours]

Dielectric and capacitance: Dielectric materials, boundary conditions, capacitance of different configurations

Poisson's and Laplace's equations: Derivations of Poisson's and Laplace's Equations, solution of Poisson's and Laplace for Single Variables

UNIT IV [10 hours]

Steady Magnetic Field: Biot - Savart Law, Ampere's circuital law, curl, Magnetic Flux, Flux Density, Scalar and Vector Magnetic Potentials



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT V [12 hours]

Magnetic forces and Inductance: Force on a moving charge, Force on different current element, Magnetic Boundary Condition, Inductance and Mutual Inductance

Time varying fields and Maxwell's equations: Faraday's Law, Displacement Current, Maxwell's Equations in Point and Integral Form, Uniform plane waves, Wave equations, solution of wave equation, wave propagation through good dielectric, good conductor, skin depth, Poynting Theorem

TEXT BOOK:

1. **Engineering Electromagnetics**, William J Hayt Jr. and John A Buck, Tata McGraw-Hill, 7th Edition, 2006

REFERENCE BOOK:

1. Electromagnetics with Applications, John Krauss and Daniel A Fleisch, McGraw-Hill, 5th Edition, 1999.
2. Field and wave electromagnetic, David K Chary, Pearson Education Asia, Second Edition 1989



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

IV SEMESTER ELECTRICAL CLUSTER

Subject	CONTROL SYSTEMS	Sub.Code	09ES4GCCST
Credits	04	L-T-P	4-0-0

COURSE OBJECTIVES

CO1: Apply the knowledge of fundamentals of mathematics and engineering sciences to differentiate between various system and their representations.

CO2: Determine behaviour of a control system based on its time domain and frequency domain responses.

CO3: illustrate and analyze the system stability using various techniques.

UNIT I [12 hours]

Introduction: Examples of Control Systems, open loop vs Closed loop Systems, Classifications of Control Systems.

Mathematical Modeling of Linear Systems: Transfer functions, Mechanical Systems, Analogous

Systems, Block diagram, Signal Flow graph (excluding gear trains lever)

UNIT II [10 hours]

Time response analysis of Control Systems :Step response of first order, second order systems, response specification , steady state error and error constants.

UNIT III [10 hours]

Stability Analysis: Concept of stability, RH criterion, applications of RH criterion with limitations, Nyquist plot, Polar plots, Stability Analysis using Nyquist criterion

UNIT IV [10 hours]

Root locus technique: Introduction to root locus concepts, Construction rules, Analysis of stability by root locus plot



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT V [10 hours]

Frequency response Analysis: Bode plots, Relative stability, Frequency domain specification.

TEXT BOOK:

Control Engineering by Nagrath & Gopal, New Age International Publishers

REFERENCE BOOKS:

- 1.Modern control Engineering- Ogata, Prentice Hall
- 2Automatic Control Systems- B.C Kuo, John Wiley and Sons

IV SEMESTER ELECTRICAL CLUSTER

FUNDAMENTALS OF HDL	Sub.Code		09ES4GCHDL
04	L-T-P		3-0-1

COURSE OBJECTIVES

CO1: Discuss the objective and role of HDL in digital system design Using latest tools like VHDL and Verilog.

CO2: Describe and test digital logic circuits in data flow description, structural description, behavioral description and advanced constructs (procedures, tasks, functions) using both VHDL and Verilog.

CO3: Apply the overall knowledge of digital circuit design using HDL for gate level synthesis targeted for FPGA /CPLD as preparation for project work.

UNIT I

[07 hours]

Introduction: Why HDL? , A Brief History of HDL, Structure of HDL Module, Operators, Data types, Types of Descriptions, simulation and synthesis, Brief comparison of VHDL and Verilog



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

V SEMESTER ELECTRICAL CLUSTER

Subject	TRANSDUCERS	Sub.Code	10IT5DCTDR
Credits	05	L-T-P	4-0-1

COURSE OBJECTIVES

CO1: Describe the concepts of input-o/p configuration & methods of correcting them.

CO2: To find Qv of different flow meters and analyse flow rates.

CO3: Choose appropriate transducers and demonstrate the measurement of temperature, pressure, flow and displacement.

UNIT I

[04 hours]

PERFORMANCE CHARACTERISTICS OF MEASURING INSTRUMENTS:

Functional elements of an instrument, calibration and standards, I/O configuration of measuring instruments & instrument system-methods of correction for interfering & modifying inputs.

UNIT II

[12hours]

FLOW MEASUREMENT: Local flow velocity, magnitude and direction, Flow visualization, Velocity magnitude from pitot static tube, Hot wire and hot film anemometer, Laser Doppler velocimeter; gross volume flow rate: calibration and standards, Constant area, variable-pressure drop meters (obstruction meters), Averaging pitot tubes, constant pressure drop, variable area meters (rotameters), turbine meters, positive displacement meters, Electromagnetic flow meters, Drag force flow meters, Ultrasonic flow meters

UNIT III [12 hours]

TEMPERATURE MEASUREMENT: Standards & calibration; thermal expansion methods

bimetallic thermometers, liquid-in-glass thermometers, pressure thermometers; thermoelectric sensor (thermocouple) - common thermocouple, reference junction considerations, special materials, configuration & techniques; electrical resistance sensors conductive (resistance sensor thermometers), bulk semiconductor sensors (Thermistors); junction semiconductor sensors; digital thermometers



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT II [08 hours]

Data-Flow Descriptions: Highlights of Data-Flow Descriptions, Structure of Data-Flow Description, Data Type Vectors. Behavioral Descriptions: Behavioral Description highlights, structure of HDL behavioral Description, The VHDL variable Assignment Statement, sequential statements.

UNIT III [08 hours]

Structural Descriptions: Highlights of structural Description, Organization of the structural Descriptions, Binding, state Machines, Generate, Generic, and Parameter statements.

UNIT IV [08 hours]

Procedures and Functions: Procedures, Tasks, and Functions: Highlights of Procedures, tasks, and Functions, Procedures and tasks, Functions. Advanced HDL Descriptions: File Processing, Examples of File Processing.

UNIT V [08 hours]

Synthesis Basics: Highlights of Synthesis, Synthesis information from Entity and Module, Mapping Process and Always in the Hardware Domain.

LAB Experiments

Combinational logic circuits, sequential circuits using data flow (simulation and implementing using FPGA/CPLD) sequential descriptions & structural descriptions. Interfacing experiments : stepper motor, dc motor, relay, waveform generation.

TEXT BOOK:

HDL Programming (VHDL and Verilog) -Nazeih M.Botros- Dreamtech Press (Available through John Wiley India and Thomson Learning), 2006 Edition

REFERENCE BOOKS:

1. Verilog HDL Samir Palnitkar, Pearson Education
2. VHDL Douglas Perry, TMH
3. Fundamentals of Digital Logic with Verilog Design-Stephen Brown, TMH
4. Circuit Design with VHDL- Volnei A.Pedroni, PHI



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT IV [12 hours]

PRESSURE MEASUREMENT: Standards & calibration; basic methods of pressure measurement; dead weight gauges & manometer, manometer dynamics; elastic Transducers; high pressure measurement; low pressure (vacuum) measurement-McLeod gage, Knudsen gage.

UNIT V [12 hours]

MEASUREMENT OF DISPLACEMENT AND FORCE: Principle of measurement of displacement Resistive potentiometers, variable inductance & variable reluctance pickups, Principle of measurement of Force, basic methods of force measurement-load cell; characteristics of elastic force Transducer-Bonded and unbonded strain gauge.

LAB EXPERIMENTS

Characteristics of potentiometric transducer, Characteristics of LVDT, Characteristics of capacitive transducer: variable area type, variable distance type, Characteristics of Thermistors, RTD, AD590, thermocouple, Characteristics of LDR, Photo Diode & photo transistor: Variable illumination, variable distance, Wheatstone bridge-measurement of bridge sensitivity, Measurement of low resistance using Kelvin double bridge., Measurement of self-inductance using Maxwell bridge and Anderson's bridge, Measurement of unknown capacitance using Desauty's bridge and Schering's bridge, Calibration of voltmeter and ammeter using DC potentiometer, Characteristics of pressure transducer, Characteristics of load cell & cantilever using strain gauge (quarter, half and full bridge)

Text book

1. "Measurement system application and design", ERNEST O DOEBEL IN, IV edition.
2. "Transducers and Instrumentation", D V S Murthy, II edition by PHI

REFERENCE BOOK:

"Instrument Engineers Handbook (process measurement)", BGLIPTAK.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

Subject	Digital Signal Processing	Sub.Code	11ES5DCDSP
Credits	05	L-T-P	4-0-1

COURSE OBJECTIVES

CO1: Demonstrate sampling, transformations by time and frequency domain representation of different signals with the fundamentals of Fourier series.

CO2: Choose the appropriate data transformation techniques to represent the discrete signals.

CO3: Analyze the computational complexities involved in implementation of data transformation operations to develop algorithms to reduce the operational complexities.

CO4: To shape and evaluate the performance of filters for selecting desired signal frequencies by using appropriate filtering techniques.

UNIT I

[11 hours]

Introduction to DSP, Sampling and reconstruction of a discrete time signal in the frequency domain. Definition of Discrete Fourier Transform (DFT). Useful properties of DFT: linearity, circular shift, Multiplication by a complex exponential sequence, Properties of even and odd parts of $x[n]$, Multiplication, Parseval's relation, Circular convolution in the time domain, use of tabular arrays and circular arrays.

UNIT II [10 hours]

Use of DFT in linear filtering, linear convolution of two finite duration sequences, overlap add and save methods. Relation between DFT and other transforms. Direct computation of DFT. Necessity for efficient computation of DFT. Radix 2 Fast Fourier Transform (FFT) algorithm for DFT computation. Decimation in time algorithm, decimation in frequency algorithms.

UNIT III

[10 hours]

Computation of $2N$ point DFT of a real sequence using single N point DFT. Computation of two N point DFT of a real sequence using N point DFT. Decomposition for 'N', a composite number,. Number of computations, number of multiplications, computational efficiency, Radix2FFTalgorithm for computation of Inverse Discrete Fourier Transform. (IDFT).



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT IV [11 hours]

Introduction to realization of digital systems, block diagrams, representation, Realization of Infinite Impulse Response (IIR) systems :direct form, parallel form, cascade form. Introduction to IIR filters, impulse invariant & bilinear transformations, Design of analog filters: Design of Digital filters: Butter worth and Chebyshev. Frequency transformations.

UNIT V [10 hours]

Realization of Finite Impulse Response (FIR) system :Direct Form, Linear Phase Form. Introduction to FIR filters, Frequency response of ideal digital low pass filter, high pass filter, frequency sampling technique of designing FIR filters, Windowing,. Design of FIR filters using rectangular, triangular, Hamming, Hanning and Black man window. Gibbs phenomenon (qualitative discussion only), comparison between IIR and FIR filters.

LAB EXPERIMENTS: Display of basic elementary signals, sampling theorem, basic operations on sequences (shifting, folding, time scaling and multiplication), linear and circular convolution, cross and auto correlation, linear convolution and correction using FFT algorithm, FFT of Sequence, FIR Filter design-LP,HP,BP and Notch filter, FIR filter design using Hamming and Kaiser window for the given order and cut-off frequency, Design of IIR FILTER-LP,HP (using both hardware and software).

TEXTBOOKS:

1. Digital Signal Processing, A computer based approach, Sanjit K Mitra, Tata McGrawHill, Third Edition,
2. Digital Signal Processing, Principles, Algorithms and Applications, JohnG. Proakis, Dimitris K Manolakis, Pearson education/PHI, (4thEdition)
3. digital signal processing by Udayshankar

REFERENCE BOOKS:

1. Fundamentals of Digital Signal Processing, Lonnie Ludeman, John Wiley & Sons; Wiley International 1st Edition, 1988.
2. Discrete-Time Signal Processing, Alan V. Oppenheim, Ronald W. Schaffer, John R. Buck, Prentice-Hall Signal Processing Series, 2nd Edition, 1999



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

3. Understanding Digital Signal Processing, Richard G. Lyons Prentice Hall, March 25, 2nd Edition 2004
4. Digital Signal Processing: Fundamentals and Applications, Li Tan, Academic Press, 1st edition 2007
5. Schaum's Outline of Digital Signal Processing, Monson Hayes, Mc Graw-Hill, 1st edition, 1998)

V SEMESTER ELECTRICAL CLUSTER

Subject	PROCESS CONTROL	Sub.Code	12IT5DCPCS
Credits	5	L-T-P	4-0-1

COURSE OBJECTIVES

JCO1: Understanding the basic principles of various manufacturing processes and Apply Engineering knowledge to do problem analysis in process control.

JCO2: Design and develop solutions for controlling several parameters in a process plant using op-amps and pneumatic controllers.

JCO3: Designing of various basic and advanced controller using modern tools and demonstration of moderately complex problems in process industry to meet societal needs.

JCO4: Understand, interpret and implement Tuning of the controllers using various methods and study about digital controllers and data loggers.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

CO5: Design requirements of optimization in chemical process control system which leads to team work and project management in a chemical process industry addressing the safety and environmental aspects.

UNIT I [10 hours]

INTRODUCTION TO PROCESS CONTROL:

Introduction, control systems, classifications, Block diagram, control systems evaluation, analog and digital processor, sensor time response, problems.

DIGITAL SIGNAL CONDITIONING:

Introduction, converters, data Acquisition systems, problems, algorithms.

UNIT II [10 hours]

ANALOG SIGNAL CONDITIONING:

Introduction to principles of analog signal conditioning, Op-amp circuits realization for various types of controllers, design principles, problems.

ANALOG CONTROLLERS:

Introduction, Electronic Controllers, pneumatic controllers, design Considerations.

UNIT III [10 hours]

CONTROLLER PRINCIPLES:

Introduction, process characteristics, control system parameters, Discontinuous controller modes, continuous controller modes, composite controller modes, problems.

UNIT IV [12 hours]

DIGITAL CONTROLLERS:

Introduction, digital electronic methods, computers in process controls, characteristics of digital data, controller software, computer controller examples and problems, p&i symbols.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

CONTROL LOOP CHARACTERISTICS :

Introduction, Control system configuration. Cascade control systems, Multi variable control systems, Control system quality, stability and process loop tuning.

UNIT V

[10hours]

INCENTIVES FOR CHEMICAL PROCESS CONTROL:

Suppress the influence of external disturbances, Ensure the stability of a process, Optimize the performance of chemical process

CONTROL SYSTEMS WITH MULTIPLE LOOPS: Cascade control, selective control system, split range control

Process Control Lab

Time response for a I order and II order system for standard input signals, Calibration Design to display the temperature using RTD with suitable signal conditioning circuit. Design and display the temperature using Thermocouple with suitable signal conditioning circuit, Design and test the circuit to display the temperature using Ad590 with suitable signal conditioning circuit. Design and test the circuit to display the load using load cell with suitable signal conditioning circuits, P,P I and PID controllers to obtain the optimum response of the given temperature controller, P,PI and PID controllers to obtain the optimum response of the given flow/Level controller,

TEXT BOOKS:

1. "Process Control Instrumentation", JOHNSONDCURTIS, 7th edition, Prentice Hall Of India Publications.
2. "Process Measurement", BELAG. LIPTAK, Instrument Engineers Hand Book, Volume1, Chilton Book Company/ Radnor, 3rd edition,1969.

REFERENCE BOOKS:

1. "Instrument Engineers Hand Book, Process Control", BELA G. LIPTAK, Volume 2, Chilton Book Company/Radnor, 3rd edition,1969.
2. "Computer Aided Process Control", S.K. SINGH, 2nd print, Prentice Hall Of India Publications, 2004.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

V SEMESTER ELECTRICAL CLUSTER

Subject	ANALYTICAL INSTRUMENTATION	Sub.Code	10IT5DCANI
Credits	03	L-T-P	3-0-0

COURSE OBJECTIVES

JCO1: Identify types of instrumental analysis methods used in industrial applications.

JCO2: Describe various spectroscopic principles and techniques.

JCO3: Analyze the applicability of methods used in various spectroscopic techniques.

UNIT I [12 hours]

INTRODUCTION:

Types of instrumental methods for analysis, electromagnetic spectrum, properties of electromagnetic radiation and its interaction with matter, emission of radiation.

MOLECULAR SPECTROSCOPY: Measurement of transmittance and absorbance, Beer Lambert's law, instrumentation, single and double beam spectrometers, application of UV-Visible spectroscopy for qualitative and quantitative analysis. IR absorption spectrometry, IR instruments, application for quantitative analysis.

UNIT II

[08 hours]

ATOMIC ABSORPTION SPECTROSCOPY:

Principles, sample atomization techniques, atomic absorption instrumentation, interferences in atomic spectroscopy, standard addition and internal standard methods of evaluation.

UNIT III

[08 hours]

ATOMIC EMISSION SPECTROSCOPY:

Principles, arc, spark and plasma sources, emission based on plasma sources, emission spectroscopy based on arc and spark sources, applications, atomic fluorescence spectroscopy and comparison.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT IV [07 hours]

X-RAY SPECTROSCOPY:

Fundamentals, instrumentation, X-ray absorption methods, X-ray fluorescence methods, X-ray diffraction, applications.

UNIT V [07 hours]

MASS SPECTROSCOPY:

Features of mass spectroscopy, ion sources, sample inlet systems, mass analyzers - single beam, double beam and quadrupole instruments, applications.

TEXT BOOK:

"Principles of Instrumental Analysis-6th Edition", Douglas A. Skoog, James Holler, Stanley R. Crouch. Thomson Learning

REFERENCE BOOKS:

1. "Instrumental Methods of Analysis", Willard H.W Merritt, L.L Dean J A Settle FA, 7th Edition, CBS Publishers.
2. "Fundamentals of Analytical Chemistry", Douglas A Skoog, Donald M west Holler Thomson Lrning. "Instrumental Methods of Chemical Analysis", Galen W. Ewing, Mc GrawHill



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

V SEMESTER ELECTRICAL CLUSTER

Subject	COMMUNICATION SYSTEMS	Sub.Code	10IT5DCCST
Credits	04	L-T-P	4-0-0

COURSE OBJECTIVES

CO1: Describe the need for different analog modulation schemes.

CO2: Discuss the effect of noise in analog modulation scheme.

CO3: Explain sampling, quantization, coding and digital modulation schemes.

UNIT I [12 hours]

AMPLITUDE MODULATION:

Time-Domain Description, Frequency domain description, Generation of AM waves, Detection of AM waves, AM/DSB, Time-Domain Description, Frequency domain description Generation of DSBSC waves, Coherent Detection of DSBSC Modulated waves. Costas loop, Quadrature Carrier multiplexing, AM-SSB/SC generation, Frequency-Domain Description, Frequency discrimination method for generation an SSB Modulated wave, time do main description, phase discrimination method for generating an SSB modulated wave, Demodulation of SSB waves, Comparison of amplitude modulation te chniques, frequency translation, FDM.

UNIT II [08 hours]

ANGLE MODULATION:

Basic Concepts, Frequency Modulation, Spectrum Analysis Of sinusoidal FM wave, NBFM, WBFM, Constant Average power, Transmission band width of FM waves, Generation of FM waves, Direct FM, demodulation of FM waves, frequency discriminator, ZCD, phase locked loop(1storder) comparison of AM and FM

UNIT III [08 hours]

NOISE IN ANALOG MODULATION SYSTEMS:

Signal-to-noiseratios,AMreceivermodel,Signal-to-noise ratios for coherent reception, DSBSC receiver, SSB receiver, noise in AM receivers using envelope detection, threshold



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

effect, FM receiver model, noise in FM reception, FM threshold effect, pre-emphasis and de-emphasis in FM systems

UNIT IV

[10 hours]

PULSE MODULATION:

Sampling theorem for low-pass and band-pass signal, statement and proof, PAM, Channel bandwidth for a PAM signal, natural sampling, flat-top sampling, quantization of signals, quantization error, PCM, electrical representations of binary digits, PCM systems, DPCM, delta Modulation, Adaptive delta modulation.

UNIT V [14 hours]

DIGITAL MODULATION:

Introduction, Binary Shift Keying, DPSK, QPSK, Type D flip-flop, QPSK transmitter, non-offset QPSK, QPSK receiver, signal-space representation, BFSK, spectrum, receiver for BFSK, geometrical representation of orthogonal BFSK, line codes, TDM.

TEXT BOOKS:

1. "Analog and Digital communication", Simon Haykin, John Wiley.
2. "Principles of communication systems", Tauband Schilling, Tata Mc Graw Hill.

REFERENCE BOOKS:

1. "Electronic Communication Systems", 2nd Edition, Blake, Thomson publishers.
2. "Electronic Communication Systems", George Kennedy.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

VI SEMESTER ELECTRICAL CLUSTER

Subject	MODERN CONTROL THEORY	Sub.Code	12IT6DCMCT
Credits	04	L-T-P	4-0-0

COURSE OBJECTIVES

JCO1: Identify the benefits of state space analysis and z-transform as modern mathematical tools in understanding the behaviour of continuous and discrete control system.

JCO2: Distinguish types of non-linearity in control system and evaluate its behaviour and stability using describing function.

JCO3: Design and analysis of compensator circuits for satisfying given set of specifications of a system using bode plot and root locus technique.

UNIT I [10 hours]

Review Of Vectors, Matrix Analysis: Definitions, Determinants, Inversion of Matrices Vector and Vector Analysis, Eigen Vectors, Eigen Values, Quadratic Forms.

Nonlinear Control Systems and Analysis :Types of Nonlinear control Systems, Examples, Derivations of their Describing Functions, its analysis, Stability of Non-linear Control Systems - Problems

UNIT II [10 hours]

Discrete Time Systems and the Z-Transform Method :Introduction to discrete systems, Review of z-transforms,PulseTransfer functions, Examples, Stability analysis in the Z-plane- Problems

UNIT III [12 hours]

State Space Analysis of Control Systems :State Space representation of Systems, Time invariant State equations, Examples, State Equations, State Transition matrix, State Transfer matrix, Linear time Variant Systems, State space representation of discrete time systems, Solution of State equations, Problems, Controllability and observability for Continuous control systems, Controllability and observability for discrete control systems



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT IV [10 hours]

Compensator Techniques using root locus technique: Lead, Lag, Lead-Lag Networks problems, Root Locus Techniques, Compensator design using Root Locus Techniques, Problems

UNIT V [10 hours]

Compensator Techniques using Bode plot :Lead, Lag, Lead-Lag Networks problems, Bode plot Techniques, Compensator design using bode plot, Problems

TEXT BOOKS:

1. Modern Control Engineering- K.Ogatta, 3rd edition, PHI, 1996.
2. Discrete Time Control Systems- K.Ogatta, 2nd edition, PHI, 1996.
3. Advanced control Theory - ANagar Kani- 2nd Edition, RBA Publication.

REFERENCE BOOKS:

1. Digital Control and State Variables methods- Madan Gopal, 2nd Edition PHI, 1997.
2. Control Systems- I.J.Nagaratnam and M.Gopal, 5th edition, PHI, 2005.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

VI SEMESTER ELECTRICAL CLUSTER

Subject	Data Acquisition & Virtual Instrumentation	Sub.Code	10IT6DCDVI
Credits	04	L-T-P	3-0-1

COURSE OBJECTIVES

-) CO1: Apply structured programming concepts in developing LabVIEW programs and employ various debugging techniques. |
-) CO2: Build applications that use general purpose interface bus or serial Instruments. |
-) CO3: Create applications that use plug in DAQ boards and use build in analysis functions to process your data. |
-) CO4: Use LabVIEW to create various instrumentation applications. |

UNIT I [08 hours]

REVIEW OF DIGITAL INSTRUMENTATION:

Representation of analog signals in the digital domain - Review of quantization in amplifier and time areas, sample and hold, sampling theorem, ADC and DAC.

UNIT II [10hours]

GRAPHICAL PROGRAMMING ENVIRONMENT IN VI:

Concepts of graphical programming-Lab-view software-Concept of VIs and sub VIs-Display types-Digital- Analog- Chart-Oscilloscope types-Loops-Case and sequence structures-Types of data- Arrays-Formulate nodes-Local and Global variables-String and file I/O.

UNIT III [08 hours]

FUNDAMENTALS OF VIRTUAL INSTRUMENTATION:

Concept of Virtual Instrumentation-PC based data acquisition - Typical on board DAQ card-Resolution and sampling frequency - Multiplexing of analog inputs-Single-ended and differential inputs-Different strategies for sampling of multi channel analog inputs.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

Concept of universal DAQ card - Use of timer- counter and analog outputs on the universal DAQ card.

UNIT IV [10 hours]

CLUSTER OF INSTRUMENTS IN SYSTEM: Interfacing of external instruments to a PC RS232C,RS-422,RS485 and USB standards - IEEE 488 standard - ISO - OSI model for series bus - introduction to bus protocols of MOD bus and CAN bus.

UNIT V [08 hours]

ANALYSIS TOOLS AND SIMPLE APPLICATION IN VI: Fourier transform-Power spectrum-Correlation-Windowing and filtering tools-Simple temperature indicator-ON/OFF controller - PID controller - CRO emulation - Simulation of a simple second order system-Generation of HTML page.

Virtual Instrumentation Lab.

The experiments are implemented using Lab VIEW Software

Designing temperature controller, Load/Strain measurement, pressure measurement and control, II order system, Piezoelectric transducer interface.

TEXTBOOKS:

- 1."PC Interfacing for Data Acquisition and Process Control",S.Gupta and J P Gupta Instrument Society of America, 1994
2. "Understanding Serial Communication",Peter W Gofton, Sybes International, 2000
3. "Learning with Lab-View"RobertH.Bishop, Prentice Hall, 2009
4. "Virtual Instrumentation, LAB VIEW",SanjayGupta, TMH,NewDelhi,2003
5. "Measurement Systems-ApplicationandDesign", Ernest O. Doebelin and Dhanesh N Manik, 5th Edn,TMH,2007.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

VI SEMESTER ELECTRICAL CLUSTER

Subject	BIO MEDICAL INSTRUMENTATION	Sub. Code	12IT6DCBMI
Credits	05	L-T-P	4-0-1

COURSE OBJECTIVES

CO1: Discuss basic performance requirements of medical instruments.

CO2: Describe the various Bio-Signal acquisition systems and relate to physiological Parameters.

CO3: Discuss the techniques for blood pressure measurement.

CO4: Understand types of pace makers and defibrillators.

CO5: Understand safety measures in a medical instrument design and signal transmission in bio telemetry.

UNIT I [12 hours]

Fundamental Concepts:

Sources of biomedical signals, Basic medical instrumentation system, performance requirements of medical instrumentation systems, PC based medical instruments, General constraints in design of medical instrumentation systems. Bioelectric Signals and Electrodes: Electrocardiogram (ECG), Electroencephalogram (EEG), Electromyogram (EMG), Electrooculogram (EOG), Electroretinogram (ERG), Recording Electrodes Electrode-tissue interface, polarization, skin contact impedance, motion artifacts, Silver-Silver Chloride electrodes, Electrodes for ECG, Electrodes for EEG, Electrodes of EMG, Electrical conductivity of electrode jellies and creams, microelectrodes.

UNIT II

[12 hours]

Recorders:

Electrocardiogram: Review of Heart Structure & Function, Conduction System of the heart, Electrical activity of the heart, Genesis & characteristics of Electrocardiogram (ECG), Electrocardiogram (ECG), Characteristics of the normal ECG, Cardiac arrhythmias and their electrocardiographic interpretation- Abnormal sinus rhythms, Premature contractions, description of an Electrocardiograph, ECG lead system, ECG recorder.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

Electroencephalograph: Genesis of Electroencephalogram (EEG), Block diagram description of an Electroencephalograph, 10-20 electrode systems, and computerized analysis of EEG

UNIT III [12 hours]

BLOOD FLOW AND Cardiac Output Measurement: Measurement of blood pressure Direct & Indirect method measurement of systolic, diastolic blood pressure, Detection of Kortokoff sounds, Electromagnetic blood flow meters, Ultrasonic blood flow meters, NMR blood flow meters, Laser Doppler flow meters. Cardiac output measurement: Indicator dilution method, Dye dilution method, Thermal dilution techniques.

UNIT IV [08 hours]

Cardiac Pacemakers and Defibrillators: Need for cardiac pacemaker, External pacemaker, Implantable pacemaker, Types of pacemakers. External, Implantable Pacemakers-Types, Ventricular synchronous demand pacemaker, Programmable pacemaker, Rate-responsive pacemakers, Packaging, Power sources, Leads & electrodes and their problems. Defibrillators- Need, DC defibrillator, Electrodes, DC defibrillator with synchronizer, Automatic external defibrillator, Implantable defibrillator, Defibrillator analyzer.

UNIT V [08 hours]

Patient Safety: Electric shock hazards, Leakage currents, safety codes and analyzer. Biomedical Telemetry & Telemedicine: Wireless telemetry, single channel telemetry, multi-patient telemetry, implantable telemetry and telemedicine.

TEXT BOOKS:

1. "Hand book of Biomedical Instrumentation", by R. S. Khandpur, 2nd Edition, Tata McGraw Hill, 2003.
2. "Biomedical Instrumentation and Measurement", by Leslie Cromwell, Fred J Weibell and Erich A. Pfeiffer, 2nd Edition, Prentice-Hall India Pvt. Ltd., 2004.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

REFERENCE BOOKS

1. "Medical Instrumentation Application & Design", John G. Webster, 3rd Edition, John Wiley & Sons/Wiley Student Edition, 2001.
2. "Principals of applied Biomedical instrumentation", LESLEY CROMWELL & OTHERS, John Wiley and sons, 2nd edition.
3. "Introduction to Biomedical equipment technology", JOSEPH J CARR, JOHN M BROWN, Prentice hall of India, 4th Edition.

LIST OF EXPERIMENTS:

Design of Pacemaker, Design of Peripheral Pulse Heart Rate Alarm System, Design of Bio Telemetry (Double FM System), Electrical Isolation for Bio signal (Elec. Safe), Design of ECG Heart Rate Alarm System, Design of EMG Bio Feedback system, Design of Electronic Stethoscope, Study of ECG System, Study of EEG System, Study of EMG System.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

VI SEMESTER ELECTRICAL CLUSTER

Subject	DSP Algorithm & Architecture	Sub.Code	10IT6DCDSA
Credits	04	L-T-P	3-0- 1

COURSE OBJECTIVES

CO1: To apply knowledge of Mathematics and Engineering to implement signal processing algorithms on DSP processor.

CO2: Using Integrated Development Environment (IDE) to implement signal processing designs by downloading to target processor for executing signal processing applications.

CO3: To describe the architecture and programming model of TMS320C54xx processor.

CO4: To analyse (trace) the execution of assembly and embedded C codes for DSP processor and hence to choose them appropriately in DSP applications.

CO5: To identify a suitable DSP processor for an application by acquiring knowledge on continuously evolving fixed and floating point processors.

UNIT I

[08 hours]

INTRODUCTION TO DIGITAL SIGNAL PROCESSING:

Introduction, A Digital Signal-Processing System, Review of the Sampling Process, Discrete Time Sequences, Discrete Fourier Transform (DFT) and Fast Fourier Transform (FFT), Linear Time-Invariant Systems and Digital Filters, Decimation and Interpolation, Number formats for signals and coefficients in DSP systems, Dynamic range and precision, Basic Architectural Features,

UNIT II [08 hours]

ARCHITECTURES FOR PROGRAMMABLE DIGITAL SIGNAL-PROCESSORS: DSP Computational Building Blocks, Bus Architecture and Memory, Data Addressing Capabilities, Address Generation Unit, Programmability and Program Execution, Features for External Interfacing. Speed issues,



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT III [08 hours]

ADDRESSING MODES AND INSTRUCTION SET

Data Addressing Modes of TMS320C54xx., Detail Study of TMS320C54X & 54xx Instructions and Programming, Memory Space of TMS320C54xx Processors, Program Control.

UNIT IV [08 hours]

Assembler directives, On-Chip peripherals, Interrupts of TMS320C54XX Processors, Pipeline Operation of TMS320C54xx Processor. Implementation of basic DSP algorithms- Introduction, The Q-notation, FIR Filters, IIR Filters, examples, interpolation and decimation filters, examples

UNIT V [07 hours]

An FFT Algorithm for DFT Computation, Overflow and Scaling, Bit-Reversed Index Generation & Implementation on the TMS320C54xx, Case Study- TMS320C6713 (TI), Case study- ADSP SHARC Processor (Analog Devices), interfacing memory and parallel I/O peripherals to programmable DSP devices.

LAB EXPERIMENTS

The laboratory experiments are implemented using Code Composer Studio and DSP Processor

Linear convolution of two given sequences, Circular convolution of two given sequences, Computation of N-Point DFT of a given sequence, Realization of an FIR filter (any type) to meet given specifications (The input can be a signal from function generator / speech signal, Audio applications such as to plot time and frequency & display of Microphone using DSP, Read a wave file and match with their respective spectrograms, Noise: Add noise above 3KHz and then remove; Interference suppression using 400 Hz tone, Impulse response of first order and second order system, Assembly Language Programming.

TEXT BOOK:

Digital Signal Processing - Avatar Singh and S. Srinivasan, Thomson Learning, 2004.

REFERENCE BOOKS:

1. Texas Instruments Reference manual
2. Digital Signal Processing, Shaila D Apte, Wiley India, 2009.
3. Digital Signal Processors - B Venkataramani and M Bhaskar TMH, 2002.
4. Architectures for Digital Signal Processing - Peter Pirsch John Wiley, 2007.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

VII SEMESTER ELECTRICAL CLUSTER

Subject	VLSI DESIGN	Sub. Code	121T7DCVLI
Credits	05	L-T-P	4-0-1

COURSE OBJECTIVES

- |) CO1: Describe the fabrication process and analyse the basic electrical properties of MOS circuits.|
- |) CO2: Design and implement CMOS circuits in silicon.|
- |) CO3: Explain analyse and design subsystems using structured approach for MOS technologies.|
- |) CO4: Discuss implementation of computational elements memories & register circuits.|

UNIT 1 : [12 hours]

INTRODUCTION AND BASIC ELECTRICAL PROPERTIES OF MOS TECHNOLOGY:

Moore's law, speed power performance, nMOS fabrication, CMOS fabrication: nwell, pwell processes, BiCMOS, comparison of bipolar & CMOS.

BASIC ELECTRICAL PROPERTIES OF MOS & BICMOS CIRCUITS:

Drain to source current versus voltage characteristics, threshold voltage, transconductance, nMOS inverter, Determination of pull up to pull down ratio, nMOS inverter driven through one or more pass transistors, alternative forms of pull-up, CMOS inverter, MOS transistor circuit model, latch up.

UNIT II [12 hours]

BASIC CIRCUIT CONCEPTS:

Sheet resistance, area capacitance calculation. Delay unit, inverter delay, estimation of CMOS inverter delay, driving of large capacitance loads, super buffers, BiCMOS drivers, propagation delays & wiring capacitances



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

MOS AND BICMOS CIRCUIT DESIGN PROCESSES:

MOS layers, stick diagrams, nMOS design style; CMOS design style, Design rules and layout, based design. Scaling of MOS circuits: Scaling factors for device parameters, limitations of scaling.

UNIT 3 [12 hours]

SUBSYSTEM DESIGN & LAYOUT:

Switch logic pass transistor, gate logic inverter, Nand gates, Nor gates, pseudo nMOS, dynamic CMOS example of structured design, parity generator, Bus arbitration, Multiplexers, logic function block, code converter. Clocked sequential circuits, dynamic shift registers, bus lines. Subsystem design processes General considerations, 4 bit arithmetic processor, 4-bit shifter.

UNIT 4 : [08 hours]

DESIGN PROCESS:

computational Elements: Regularity, design of ALU subsystem, ALU using adders, Carry look ahead adders, Multipliers, serial parallel multipliers, Braun array, Bough-wooley multiplier. Pipelined multiplier array, modified Booth's algorithm, Wallace tree multiplier.

UNIT 5 : [08 hours]

MEMORY, REGISTER & ASPECTS OF TIMING: 3 TRANSISTOR DYNAMIC RAM CELL, DYNAMIC MEMORY CELL, PSEUDO-static RAM, JK FF, D FF circuits, RAM arrays. Practical aspects and testability: Some thoughts of performance, optimization, and CAD tools for design & Simulation.

LAB EXPERIMENTS:

PART A: DIGITAL DESIGN: Write the Verilog code for the following circuits and observe the waveforms and synthesize the code with technological library with given constrains- An Inverter and Buffer, Transmission Gate, Basic/Universal Gates, Flip flops, Counters, Registers, Adders



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

PART B: ANALOG DESIGN: Design an inverter circuit by drawing the schematic and verify the following : i) Transient Analysis ii) DC Analysis, Design the following circuits with given specification, draw the schematic and verify the following i) Transient Analysis ii) DC Analysis iii) AC Analysis, Common Source Amplifier, Common Drain Amplifier, Differential Amplifier

TEXT BOOK:

1. " Basic VLSI design", 3rd Edition Douglas APucknell, KamaranEshraghian, Prentice Hall of India publication, 2005.

REFERENCE BOOKS:

1. " Introduction to VLSI Circuits and systems", JOHN P. UYEMURA, John Wiley publication, 2001.
2. " CMOS Digital Integrated Circuits, Analysis and design", 3rd Edition, Sung-Mo (steve) Kang, Yusuf Leblbici, Tata Mcgraw Hill.
3. " VLSI Technology", 2nd Edition, S.M .Sze, Tata Mcgraw Hill.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

VII SEMESTER ELECTRICAL CLUSTER

Subject	Automation in Process Control	Sub. Code	12IT7DCAPC
Credits	04	L-T-P	3-0-1

Course Outcomes

CO1: Memories the basic concepts of use of computers in process control through data loggers, DAS, SCADA, DDC and DCS.

CO2: Understanding and implementation of ladder programming for various applications using PLCs.

CO3: Illustrate the various parts of distributed control systems and its application in industry.

CO4: Classify various industrial communication protocols used in PLC's and DCS network.

Unit I

Computers in process control

07 HRS

Data loggers – Data Acquisition Systems (DAS) –Introduction to Direct Digital Control (DDC) – Introduction to Supervisory Control and Data Acquisition Systems (SCADA) and Distributed Control System.

Unit II

Programmable Logic Controller (PLC) Basics

08 HRS

Review of relay based systems. Introduction and importance of PLC, Types of PLC, Basic architecture of CPU of PLC, Basic wiring diagram of PLC, PLC operation and various standards, input/output modules- power supplies and isolators. General PLC programming procedures-programming on-off inputs/ outputs-Auxiliary Commands and functions- PLC Basic Functions- register.

Unit III Programming of PLC using timers and counters

08 HRS

Introduction to programming standards of PLC, basic relay instruction, timer and counter instructions, Related programming and practice examples.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

Unit IV

Advanced instructions in PLC

08 HRS

PLC intermediate functions: Arithmetic functions - comparison functions, logic functions – Data handling instructions. PLC input-output and PLC sequencer instructions, PLC program flow instructions, Designing of I/O system, creating ladder diagram from process control description.

Unit V:

Introduction to DCS

08HRS

Distributed Control Systems (DCS): Definition – merits and demerits, Local Control Unit (LCU) architecture – hierarchical system structure functional level, database organization, field stations, intermediate stations, central computer station, monitoring and command facilities.

Laboratory:

1. Implementation of basic logic gate truth table using PLC
2. Problems based on Timer operations of PLC
3. Problems based on Counter operations of PLC
4. Problems based on logical operations of PLC
5. Problems based on arithmetic and data transfer operations of PLC
6. Problems based on sequencing operation using PLC
7. Implementation of Bottle filling Plant using PLC
8. Implementation of LIFT control model using PLC
9. Implementation of Water level controller using PLC
10. Implementation of Star delta starter
11. Implementation of Motor control using PLC
12. Implementation of Stepper motor control using PLC



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

Text Books:

1. John.W. Webb, Ronald A Reis, "Programmable Logic Controllers - Principles and Applications", Prentice Hall Inc., New Jersey, 2003.
2. B.G. Liptak, "Instrument Engineers Hand, Process control and Optimization", CRC Press- Radnor, Pennsylvania, 2006.
3. M.Chidambaram, "Computer Control of Process," Narosa Publishing, New Delhi, 2003
4. Gary Dunning "introduction to Programmable logic controllers" 3rd edition, CENGAGE learning

Reference Books:

1. B.G. Liptak, "Process software and digital networks," CRC press, Florida-2003.
2. Curtis D. Johnson "Process control instrumentation technology," Prentice Hall , New Jersey 2006.
3. Krishna Kant, "Computer-Based Industrial Control," PHI, New Delhi, 2004
4. Frank D. Petruzella, "Programmable Logic Controllers", McGraw Hill, New York, 2004.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

PROJECT WORK - I
12I T8DCPRJ

Objectives :

)]To identify, formulate and solve problems by applying the knowledge of Mathematics and Engineering in the instrumentation technology domain.

)]Ability to work in teams leading to improvement in team work and leadership qualities

)]Ability to come out with different alternatives to solve their problem. These alternatives should include emerging technologies and their associated cost etc

Project Evaluation

Students in groups of 2-4 are allowed to implement a project. For each project group, one faculty supervisor is assigned to guide and monitor the progress. In addition, the department constitutes the Project Evaluation Committee (PEC) comprising three faculty members to ensure uniform evaluation for all project groups. The work carried out in this Semester is termed as Phase-I & the Continuation of this work shall be taken up in VIII Semester.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

VIII SEMESTER ELECTRICAL CLUSTER

Subject	INTELLECTUAL PROPERTY RIGHTS	Sub. Code	11HS8GCI PR
Credits	02	L-T-P	2-0-0

Course Outcomes

CO1: Understand rights conferred on a patentee; copy right, trademarks and trade secrets leading to improvement in team work and leadership qualities.

CO2: Ability to identify, formulate and solve problems with case study on patent law and trademarks.

CO3: Understand the principles and procedure for obtaining patent which supports engineer and society and Use of technology to invent new product using modern tools which leads to patents.

CO4: Ability to apply engineering knowledge to give an insight view into IP management, licensing, valuation, audit and other aspects of IP attributing to ethics.

UNIT I [05 hours]

Basic principles of IP laws: Introduction, Concept of property, Need for a holistic approach, Constitutional aspects of IP, Evolution of the patent system in UK, US and India, Basis for protection, Invention, Criteria for patentability, Non - patentable inventions.

UNIT II [05 hours]

Patents: Introduction, Origin and meaning of the term patent, Objective of a patent law, the legislative provisions regulating patents, principles underlying the patent law in India, patentable invention. Procedure for obtaining patent: Submission of application, Filing provisional and complete specification, Examination of the application, advertisement of the acceptance, opposition, Grant and sealing of patent, Term of the patent, compulsory license. Provisional and complete specification: Definition of Specification, Kinds of specification, provisional specification, complete specification, Claims, Conditions for amendment.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT III

[05 hours]

Rights conferred on a patentee: Patent rights, Exception and limitations, Duties of a Patentee. Transfer of patent: Forms of transfer of Patent rights, Assignment, kinds of assignment, License, kinds of license, Rights conferred on a licensee, Transmission of patent by operation of law. Infringement of patents: Construction of claims and infringement, patents held to be infringed, patents held to be not infringed. Action for Infringement: Where a suit is to be instituted, procedure followed in the suit, Onus of establishment infringement, Defence by the defendant, The Relief's, Injunction, Damages or account of profits, patent agents, patent drafting, database searching, and Case studies.

UNIT IV

[06 hours]

Copy Right: Meaning and characteristics of copy right, Indian copy right law, requirement of copy right, Illustrations copy right in literary work, Musical work, Artistic work, work of architecture, Cinematograph film, sound recording. Author and Ownership of copy right: Ownership of copy right, Contract of service, Contract for service, rights conferred by copy right, terms of copy right, license of copy right. Infringement of copy right: Acts which constitute infringement, general principle, direct and indirect evidence of copying, Acts not constituting infringements, Infringements in literary, dramatic and musical works, Remedies against infringement of copy right, Case studies.

UNIT V

[04 hours]

Trade Marks: Introduction, Statutory authorities, procedure of registration of trade marks, rights conferred by registration of trade marks, licensing in trade mark, infringement of trade mark and action against infringement.

Textbook:

1. Dr. T Ramakrishna, "Basic principles and acquisition of Intellectual Property Rights", CIPRA, NSLIU -2005
2. Dr. B.L. Wadehra, " Intellectual Property Law Handbook", Universal Law Publishing Co. Ltd., 2002.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

Reference books:

1. Dr. T Ramakrishna , "Ownership and Enforcement of Intellectual PropertyRights" , CIPRA, NSLIU -2005
2. "Intellectual Property Law (Bare Act with short comments)", Universal Law Publishing Co. Ltd.. 2007.
3. "The Trade marks Act 1999 (Bare Act with short comments)", Universal Law Publishing Co. Ltd., 2005.
4. "The Patents Act, 1970 (Bare Act with short comments), as amended by Patents (Amendment) Rules 2006 w.e.f. 5-5-2006". Commercial law publishers (India) Pvt. Ltd., 2006.
5. Thomas T Gordon and Arthur S Cookfair, "Patent Fundamentals for Scientist and Engineers", CRC Press 1995.
6. PrabuddhaGanguli, "Intellectual Property Rights", TMH Publishing Co. Ltd, 2001



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

VIII SEMESTER ELECTRICAL CLUSTER

Subject	PROJECT MANAGEMENT	Sub. Code	11HS8GCPRM
Credits	02	L-T-P	2-0-0

Course Outcomes

CO1: Understand the fundamental concepts of project and project management as a project manager.

CO2: Able to visualize and apply the finance for establishing the project in time.

CO3: Able to manage the human resource for project execution.

CO4: Able to perform systematic and statistical analysis for the effective project execution.

CO5: Understanding the control and coordination of the project as a project manager.

UNIT I

Concepts of Project Management: Concept of Project, Categories of Projects, Project Life Cycle Phases, Project Management Concepts, Tools and Techniques for Project Management, The Project Manager, Basic Education for a Project Manager, Roles and Responsibilities of Project Manager, Project Manager as a Profession, Summary.

UNIT II

Establishing the Project: Scope, Time, Cost and Performance Goals , Feasibility Report, Financing Arrangements, Preparation of Cost Estimates, Finalization of Project Implementation Schedule, Evaluation of the Project Profitability, Appointing a Project Manager, Fixing the Zero Date, Summary.

UNIT III

Organizing Human Resources and Contracting, Delegation, Project Manager's Authority, Project Organization, Accountability in Project Execution, Contracts' Roles of Contracting, Tendering and Selection of Contractors, Team Building, Summary.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT VI

Organizing Systems and Procedures for Project Implementation, Working of Systems, Design of Systems, Project Work System Design, Work Breakdown Structure (WBS), Project Execution Plan (PEP), Project Procedure Manual, Project Control System, Planning, Scheduling and Monitoring, Monitoring Contracts, Project Diary, Summary.

UNIT V

Project Direction, Coordination and Control, Communications in a Project, Project Coordination, Project Control, Scope/Progress Control, performance control, Schedule Control, Cost Control, and Summary.

TEXT BOOKS:

1. Project Management – S Choudary, Tata McGRAW Hill Publishing Company Limited

REFERENCE BOOKS:

1. Project management – David I Cleland – Mcgraw Hill International Edition, 1999.
2. Project Management – Gopalakrishnan – Mcmillan India Ltd.
3. Project Management –Harry-Maylor-Pearson Publication.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

PROJECT WORK - II
12IT8DCPRJ

Objectives :

)]To develop team work and communication skills by the oral presentation on the work carried out.

)]Use project management tools such as Gantt Charts created with MS Project.)Ability to test, debug, and verify that the design meets the desired specifications.)To emphasize the need for professional and ethical responsibility, if the work leads to a technical conference/journal paper

Project Evaluation

Students in groups of 2-4 are allowed to implement a project. For each project group, one faculty supervisor is assigned to guide and monitor the progress. In addition, the department constitutes the Project Evaluation Committee (PEC) comprising three faculty members to ensure uniform evaluation for all project groups. Completion of the work taken up in Phase - I is evaluated by the internal PEC Project evaluation committee and external evaluation.

SEMINAR
11IT8DCSMR

Objectives :

)]Ability to develop communication skills in both technical and non-technical environment.

)]To identify the latest topics in the field of instrumentation technology Engineering

)]To emphasize the need for professional and ethical responsibility, if the study leads to a technical conference/journal paper

Student Task:

The technical seminar presentation requires students to present from recent (within one year) IEEE/IET Journals/Transactions.

This is an individual presentation for 30 minutes.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

A copy of the seminar report is to be submitted to each member of the SEC one week before the presentation.

One day before the seminar, a copy of the presentation handout is to be given to each member of the SEC.

There is an external evaluation for the Seminar report submitted by every student.

Seminar Evaluation:

Student seminars are evaluated by the Seminar Evaluation Committee (SEC) constituted by department. It comprises minimum three faculty members from the department. The evaluation is as based on assessing the following:

)Selection of topic from recent Journals

)Seminar as a whole

)Clarity of presentation

)Use of audio-visual aids

)Makes an interesting presentation

)Management of allocated time

)Ability to answer questions posed by audience

Each member of the SEC evaluates on the above parameters. The average constitutes the CIE.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

OBJECT ORIENTED PROGRAMMING USING C++ 12ES5GE1OP

Objective:

Ability to apply the concepts of Object Oriented Programming with emphasis on C++

Ability to design and analyze real life applications by writing efficient programs to implement various modules considering constraints such as memory and portability.

- Emphasize the importance of Classes, Objects, Data Abstraction, Data encapsulation, Overloading, Inheritance, Polymorphism and Reusability
- Impart education to understand the need for life-long learning in the area of software engineering/programming

UNIT I [10 hours]

Principles of Object oriented programming: OOP Concepts, Program construction, directives, preprocessor directives, header files and library files, Benefits and applications Beginning with C++: Definition, application, structure of C++ program, compiling and linking Tokens, expressions and control structures: Tokens, keywords, identifiers and constants, data types, symbolic constants, variables, operators, manipulators, control statements and loops.

UNIT II [10 hours]

Functions in C++: Function prototype, argument passing, recursion, inline functions, friend and virtual functions Classes and objects: Class definition and declaration, member functions, static data members and member functions, arrays of objects, returning objects.

UNIT III [10 hours]

Constructors and destructors: Constructors, parameterized constructors, multiple constructors in a class, copy constructor, dynamic constructors, and destructors. Operator overloading and type conversions: Overloading unary and binary operators, overloading using friends, rules for overloading.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT IV

[12 hours]

Inheritance: Inheritance, public, private and protected inheritance. Private member inheritance. Types of inheritance: Single, Multilevel, multiple, hierarchical, hybrid. Pointers, virtual functions and polymorphism. Pointers, pointers to objects, this pointer, pointers to derived classes, virtual functions. Constructors in derived class. Managing console I/O operations: C++ streams, C++ stream classes, I/O operations, managing O/P with manipulators.

UNIT V

[10 hours]

Templates: Class templates, function templates, overloading template functions, member function templates and non type template arguments. Exception handling: Basics, throwing and catching mechanisms, rethrowing an exception, specifying exceptions.

TEXT BOOKS:

4. Object oriented Programming with C++, -E Balagurusamy (TMH Publications, 4th edn)
5. Object oriented Programming in turbo C++, Robert Lafore (GALGOTIA Publications)

REFERENCE BOOK:

4. Let Us C++--Yashavanth P. Kanetkar (BPB Publications)
Programming With C++-----Schaum'sseries (TMH Publications)
5. Programming With C++-----Schaum'sseries (TMH Publications) .



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

DIGITAL SYSTEM DESIGN USING VHDL 12ES5GE1DD

Objective:

5. Ability to design, conduct and analyze experiments in the field of Digital Electronics using software tools.
ability to work in teams leading to improvement in team work
ability to identify, formulate and solve problems in Digital Electronics using VHDL

UNIT I [10 hours]

Introduction: VHDL description of combinational networks, Modeling flip-flops using VHDL, VHDL models for a multiplexer, Compilation and simulation of VHDL code, Modeling a sequential machine, Variables, Signals and constants, Arrays, VHDL operators, VHDL functions, VHDL procedures, Packages and libraries, VHDL model for a counter.

Additional Topics in VHDL: Attributes, Transport and Inertial delays, Operator overloading, Multi-valued logic and signal resolution, IEEE-1164 standard logic, Generics, Generate statements, Synthesis of VHDL code, Synthesis examples, Files and Text IO.

UNIT II [08 hours]

Designing With Programmable Logic Devices: Read-only memories, Programmable logic arrays (PLAs), Programmable array logic (PLAs), Other sequential programmable logic devices (PLDs), Design of a keypad scanner.

UNIT III [06 hours]

Designing With Programmable Gate Arrays And Complex Programmable Logic Devices: Xilinx 3000 series FPGAs, Designing with FPGAs, Xilinx 4000 series FPGAs, using a one-hot state assignment, Altera complex programmable logic devices (CPLDs), Altera FELX 10K series CPLDs.

UNIT IV [08 hours]

Design Of Networks For Arithmetic Operations: Design of a serial adder with accumulator, State graphs for control networks, Design of a binary multiplier, Multiplication of signed binary numbers, Design of a binary divider.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT V

[07 hours]

Digital Design with SM Charts: State machine charts, Derivation of SM charts, Realization of SM charts. Implementation of the dice game, Alternative realization for SM charts using microprogramming, Linked state machines.

TEXT BOOK:

Charles H. Roth. Jr., Digital Systems Design using VHDL, Thomson Learning, Inc, 9th reprint, 2006.

REFERENCE BOOKS:

3. Stephen Brown & Zvonko Vranesic, Fundamentals of Digital Logic Design with VHDL, Tata McGraw-Hill, New Delhi, 2nd Ed., 2007
4. Mark Zwolinski, Digital System Design with VHDL, 2 Ed, Pearson Education., 2004
5. Volnei A Pedroni, Digital electronics and Design with VHDL. Elsevier

DATA STRUCTURES WITH C++
10ML5GE1DS (Except ML)

The objective: To introduce the fundamentals of Data Structures, Abstract concepts and how these concepts are useful in problem solving. Analyze step by step and develop algorithms to solve real world problems.

UNIT I

[08 hours]

C++ programming Basics: Need of object oriented programming, procedural languages, characteristics of OOP, preprocessor directives, data types, manipulators. Structures: Structures, enumerated data types, Boolean type, Functions: passing arguments, returning values, reference arguments, overloaded functions, inline functions, variable and storage classes.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT II [07 hours]
Objects and classes: objects as data types, constructors, destructors, overloaded constructors. Arrays: Arrays as class member data types, passing arrays, arrays as objects, strings, arrays of strings.

UNIT III [07 hours]
Operator overloading: over loading of unary operators, binary operators, data conversion.
Inheritance: Inheritance, derived class and base class, overriding member functions, scope

UNIT IV [07 hours]
Pointers, pointers to objects, linked list, virtual functions, static functions, Working with files: Introduction, Classes for the stream operators, opening and closing files, detecting end-of-file, more about open(); file modes, file pointers and their manipulations, sequential input and output operations, Updating a file: Random access, error handling during file operation.

UNIT V [10 hours]
Data structures: data representation, matrices, stacks, Queues

Lab experiments

Lab components must comprise of experiments that reinforce the theoretical understanding of the corresponding subject. Experiments would address concepts of Structures, Classes, Objects, Operator overloading, Inheritance, File I/O. Stacks and Queues.

TEXT BOOKS:

- 2 Object oriented programming in TURBO C++, Robert Lafore, Galgotia Publications.2002
- 3 Data Structures using C++, D.S.Malik, Thomson, 2003

REFERENCE BOOKS:

- 2 Object Oriented Programming with C++, E Balaguruswamy, Third edition, TMH2006
- 2.C++ the complete reference, Herbert Schildt, Fourth edition, TMH, 2003
5. Data Structures, Algorithms and Applications in C++: SartajSahni, Tata McGrawHill Publications.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

ESSENTIALS OF INFORMATION TECHNOLOGY

12ES5GE1IT

UNIT I [07 hours]

Introduction to Computer Systems - Basics of computer systems - Various hardware components - Data storage and various Memory units - Central Processing Unit - Execution cycle - Introduce to software and its classifications

Operating system concepts– Introduction – Memory management - Process management - Interprocess Communication – Deadlocks - File management - Device management

UNIT II [08 hours]

Problem solving Techniques - Introduction to problem solving - Computational problem and it's classification - Logic and its types - Introduction to algorithms - Implementation of algorithms using flowchart - Flowcharts implementation through RAPTOR tool - Searching and sorting algorithms - Introduction and classification to Data Structures - Basic Data Structures - Advanced Data Structures

UNIT III [08 hours]

Programming & Testing - Introduction to Programming Paradigms and Pseudo Code - Basic programming concepts - Program Life Cycle - Control Structures - Introduction and Demonstration of 1-D Array and 2-D Array - Code Optimization techniques Structured Programming – Functions – Structures - File Handling - Introduction to Software Development Life Cycle - Industry Coding Standards and Best Practices - Testing and Debugging - Code Review

UNIT IV [08 hours]

2. RDBMS- data processing – the database technology – data models
3. ER modeling concept –notations – Extended ER features
4. Logical database design - normalization
5. SQL – DDL statements – DML statements – DCL statements
6. Joins - Sub queries – Views
7. Database design Issues



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT V

[08 hours]

Integrated Project - Project Briefing, Project contact sessions and Project evaluation.
Sample exercise on using Embedded SQL /JDBC

Note: The project is a Group Activity consisting of 3/4 members in a team. The Project will be carried out offline for duration of 25-32 hours.

3. Tutorial

The assignments for Operating System Concepts, Problem Solving techniques, Programming & Testing, Object Oriented Concepts and RDBMS have to be completed as a part of Tutorial.

4. Project

Students are required to implement an integrated project using the concepts of Programming & Testing, Object Oriented Concepts and RDBMS.

Following activities are involved in Project Development:

3. Preparation of High level design and Detailed design document,
4. Unit Test Plan and Integrated Test Plan
5. Coding and Unit Testing , Integration Testing

Students can use the following to implement the Project:

6. Programs using C or C++ or Java Language
7. Embedded SQL can be used to connect the Front-End with the backend Database systems in case of C/C++
8. JDBC can be used to connect Front-End with the backend Database systems in case of Java

TEXT BOOK:

Foundation Program, Volume I, II & III developed by Campus Connect initiative of Infosys



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

REFERENCE BOOKS:

2. Andrew S. Tanenbaum , : Structured Computer Organization , PHI, 4th edition, 1999
 3. John L. Hennessy, David Goldberg, David A. Patterson, Computer Architecture : A Quantitative Approach, 2nd Edition Published by Morgan Kaufman Publishers, 1996
 4. Silberschatz and Galvin, Operating System Concepts, John Wiley & Sons ,Sixth edition
- Andrew Tanenbaum, Modern Operating Systems, Pearson Education
 - Milan Milenkovic, "Operating Systems: concepts and design", McGraw-Hill
 - Charles Crowley, "Operating Systems: A Design-Oriented Approach"
 - Dromey, R.G. , How to solve it by computers, Prentice Hall, 2005
 - Alfred V.Aho, Ullman, Hopcroft, Data Structures and Algorithms, Addison-wesely.
 - Lipschutz, Seymour & G A V Pai, Data Structures, Tata McGraw – Hill
 - Baldwin, Douglas & Scragg, Greg W., Algorithms and Data Structures The Science of Computing, Dreamtech
 - Kernighan., Ritchie, ANSI C Language, Prentice Hall of India, New Delhi, 1992.
 - Yashwant Kanitker, Let Us C, by Yashwant Kanitker, Second Edition
 - Schaum series, Programming in C, Third Edition
 - Programming Pearls , by Jon Bentley, Pearson Education publication
 - Aho, Alfred V, Compiler Principles, Techniques and Tools, Pearson Education
 - Tharp Alan L, File Organization and Processing, John Willey and Sons
 - Henry F Korth, Abraham Silberschatz, "Database system concepts", Second ed., McGraw-Hill International editions, Computer Science series, 1991
 - Elmasri, Navathe, "Fundamentals of Database Systems", Third ed, Addison Wesley
 - C.J.Date , "An introduction to Database Systems", Sixth ed, Narosa Publications



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

DIGITAL CONTROL OF DYNAMIC SYSTEMS

12EC5GE1DC

UNIT I [10 hours]

Review of Continuous Control and Introductory Digital Control

Introduction - Review of Continuous Control - Dynamic Response - Basic Properties of Feedback - Root Locus - Frequency Response Design – Compensation - State-Space Design - Introductory Digital Control – Digitization - Effect of Sampling - PID Control

UNIT II [08 hours]

Discrete System Representation and Analysis

Discrete Systems Analysis - Linear Difference Equations - The Discrete Transfer Function - Discrete Models of Sampled-Data Systems - Signal Analysis and Dynamic Response - Frequency Response - Properties of the z-Transform - Sampled-Data

Systems - Analysis of the Sample and Hold - Spectrum of a Sampled Signal - Data Extrapolation - Block Diagram Analysis - System Output Between Samples

UNIT III [10 hours]

Design of Digital Control Systems in Time Domain and Frequency Domain

Discrete Equivalents - Design via Numerical Integration - Zero-Pole Matching - Hold Equivalents - Design Using Transform Techniques - System Specifications - Design by Emulation - Direct Design by Root Locus in the z-Plane - Frequency Response Methods - Direct Design Method of Ragazzini – Specific case studies

UNIT IV [10 hours]

Design of Digital Control Systems using State Space Technique

Design Using State-Space Methods - Control Law Design - Estimator Design -

Regulator Design: Control + Estimator - Reference Input - Integral Control and

Disturbance Estimation - Effects of Delays - Controllability and Observability - Specific



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

case studies - Multivariable and Optimal Control – Decoupling - Time-varying optimal Control - LQR Steady-State Optimal Control - Optimal Estimation - Multivariable Control Design – with Examples - Specific case studies

UNIT V

[10 hours]

Quantization effects and parametric sensitivity

Quantization Effects - Analysis of Round-off Errors - Effects of Parameter Round-off - Limit Cycles and Dither - Sample Rate Selection - The Sampling Theorem's Limit - Time Response and Smoothness - Errors due to Random Plant Disturbances - Sensitivity to Parameter Variations - Measurement Noise and Antialiasing Filters - Multirate Sampling

TEXT BOOK:

“Digital Control of Dynamic Systems”, Gene F. Franklin, J. David Powell and Michael Workman, Addison-Wesley, 3rd Edition, 1998

MEDICAL PHYSICS

12ML5GE1MP

UNIT I

[07 hours]

Heat and Cold in Medicine: Physical Basis of Heat and temperature. Thermometry and Temperature Scales. Thermography-Mapping the Body's temperature. Heat therapy. Use of cold in Medicine. Cryosurgery, safety with Cryogenics

UNIT II

[08 hours]

Energy, Work and Power of the Body: Conservation of Energy in the Body, Energy changes in the body, Work and Power, Heat losses from the Body.

Pressure: Measurement of Pressure in the body, Pressure inside the Skull, Eye Pressure, Pressure in Digestive system, Pressure in the skeleton, Pressure in urinary bladder, Pressure effects while Diving, Hyperbaric Oxygen Therapy (HOT)



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT III [07 hours]

The Physics of the Lungs and Breathing: The Airways, How the blood and lungs interact, Measurement of Lung volumes, Pressure-Airflow-Volume relationships of the Lungs, Physics of alveoli, The breathing mechanism, Airway Resistance, Work of breathing, Physics of some common lung diseases.

UNIT IV [10 hours]

Physics of the Cardiovascular System: major components of the Cardiovascular system. O₂ and CO₂ exchange in the capillary system, work done by the Heart, Blood Pressure and its Measurement, Pressure across the blood vessel wall (Transmural Pressure), Bernoulli's Principal applied to the cardiovascular system. Blood Flow- Laminar and Turbulent, Heart sounds, the physics of some cardiovascular diseases.

Applications of Electricity and Magnetism in Medicine: Electric shock, High frequency electricity in Medicine, Low frequency electricity and Magnetism in Medicine.

UNIT V [10 hours]

Sound in Medicine: General properties of sound, the body as a drum, The stethoscope, Ultrasound pictures of the body, Ultrasound to measure motion, Physiological effects of Ultrasound in therapy, The production of Speech (Phonation). The structure of Ear, Sensitivity of Ear, testing your hearing deafness and Hearing aids.

Light in medicine: measurement of visible Light and its units, Application of Visible light in Medicine, Application of Ultraviolet and Infrared light in Medicine, Lasers in Medicine, Application of Microscopes in Medicine. Focussing Elements of the Eye, The structure and functioning elements of the eye, Diffraction effects of the Eye, Optical illusions and related phenomena, Defective vision and its correction, Color vision and Chromatic aberration, Instruments used in Ophthalmology.

TEXT BOOK:

- MEDICAL PHYSICS - by John R Cameron, James G Skofronick, A Wiley-nterscience Publication.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

COMMUNICATION SYSTEMS (EE ONLY)

10EE5GE1CS

Objectives:

This course provides an understanding of communication theory as applied to the transmission of information bearing signals with equal emphasis and attention given to both analog and digital communication techniques.

UNIT I [12 hours]

Amplitude modulation: Time-Domain Description, Frequency domain description, Generation of AM waves, Detection of AM waves, AM/DSB, Time-Domain Description, Frequency domain description Generation of DSBSC waves, Coherent Detection of DSBSC Modulated waves. Costas loop, Quadrature Carrier multiplexing, AM-SSB/SC generation, Frequency-Domain Description, Frequency discrimination method for generation an SSB Modulated wave, time domain description, phase discrimination method for generating an SSB modulated wave, Demodulation of SSB waves, Comparison of amplitude modulation techniques, frequency translation, FDM.

UNIT II [07 hours]

Angle modulation: Basic Concepts, Frequency Modulation, Spectrum Analysis Of sinusoidal FM wave, NBFM, WBFM, Constant Average power, Transmission bandwidth of FM waves, Generation of FM waves, Direct FM, demodulation of FM waves, frequency discriminator, ZCD, phase locked loop (1st order) of AM and FM.

UNIT III [07 hours]

Noise in Analog modulation systems: Signal-to-noise ratios, AM receiver model, Signal-to - noise ratios for coherent reception, DSBSC receiver, SSB receiver, noise in AM receivers using envelope detection, threshold effect, FM receiver model, noise in FM reception, FM threshold effect, pre-emphasis and de-emphasis in FM systems.

UNIT IV [12 hours]

Pulse modulation :Sampling theorem for low-pass and band-pass signal, statement and proof, PAM, Channel bandwidth for a PAM signal, natural sampling, flat-top sampling, signal recovery through holding, quantization of signals, quantization error, PCM, electrical representations of binary digits, PCM systems, DPCM, delta Modulation, Adaptive delta modulation.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT V

[14 hours]

Digital Modulation: Introduction, Binary Shift Keying, DPSK, QPSK, Type D flip-flop, QPSK transmitter, non-offset QPSK, QPSK receiver, signal - space representation, BFSK, spectrum, receiver for BFSK, geometrical representation of orthogonal BFSK, line codes, TDM.

TEXT BOOKS:

3. "Analog and Digital communication", Simon Haykin, John Wiley.
4. "Principles of communication systems", Taub and Schilling, Tata McGraw Hill.

REFERENCE BOOKS:

4. "Electronic Communication Systems", 2nd Edition, Blake, Thomson publishers.
5. "Electronic Communication Systems", George Kennedy.

FUNDAMENTALS OF HDL

12EE5GE1HD

UNIT I

[07hours]

Introduction: Why HDL? , A Brief History of HDL, Structure of HDL Module, Operators, Data types, Types of Descriptions, simulation and synthesis, Brief comparison of VHDL and Verilog

UNIT II

[08 hours]

Data-Flow Descriptions: Highlights of Data-Flow Descriptions, Structure of Data-Flow Description, Data Type – Vectors. Behavioral Descriptions: Behavioral Description highlights, structure of HDL behavioral Description, The VHDL variable –Assignment Statement, sequential statements.

UNIT III

[08 hours]

Structural Descriptions: Highlights of structural Description, Organization of the structural Descriptions, Binding, state Machines, Generate, Generic, and Parameter statements.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT IV [08 hours]

Procedures and Functions: Procedures, Tasks, and Functions: Highlights of Procedures, tasks, and Functions, Procedures and tasks, Functions. Advanced HDL Descriptions: File Processing, Examples of File Processing.

UNIT V [08 hours]

Synthesis Basics: Highlights of Synthesis, Synthesis information from Entity and Module, Mapping Process and Always in the Hardware Domain.

LAB Experiments

Combinational logic circuits, sequential circuits using data flow (simulation and implementing using FPGA/CPLD) sequential descriptions & structural descriptions. Interfacing experiments : stepper motor, dc motor, relay, waveform generation.

TEXT BOOK:

HDL Programming (VHDL and Verilog)- Nazeih M.Botros- Dreamtech Press (Available through John Wiley – India and Thomson Learning), 2006 Edition

REFERENCE BOOKS:

5. Verilog HDL –Samir Palnitkar, Pearson Education
6. VHDL –Douglas Perry, TMH
7. Fundamentals of Digital Logic with Verilog Design-Stephen Brow

TMH

8. Circuit Design with VHDL- Volnei A.Pedroni, PHI



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

FUNDAMENTALS OF VLSI (EE ONLY)

10EE6GE2FV

Objective

Use of VLSI technology has increased in recent past. With the advent of power semiconductor devices, most of the large circuits have been replaced by small and compact VLSI circuits.

UNIT I [10 hours]

A Review of Microelectronics and an Introduction To Mos Technology: Introduction to integrated circuit technology, Production of E-beam masks. Introduction, VLSI technologies, MOS transistors, fabrication, thermal aspects, production of E-beam masks.

UNIT II [10 hours]

Basic Electrical Properties of Mos an Bicmos Circuit: Drain to source current I_{ds} versus V_{ds} relationships-BICMOS latch up susceptibility. MOS transistor characteristics, figure of merit, pass transistor NMOS and COMS inverters, circuit model, latch up.

UNIT III [08 hours]

Mos And Bicmos Circuit Design Processes: Mass layers, stick diagrams, design, symbolic diagrams.

UNIT IV [14 hours]

· Basic Circuit Concepts: Sheet resistance, standard capacitance, capacitance layer, inverter delays, wiring capacitance, choice of layers.

Scaling of Mos Circuits: Scaling model and scaling factors- Limit due to current density.

UNIT V [10 hours]

Subsystem Design And Layout , Subsystem Design Processes : Some architecture issues- other systems considerations. Examples of structural design, clocked sequential circuits . Some general considerations, an Illustration of design process, observations.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

TEXT BOOKS:

"Basic VLSI Design" -3rd Edition, Pucknell Douglas AI , PHI

REFERENCE BOOKS:

c. "Fundamentals of Modern VLSI Devices" -Yuan TaunTak H Ning Cambridge Press, South Asia Edition 2003,

d. "ModernVLSI Design Wayne wolf" , Pearson Education Inc. 3rd edition"-Wayne Wolf 2003.

UTILIZATION OF ELECTRICAL POWER
10EE6GE2UP

Objective:

4. To understand the fundamentals of illumination and its classification and the electric heating and welding.
5. To study Electric traction systems in detail and their practical applications.

UNIT I [12 hours]

Heating and welding: Advantages and methods of electric of heating, resistance ovens, induction heating, dielectric heating, the arc furnace, heating of building, electric welding, resistance and arc welding, control device and welding equipment.

UNIT II [08 hours]

Electrolytic process: Fundamental principles, extraction, refining of metals, electroplating. Factors affecting electro deposition process, power supply for electrolytic process.

UNIT III [08 hours]

Illumination: Laws of illumination, lighting calculation, factory lighting, flood lighting, street lighting, different types of lamps, incandescent, fluorescent, vapor and CFL and their working.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT IV

[12 hours]

Introduction to Electric traction: Systems of traction, speed time curve, tractive effort, co-efficient of adhesions, specific energy, factors affecting specific energy consumption, selection of traction motors.

UNIT V

[12 hours]

Control of Traction Motors: Methods of speed control, energy saving by series parallel control. AC series motor, characteristics, regenerative braking, linear induction motor and their use. Diesel electric equipment, train lighting system.

TEXT BOOKS:

- J.B.Gupta, Utilization of Electric Power and Electric Traction, S.K Kataria and Sons
- SoniGupta & Bhatnagar, A Course in Electrical Power, DhanpatRai and Sons

REFERENCE BOOK:

Openshaw Taylor, Utilization of electric energy, Orient Longman



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

OPERATING SYSTEMS CONCEPTS

12ES6GE2OS

Objective:

2. ability to apply knowledge of computers in field of Electronics & Telecommunication Engineering
3. ability to identify and solve memory allocation problems in operating system
4. recognize the need for operating systems in the field of telecommunication and pursue life-long learning in it

UNIT I [10 hours]

INTRODUCTION AND OVERVIEW OF OPERATING SYSTEMS:

Abstract views of an Operating system, Goals of an O.S, Operation of an O.S, Efficiency, system performance and user convenience, Classes of operating systems: O.S and the computer system, Batch processing system, Multi programming systems, Time sharing systems, Real time operating systems, distributed operating systems

UNIT II [12 hours]

SCHEDULING: Preliminaries, Non-preemptive Scheduling Policies, Preemptive Scheduling Policies, Scheduling practice, Real Time Scheduling, Scheduling in Unix
MEMORY MANAGEMENT: Static and Dynamic memory allocation, Memory allocation to a process, Reuse of memory, Contiguous memory allocation, Noncontiguous memory allocation, Paging, Segmentation,

UNIT III [11 hours]

VIRTUAL MEMORY: Virtual memory basics, Demand paging : overview of paging, demand paging preliminaries, page replacement, Page replacement policies, Memory allocation to process, Copy-on-write, UNIX virtual memory.

FILE SYSTEMS: File system and IOCS, Files and File Operations, Fundamental of File Organizations, Directory Structures, Allocation of disk space, Implementing file access, UNIX file system.

UNIT IV [11 hours]

Message Passing: Overview of message passing, Implementing message passing, Mailboxes, Message passing in Unix

Deadlocks: Definition of deadlock, Deadlock in resource allocation, Handling deadlocks, Deadlock detection and resolution, Deadlock prevention, Deadlock avoidance, Deadlock handling in practice



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT V

[08 hours]

STRUCTURE OF THE OPERATING SYSTEMS: Operation of an O.S, Structure of an operating system, Operating system with monolithic structure, layered design of operating system, Virtual machine operating systems, Kernel based operating systems, and Microkernel based operating systems

TEXT BOOK:

“Operating Systems - A Concept based Approach”, D. M. Dhamdhare, TMH, 2nd Ed, 2006.

REFERENCE BOOKS:

4. Operating Systems Concepts, Silberschatz and Galvin, John Wiley, 7th Edition, 2001.
5. Operating System – Internals and Design Systems, Willaim Stalling, Pearson Education, 4th Ed, 2006

ADVANCED MICROCONTROLLERS & APPLICATIONS

12ES6GE2MC

Objective

- To provide basic concepts of a RISC Machine(ARM) Processor
- Understand architecture, instruction set and programming both in ARM and Thumb mode
- Understand the various aspects embedded C programming and embedded system protocols

UNIT I

[08 hours]

Migration from 8051 to 32bit cores, RISC design and ARM Design Approach, Advantages of ARM, ARM Organization, Registers, Current Program Status Registers,3 stage and 5 Stage Pipeline, Exceptions ,Interrupts and Vector Table, Co processor Interface.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

- UNIT II [08 hours]
ARM Instruction Sets, Data Processing Instructions, Branch Instructions, Load Store, Software Interrupt, Program Status Register Instructions, Thumb Instruction Sets; Thumb
Register Usage, ARM-Thumb Inter-working, Cross compilers and Optimization, Overview of C compilers and Optimization, Basic C data types, C looping Structures, Function calls., Portability Issues.
- UNIT III [08 hours]
Writing and Optimizing ARM Assembly Code, Writing Assembly Code, Instruction Scheduling, Register Allocation, Looping Constructs, Bit Manipulation, Example Programs.
- UNIT IV [05 hours]
Firmware and Bootloader, Embedded Operating Systems, Memory Management Unit, Working With I2C SPI and USB protocols.
- UNIT V [10 hours]
Introduction to Intel Atom Architecture
Atom Processor: Addressing Modes, Registers, Memory accesses and memory map, Segmentation, Task switching, Paging, Hyper threading, Caches and TLB, Execution pipeline, x86 legacy features, Interrupts.
- LAB Experiments (Experiments to be done using ARM7 & Cortex M3) Part-I
- Experiment 1 : Interfacing a 7 segment display and working.
- Experiment 2 : Using GPIOs on Expansion ports
- Experiment 3 : Write serial communication program in C.
- Experiment 4 : Interfacing and running PWM drive.
- Experiment 5: Configuring and working with Audio Codec
- Experiment 6: LCD Interface(Cortex M3)
- Experiment 7: Hardware & Software INT (Cortex M3)
- Experiment 8: File Operation (Cortex M3)
- Project Work



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

TEXT BOOK:

2. ARM System Developer's Guide By Andrew N Sloss
3. ARM System-On-Chip Architecture By Steve Furber, Addison Wesley, Pearson Education, 2nd edition
4. Experiments on ARM 9 –Practical Guide ,Book By Innovate Software Solutions Pvt Ltd

REFERENCE BOOKS:

2. Jagger (Ed) ARM architectural reference manual, Prentice Hall
3. ARM assembly language an introduction by J. R. Gibson

INTRODUCTION TO SPEECH AND AUDIO PROCESSING 10TC6GE2SA

Objective:

5. Ability to apply the fundamentals of signal processing for speech and related applications
6. To emphasize the need for speech analysis and speech synthesis
7. To design and analyze problems in the area of application of speech enhancement, speech coding and speech recognition
8. To conduct laboratory experiments applying the concepts of speech processing on a speech/audio sample

UNIT I

[07 hours]

Production and classification of speech sounds: Introduction, mechanism of speech production. Acoustic phonetics: vowels, diphthongs, semivowels, nasals, fricatives, stops and affricates. DSP review. General discrete time model for speech production.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT II [08 hours]

Time-domain methods for speech processing: Time dependent processing of speech, short-time energy and average magnitude, short-time average zero crossing rate, Speech vs. silence detection, pitch period estimation using parallel processing approach, short-time autocorrelation function.

UNIT III [08 hours]

Frequency domain methods for speech processing: Introduction, definitions and properties: Fourier transforms interpretation and linear filter interpretation, sampling rates in time and frequency, Filter bank summation and overlap add methods for short-time synthesis of speech, sinusoidal and harmonic plus noise method of analysis/synthesis.

UNIT IV [08 hours]

Linear predictive coding of speech: Basic principles of linear predictive analysis, Solution of LPC equations, Prediction error signal, Frequency domain interpretation, Relation between the various speech parameters, Synthesis of speech from linear predictive parameters, Applications

UNIT V [08 hours]

Homomorphic speech processing: Introduction, Homomorphic systems for convolution, the complex cepstrum of speech, pitch detection, formant estimation. The homomorphic vocoder

Lab Experiments:

Basic audio experiments, Time domain methods, frequency domain based experiments, speech estimation, speech synthesis

Mini-project

TEXT BOOKS:

3. Digital processing of speech signals – L. R. Rabiner and R. W. Schafer, Pearson Education Asia, 2004.
4. Fundamentals of Multimedia – Z. Li and M.S. Drew, Pearson Education Ltd., 2004.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

REFERENCE BOOKS:

4. Discrete time speech signal processing– T. F. Quatieri, Pearson Education Asia, 2004.
5. Speech and audio signal processing: processing and perception of speech and music– B. Gold and N. Morgan, John Wiley, 2004.

OBJECT ORIENTED PROGRAMMING USING C++ AND JAVA

12ES6GE2CJ

(Not for those who have taken 12ES5GE1OP)

Objective:

3. Ability to apply the concepts of Object Oriented Programming with emphasis on C++
4. Ability to design and analyze real life applications by writing efficient programs to implement various modules considering constraints such as memory and portability.
5. Emphasize the importance of Classes, Objects, Data Abstraction, Data encapsulation, Overloading, Inheritance, Polymorphism and Reusability
6. Impart education to understand the need for life-long learning in the area of software engineering/programming

UNIT I

[10 hours]

Functions in C++: Function prototype, argument passing, recursion, inline functions, friend and virtual functions
Classes and objects: Class definition and declaration, member functions, static data members and member functions, arrays of objects, returning objects.

UNIT II

[10 hours]

Constructors and destructors: Constructors, parameterized constructors, multiple constructors in a class, copy constructor, dynamic constructors, and destructors.
Operator overloading and type



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

conversions: Overloading unary and binary operators, overloading using friends, rules for overloading.

UNIT III [10 hours]

Inheritance: Inheritance, public, private and protected inheritance. Private member inheritance. Types of inheritance: Single, Multilevel, multiple, hierarchical, hybrid. Pointers, virtual functions and polymorphism. Pointers, pointers to objects, this pointer, pointers to derived classes, virtual functions. Constructors in derived class. Managing console I/O operations: C++ streams, C++ stream classes, I/O operations, managing O/P with manipulators.

UNIT IV [12 hours]

Templates: Class templates, function templates, overloading template functions, member function templates and non type template arguments. Exception handling: Basics, throwing and catching mechanisms, rethrowing an exception, specifying exceptions.

UNIT V [10 hours]

Introduction Object Oriented Programming using Java – Object Oriented Concepts – Abstraction & Encapsulation – Relationships – Polymorphism – Interfaces and Packages

TEXT BOOKS:

6. Object oriented Programming with C++, -E Balagurusamy (TMH Publications, 4th edn)
7. Object oriented Programming in turbo C++, Robert Lafore (GALGOTIA Publications)

REFERENCE BOOK:

3. Let Us C++--Yashavanth P. Kanetkar (BPB Publications)
- Programming With C++-----Schaum'sseries (TMH Publications)
3. Programming With C++-----Schaum'sseries (TMH Publications)



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

BIOSENSORS

10ML6GE2SN

Objective

Understanding the components used for various biosensors and biosensor family. Principles and types of transducers. Helps to Know about the applications on clinical chemistry, healthcare and veterinary and agriculture, the usage of biosensors on environmental samples and application on Biochips and genomics. Understanding the principles of semiconductor electrodes used for preparation of biosensors and its different types and different photometric assay techniques.

UNIT I [10 hours]

Introduction: Introduction to Biosensors. Advantages and limitations, various components of biosensors, the growing of biosensor. The biosensor family, the biomolecule ingredients, proteins, enzymes complexes, enzymes kinetics, the proteins of the immune systems.

UNIT II [10 hours]

Transducers in biosensors: Various types of transducers, principles and applications - Calorimetric, optical, potentiometric / amperometric, conductrometric/resistometric, piezoelectric, semiconductor, impedimetric, mechanical and molecular electronics based transducers. Chemiluminescences - based biosensors.

UNIT III [10 hours]

Application and uses of biosensors: Biosensors in clinical chemistry, medicine and health care, biosensors for veterinary, agriculture and food. Biosensors for personal diabetes management, application of biosensors to environmental samples. Biochips and their application to genomics.

UNIT IV [12 hours]

Semiconductor electrodes: Measurement of H^+ , Ion selective interfaces, Ion selective electrodes, semiconductor electrodes, MIS structures, semiconductor solution interface, FET, chemical sensitive FETA (CHEMFETA), suspended gate field effect transistor, selectivity via pattern recognition, Ion selective FET (ISFET), reference FET, CHEMFET, assessment of CHEMFETS.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT V

[10 hours]

Photometric assay techniques: Energy transition, ultraviolet and visible absorption spectra, fluorescence and phosphorescence, infra Red transitions, light scattering, Raman scattering, applications of ultraviolet visible spectra, indicator linked bioassay, irrational spectroscopy, the optical transducer, wave guides in sensors, device construction, PH optical probes, light scattering analysis.

TEXT BOOKS:

2. Biosensors by Elizabeth A. Hall - Open University press, Milton Keynes.
3. Commercial Biosensors by Graham Ramsay, John Wiley and son, INC. (1998).

REFERENCE BOOKS:

3. Biosensors by Eggins
4. Biosensors edited by AEG CASS – OIRL press, Oxford University.
5. Transducers and Instrumentation by Murthy D V S. Prentice Hall, 1995

BIOSTATISTICS

10ML6GE2BS

UNIT I

[08 hours]

Introduction to Biostatistics: Introduction, Some basic concepts, Measurement and Measurement Scales, Simple random sample.

Descriptive Statistics: Introduction, Ordered array, Grouped data-frequency distribution, Descriptive statistics- measure of central tendency, Measure of dispersion, measure of central tendency computed from grouped data, variance and standard deviation-grouped data.

UNIT II

[10 hours]

Basic probability Concepts: Introduction, Two views of probability – Objective and Subjective, Elementary properties of Probability, Calculating the probability of an event, Bayes's Theorem, Screening Tests, Sensitivity, Specificity and Predictive Value Positive and Negative.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

Probability distribution: Introduction, Probability distribution of discrete variables, binomial distribution, Poisson distribution, Continuous probability distribution, Normal distribution and applications.

Sampling distribution: Introduction, Sampling distribution, Distribution of the sample mean, Distribution of the difference between two sample means, Distribution of the sample proportion, Distribution of the difference between two sample proportions.

UNIT III [10 hours]

Estimation: Introduction, Confidence interval for population mean, t-distribution, Confidence interval for difference between two population means, Population proportion and difference between two population proportions, determination of sample size for estimating means and proportions, confidence interval for the variance of normally distributed population and ratio of the variances of two normally distributed populations.

UNIT IV [12 hours]

Hypothesis Testing: Introduction, Hypothesis testing – Single population mean, difference between two population means, paired comparisons, hypothesis testing – single population proportions, single population variance, ratio of two population variance. Comparison of Means by three or more Samples (ANOVA) – Technique of Analysing Variance, Procedure for calculation of F-Statistic, ANOVA for one way classification, ANOVA for one way classification for samples of unequal size, ANOVA for two way classification, F-Analysis by coding method, Missing data formulation technique for Analysis of Variance.

UNIT V [12 hours]

Linear Regression and Correlation: Introduction, Regression model, Sample Regression equation, Evaluating the regression equation, Using the regression equation, Correlation model, Correlation coefficient, Some Precautions.

Chi-square Test: Introduction, Characteristics, Assumptions for validity, Applications of Chi-Square test: Goodness of fit, Test of Independence, Test of Homogeneity.

TEXT BOOKS:

3. BIOSTATISTICS-A Foundation for analysis in the Health Sciences by Warne W Daniel, 7th Edition, John Wiley & Sons Publication.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

3. Fundamentals of Biostatistics by Khan and Khanum, 2nd Edition,
Ukaaz Publications, 2004.

REFERENCE BOOKS:

3. Principles of Biostatistics – by Marcello Pagano and KimberleeGauvreu, Thomson
Learning Publication, 2006
4. Introduction to Biostatistics – by Ronald N Forthofer and EunSul Lee,
Academic Press, 1995.
- Basic Biostatistics and its Applications – by Animesh K Dutta, New
Central Book Agency, 2006.

BIOMEDICAL DSP
10IT6GE2MD (EXCEPT ML)

Objective:

Examining the full scope of digital signal processing in the biomedical field, this course provides the basics of digital signal processing as well as programming in MATLAB for designing and implementing digital filters for biomedical application. It provides a set of laboratory experiments that can be done using either an actual analog-to-digital converter , or taking the available data base to process the biomedical signals. The course emphasizes on feature extraction and classification of normal and abnormal features using different modeling techniques.

UNIT I

[10 hours]

INTRODUCTION TO BIOMEDICAL SIGNALS:

The nature of biomedical signals, the action potential, objectives of biomedical signal analysis, Difficulties in biomedical signal analysis, computer aided diagnosis. Basic Electrocardiography, ECG data acquisition, ECG lead system, ECG parameters and their estimation, The use of multi- scale analysis for parameter estimation of ECG waveforms, Arrhythmia analysis monitoring, long term continuous ECG recording, Neurological Signal Analysis The brain and its potentials, The electrophysiological origin of brain waves, The EEG signal and its characteristics, EEG analysis.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT II [09 hours]

Cardiological Signal Processing:

Adaptive Interference/Noise Cancellation, A review of Wiener filtering problem, Principle of an Adaptive filter, The steepest-descent algorithm, the Widrow-Hoff least mean square adaptive algorithm, Adaptive noise canceller, Cancellation of 60Hz interference in ECG, Canceling Donor-heart interference in Heart-transplant electrocardiography, Cancellation of ECG signal from the electrical activity of the chest muscles, canceling of maternal ECG in fetal ECG, Cancellation of High frequency noise in Electro-surgery.

UNIT III [06 hours]

ECG Data Reduction Techniques

Direct data compression techniques, Direct ECG data compression techniques, Transformation compression techniques, Transformation compression techniques, other data compression techniques, Data compression techniques comparison.

UNIT IV [06 hours]

LINEAR PREDICTION THEORY

The Autoregressive (AR) method, Recursive estimation of AR parameters, Spectral error measure, Adaptive segmentation, Transient detection and elimination- the case of epileptic patients, overall performance. Sleep EEG. Data acquisition and classification of sleep stages, The Markov model and Markov chains, Dynamics of sleep-wake transitions, Hypnogram model parameters, Event history analysis for modeling sleep.

UNIT V [08 hours]

Prony's Method:

Exponential modeling, Exponential parameter estimation, The original Prony problem, Least squares Prony method, The covariance method of linear prediction, Prony's method in the presence of noise, clinical application of Prony's method.

Simulation EXPERIMENTS:

FIR filter Design, iir filter design , implementing Pan Tompkins algorithm, adaptive filters for cancelling different noise in ecg, AR prediction , Time frequency analysis for biomedical signals.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

TEXT BOOKS:

6. " Biomedical Signal Processing Principles and Techniques" , by D C Reddy, The McGraw- Hill publications.
7. " Biomedical Signal Analysis a case study approaches" , by Rangaraj M. RangayyanThe John Wiley publications

REFERENCE BOOK:

" Biomedical Digital Signal Processing" , Willis J. Tompkins, The Prentice Hall of India publications.

MEMS TECHNOLOGY

12IT6GE2MT

UNIT I [12 hours]

MEMS: MICRO-FABRICATION, MATERIALS AND ELECTRO- MECHANICAL CONCEPTS

Overview of micro fabrication – Silicon and other material based fabrication processes –

Concepts: Conductivity of semiconductors-Crystal planes and orientation-stress and strain- flexural beam bending analysis-torsional deflections-Intrinsic stress- resonant frequency and quality factor.

UNIT II [08 hours]

ELECTROSTATIC SENSORS AND ACTUATION

Principle, material, design and fabrication of parallel plate capacitors as electrostatic sensors and actuators-Applications

UNIT III [10 hours]

THERMAL SENSING AND ACTUATION

Principle, material, design and fabrication of thermal couples, thermal bimorph sensors, thermal resistor sensors-Applications.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT IV [10 hours]

PIEZOELECTRIC SENSING AND ACTUATION

Piezoelectric effect-cantilever piezo electric actuator model-properties of piezoelectric materials- Applications.

UNIT V [12 hours]

CASE STUDIES

Piezoresistive sensors, Magnetic actuation, Micro fluidics applications, Medical applications, Optical MEMS, Introduction to NEMS.

REFERENCE BOOKS:

3. Chang Liu, "Foundations of MEMS", Pearson International Edition, 2006.
4. Marc Madou , "Fundamentals of microfabrication",CRC Press, 1997.
5. Boston , "Micromachined Transducers Sourcebook",WCB McGraw Hill, 1998.
6. M.H.Bao "Micromechanical transducers : Pressure sensors, accelerometers and gyroscopes", Elsevier, Newyork, 2000.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

EMBEDDED SYSTEM DESIGN

12EE6GE3ED

Objectives:

- 2 Create an environment for the systematic and effective application of scientific principles to the efficient design and operation of computer-based structures, processes and systems.
- 3 Create a setting where students can identify deficiencies or weaknesses in an existing solution and try novel ideas to improve it.
- 4 Indulge the concept where the task of design is fundamental and central.
- 5 Prepare educational materials that have a great deal of content, while at same time teaching students to think and discover for themselves.
- 6 Further enhancement of this subject for students will be in the field of Robotics, navigation, missile, satellite launching, wireless communication, instrumentation controls and defense applications from which students are benefitted to the greater extent and they will be convinced that this subject plays a vital role for the future scope.

UNIT I [12 hours]

Concept of embedded system design: Internal Block Diagram, Components, classification,

skills required. Embedded Micro controller cores: Features, Architecture and block diagram of Motorola Controller (6808 or 6811). Embedded Memories ROM variants, RAM, Applications of embedded system: Examples of Embedded systems, SOC for cellular phones, Smart cards, etc.

UNIT II [09 hours]

Technical aspects of Embedded System: Interfacing between analog and digital blocks, Signal conditioning, digital signal processing, DAC & ADC interfacing, various signal conditioning circuits using DSP or Motorola Controller.

UNIT III [10 hours]

Interfacing Concepts: Sample & hold, multiplexer interface, Internal ADC interfacing with DSP or Motorola Controller, Data Acquisition System and Signal processing circuits, criteria in the selection of embedded system design, Design challenge, design technology, Software aspects of Embedded Systems.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT IV

[11 hours]

Software Design: Real time programming Languages, operating systems. Programming concepts and embedded programming in C, Scheduling algorithms such as Round Robin, Round Robin with interrupts, priority, pre-emptive, function queue-scheduling architecture, Real time OS architecture, and selection.

UNIT V

[10 hours]

Peripheral Interfacing: Introduction to RTOS, Subsystem interfacing with external systems such as, Serial I/O devices, Parallel port interfaces, Input switches, Key boards and Memory interfacing. 10 Hours

TEXT BOOKS:

4. " Embedded Microcomputer systems: Real time interfacing" - Valvano, J.W, Brooks/Cole, 2000
5. " Embedded System, Architecture, Programming and Design" - Raj Kamal TMH 2003.

REFERENCE BOOKS:

5. " A Unified Hardware/Software Introduction" -Frank Vahid/Tony Givargis, Wiley student edition 2002 .
6. Jane W.S., Liu, " Real time systems" , Pearson Education Asia Pub, 2004.
7. Motorola and Intel Manuals



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

ELECTRONIC INSTRUMENTATION

10EE6GE3EI (EE ONLY)

Objective

The measuring instruments play an important role for any circuit applications. The various types of electrical/mechanical measurements can be done using voltmeters, recorders, transducers, phase meters etc. The subject gives a detail study of various types of measurements needed and the type of instruments needed for the same. This would be useful to students to enhance their knowledge in this field.

UNIT I [11 hours]

Electric instruments for measuring basic parameters: Introduction, amplified DC meter, AC voltmeter using rectifiers, electronic multi meter, considerations in choosing an analog voltmeter, Q meter.

UNIT II [10 hours]

Strip Chart Recorders, Galvanometer type, Null type, X-Y recorders

Standard Signal Generator, AF sine and square wave generator, function generator, square and pulse generator .(block diagram description)

UNIT III [11 hours]

Transducers: classification of transducers, selecting a transducer, potentiometric transducer, LVDT, strain gauges types, Piezo electric transducers, problems.

UNIT IV [10 hours]

Field Strength Meter, Stroboscope Phase meter, Direct reading Impedance meter, LC bridge, R-X meter

UNIT V [10 hours]

Instrumentation Systems, interfacing transducers to electronic control and measuring systems, multiplexing.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

TEXT BOOKS:

- Modern Electronic Instrumentation and Measurement Techniques, Albert.D.Helfrick, William.D.Cooper, 3/e Pearson, PHI.
- Electronic Instrumentation, H.S. Kalsi, TMH.

REFERENCE BOOK:

A course in Electrical and Electronic Measurements and Instrumentation, A.K. Sawhney, 18th Edition, DhanpatRai and Co., New Delhi.

RENEWABLE ENERGY RESOURCES

10EE6GE3RE

UNIT I

[13 hours]

Introduction to energy sources, need for non-conventional energy sources

SOLAR ENERGY: Introduction, extra terrestrial and terrestrial solar radiation, Solar Constant, Basic Sun-Earth Angles – definitions and their representation, Solar Radiation Geometry (numerical problems), Estimation of Solar Radiation of Horizontal and Tilted Surfaces (numerical problems); Measurement of Solar Radiation Data – Pyranometer and Pyrhelimeter.

SOLAR- ELECTRIC CONVERSION SYSTEM: solar energy collection ,thermal energy transfer, thermal energy storage, energy conversion

Solar Thermal Systems: Solar Water Heaters (Flat Plate Collectors), Solar Cookers – Box type, concentrating dish type, Solar driers, Solar Still, Solar Furnaces, Solar Green Houses.

UNIT II

[07 hours]

Solar Electric Systems: Solar Thermal Electric Power Generation – Solar Pond and Concentrating Solar Collector (parabolic trough, parabolic dish, Central Tower Collector). Advantages and Disadvantages; Solar Photovoltaic – Solar Cell fundamentals, characteristics, classification, construction of module, panel and array. Photo-voltaic energy storage, Solar PV Systems – stand-alone and grid connected; Applications – Street lighting, Domestic lighting and Solar Water pumping systems. Central receiver systems, the Heliostats, satellite solar power systems.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT III

[09 hours]

Wind energy: Introduction, principles of wind power, wind turbine operation, site characteristics, variation of power output with wind speed, new developments: small machines, large machines.

UNIT IV

[12 hours]

Energy from oceans: Introduction, ocean temperature differences, the open or Claude cycle, modification of the open OTEC cycle, the closed or Anderson cycle, OTEC cycle, ocean waves, wave motion, energy and power from waves, wave-energy conversion by floats, high pressure accumulation wave machines, the tides, the simple single-pool tidal system, the modified single-pool tidal system, the two-pool tidal system biofouling, Advantages & Limitation of OTEC.

GEOTHERMAL ENERGY: Introduction, origin and types of geothermal energy, operational and environmental problems, vapor dominated systems, liquid dominated systems, (flashed steam, binary cycle, total flow concept)

UNIT V

[11 hours]

Energy storage: Energy storage systems, pumped hydro, compressed air storage, energy storage by (i) flywheels (ii) electrical battery (iii) super conducting magnet, (iv) latent heat (v) chemical reaction (vi) thermal sensing.

Emerging Technologies: Fuel Cell, Small Hydro Resources, Magneto Hydro Dynamic Generation, Hydrogen Energy, (Principle of Energy generation using block diagrams, advantages and limitations).

TEXT BOOK:

"Non-Conventional Sources of Energy" - 4th Edition, G.D.Rai, Khanna Publishers, New Delhi, 2007

REFERENCE BOOKS:

2. "Generation of electrical Energy" -B.R.Gupta-S.Chand& Company Ltd
3. "Non-Conventional Energy Resources" - Khan, B. H., TMH, New Delhi, 2006.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

REAL TIME EMBEDDED SYSTEMS

10TC6GE3RT

Objective

2. ability to apply knowledge of simple Mathematics like probability and Science in Telecommunication Engineering
3. ability to analyze computer controlled system operations & its application in a field Electronics and Communication (embedded system)
4. ability to identify the real time application and analyze its benefits in Electronics Engineering
5. recognize the need for real time systems in communication engineering and pursue life-long learning in it

UNIT I [10 hours]

INTRODUCTION TO REAL-TIME SYSTEMS: Definition of Real Embedded Systems, RTS Definition, Classification of Real-time Systems, Time constraints, applications, basic model, Characteristics, Classification of Real-time Systems, safety and reliability. modeling time constraints.

UNIT II [11 hours]

REAL TIME OPERATING SYSTEM CONCEPTS- features of RTOS, architecture of the kernel, tasks and task scheduler, task states, context switching, classification of task scheduling algorithm, clock driven scheduling, hybrid scheduler, event driven scheduling, Earliest Deadline First (EDF) scheduling, Rate Monotonic Algorithm(RMA)

UNIT III [11 hours]

HANDLING RESOURCE SHARING AND DEPENDENCIES AMONG REAL TIME TASKS: resource sharing among real time tasks, assigning priorities, Priority Structures, priority inversion, critical region, semaphores, shared data , signals, message queues, mailboxes , pipes, timers, memory management

UNIT IV [10 hours]

REAL TIME COMMUNICATION: I/O devices, timer & counting devices; watchdog timer, real time clock, serial communication protocols- I2C, CAN, USB buses, IEEE 1394 - Firewire; parallel communication protocols-ISA, PCI and PCI/X buses, ARM bus.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT V

[10 hours]

SURVEY OF COMMERCIAL RTOS AND CASE STUDIES: Windows as RTOS(Windows CE), RTLinux, POSIX, VXworks, MicroC/OS II, case study of digital camera hardware and software architecture, case study of an embedded system for a smart card, embedded system for automobile

TEXT BOOK:

13. Embedded Systems Architecture; Programming and Design - Rajkamal;second edition, Tata McGraw Hill Publications.
14. Real-time computer control – An Introduction – Stuart Bennet, 2nd Edn. Pearson Education. 2005
Embedded /real time Systems: concept, design & programming - DR K.V.K.K. Prasad-dream tech press

REFERENCE BOOKS:

- 2 An Embedded software primer-David E Simon; Addison Wesley; 2000
- 3 Real-Time Systems Design and Analysis--3rd Edition, Phillip A. Laplante. Apr 2004. Wiley-IEEE Press.
- 4 An Introduction to Real Time Systems-Raymond J.A. Buhr; Donald L. Bailey; Prentice Hall International; 1999.
4. "A Unified Hardware/Software Introduction" -Frank Vahid/Tony Givargis, Wiley student edition 2002 .



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

**AUTOMOTIVE EMBEDDED SYSTEMS DEVELOPMENT
TECHNOLOGY
12ES6GE3AE**

- UNIT I [10 hours]
Powertrain systems-Introduction to Automotive systems-Engine Management System, Transmission system-front, rear and 4-way drive, Manual/ Automatic transmission, Differential system
- UNIT II [10 hours]
Sensors and Actuators-Classification of sensors, Pressure sensors, Linear and Angle position sensors, flow sensors, Temperature, Heat and Humidity sensors, speed and acceleration sensors, exhaust Gas sensors, Actuators-types of Electromechanical Actuators, Automotive Actuators, fluid mechanical actuators.
- UNIT III [10 hours]
Body electronics-Comfort and Safety oriented Automotive Subsystems, Requirements, Modeling/Design of Automotive Body Electronics (Night Vision, Power windows, Power mirrors, Power Seat, Seatbelt Tensioners, Airbag, Cruise control, Lane departure warning, parking), Simulation based case studies.
- UNIT IV [10 hours]
Embedded System Communication Protocols- Introduction to control networking – Communication protocols in embedded systems – SPI, I2C, USB – Vehicle communication protocols – Introduction to CAN, LIN, FLEXRAY, MOST, KWP2000, IDB1394.
- UNIT V [12 hours]
Automotive System Development- Model based development of Automotive grade ECUs, AUTOSAR development standards, Value Engineering, Fundamentals of safety and hazard analysis (ISO26262), Fault diagnosis of automotive subsystem, SPICE Development standards, EMI-EMC consideration, overview of calibration development support subsystem



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

TEXT BOOK:

- 2 "Automotive Electrics Automotive Electronics", 5th edition, Robert Bosch GmbH, Wiley publications.
- 3 "Automotive Electronics Handbook", Ronald K Jurgen, McGraw-Hill, Inc, 2nd edition.
- 4 "Automotive Embedded Systems Handbook", Nicolas Navet, Industrial Information Technology Series, CRC press.

FUNDAMENTALS OF IMAGE PROCESSING

12ES6GE3IP

UNIT I [07 hours]

Introduction to Image Processing: Introduction, Fundamental steps in DIP, Components of DIP system, A simple image formation model, Image sampling and quantization, Basic relationship between pixels, Arithmetic and Logical operations on images, Image file formats

UNIT II [08 hours]

Image Enhancement in Spatial Domain: Background, Point processing – Image negatives, Log transformations, Power law transformations, Contrast stretching, Gray level slicing, Bit plane slicing, Histogram processing – Histogram equalization, Local enhancement.

UNIT III [08 hours]

Image Enhancement in Frequency Domain: Background, Basic properties of the frequency domain, Basic filtering in the frequency domain, Basic filters and their properties, Smoothing frequency domain filters – Ideal lowpass filters, Butterworth lowpass filters, Gaussian lowpass filters, Sharpening frequency domain filters – Ideal highpass filters, Butterworth highpass filters, Gaussian highpass filters, Frequency domain filters, Homomorphic filtering.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT IV

[08 hours]

Image Restoration: Image degradation/restoration model, Inverse filter, Pseudo Inverse filter, Noise models, Restoration using spatial filtering – Mean filters, Geometric mean filters, Harmonic mean filters, Median filter, Max & min filters, Midpoint filter, Wiener filter, Constrained Least squares filter.

UNIT V

[08 hours]

Color Image Processing: Fundamentals of color image processing, Color models, Conversion of color models from one form to other form, Pseudo color image processing, Full color image processing, Color Image Quantization, Histogram of color Image.

Basic Image Transforms: Two-dimensional orthogonal unitary transforms, Properties of Unitary Transforms, K-L Transform.

Lab Experiments to be conducted using MATLAB:

5. Negative of an image
6. Arithmetic and Logical Operations on an image
7. Average of an image, Zooming and Pixel replication of an image
8. Bit – plane slicing of an image
9. Power – law transformation and Logarithmic transformation of an image
10. Histogram equalization and Contrast enhancement of an image
11. Basic transformation of an image
12. Gray level slicing of an image (With and Without Background Preservation)
13. Butterworth Low Pass and High Pass filters
14. Gaussian Low Pass and High Pass filters.
15. Inverse Filter and Pseudo Inverse filter.
16. Wiener filter.
17. Color Median filter
18. Color histogram equalization
19. Pseudo – color image processing



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

TEXT BOOK:

Digital Image Processing by Rafael C. Gonzalez & Richard E. Woods, Third Edition, Pearson education, 2009.

REFERENCE BOOKS:

Digital Image Processing by S.Jayaraman, S.Esakkirajan, T.Veerakumar, TMH, 2009.

DESIGN OF ANALOG & MIXED MODE VLSI CIRCUITS
10TC6GE3MM

Objective:

This course deals with the analysis and design of analog CMOS integrated circuits, emphasizing fundamentals as well as new paradigms. The objective is to develop both a solid foundation and methods of analyzing circuits by inspection so that the student learns what approximations can be made in which circuits and how much error to expect in each approximation.

UNIT I [10 hours]

Introduction to CMOS analog circuits

Basic MOS Device Physics: General considerations, MOS I/V Characteristics, second order effects, MOS device models.

UNIT II [10 hours]

Single stage Amplifier: CS stage with resistance load, diode connected load, current source load, triode load, CS stage with source degeneration, source follower, common-gate stage, cascade stage, choice of device models.

Differential Amplifiers: Basic difference pair, common mode response, Differential pair with MOS loads, Gilbert cell.

UNIT III [10 hours]

Operational Amplifiers: One Stage OP-Amp. Two Stage OP-Amp, Gain boosting, Common Mode Feedback, Slew rate, PSRR. Compensation of 2stage OP-Amp, Other compensation techniques



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT IV [10 hours]
Data converter fundamentals: Analog versus Digital Discrete Time Signals, Converting Analog Signals to Data Signals, Sample and Hold Characteristics, DAC Specifications, ADC Specifications, Mixed-Signal Layout Issues.

UNIT V [12 hours]
Data Converters Architectures: DAC Architectures, Digital Input Code, Resistors String, R-2R Ladder Networks, ADC Architectures, Flash, 2-Step Flash ADC, Pipeline ADC, Integrating ADC, Successive Approximation ADC.

TEXT BOOKS:

- 2 Design of Analog CMOS Integrated Circuits, B Razavi, First Edition, McGraw Hill, 2001
- 3 Design, Layout, Stimulation, R. Jacob Baker, Harry W Li, David E Boyce, CMOS Circuit, PHI Education, 2005

REFERENCE BOOKS:

CMOS Analog circuit Design Phillip. E. Allen, Douglas R. Holberg, Oxford University Press, 2002



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

12ML6GE3BC (4-0-0)

BIOMEDICAL CIRCUITS WITH VLSI

UNIT I [09 hours]

An Overview of VLSI: Complexity and design. Basic concepts, Physical structure of CMOS integrated circuits: Integrated circuit layers, MOSFETS.

UNIT II [10 hours]

Ideal switches and Boolean operation, MOSFETS and switches, Basic logic gates in CMOS, Complex logic gates in CMOS, Transmission gate circuits, CMOS layers, Designing FET array.

UNIT III [10 hours]

Electronic analysis of CMOS Logic gates, DC characteristics of the CMOS Inverter, Inverter Switching characteristics, Power dissipation, DC characteristics of NAND and NOR gates, NAND and NOR transient response, Analysis of complex logic gates, Gate design for transient performance.

UNIT IV [9 hours]

Fabrication of CMOS integrated circuits, Overview of silicon processing, material growth and deposition, lithography, CMOS process flow

UNIT V [14 hours]

CMOS Circuits for Biomedical Implantable Devices: Introduction, Inductive Link to Deliver Power to Implants, High Data rate Transmission Through Inductive links, Energy and Bandwidth Issues in Multi –Channel Biopotential Recordings. Self-Powered Sensors and circuits for biomechanical Implants: Introduction, Fundamentals of Piezoelectric Transduction and power Delivery. CMOS Circuits for Wireless Medical Applications: Introduction, Spectrum Regulations for Medical use, Integrated Receiver Architecture, Integrated Transmitter Architecture, Radio Architecture selection, System Budget calculations, Low noise Amplifier, Mixers, Polyphase Filter, Power Amplifier, PLL.

TEXT BOOKS:

5. JOHN P. UYEMURA, John Wiley , " Introduction to VLSI circuits and systems", Wiley 2001 edition. For Unit: 1, 2,3.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

2. Krzysztof Iniewski " VLSI circuits for Biomedical Applications" Artech House
2008 edition. For Unit 4 and 5.

REFERENCE BOOK:

Douglas A. Pucknell and Kamran Eshranghian, "Basic VLSI Design", PHI third edition,
2005.

Question Paper Pattern: Internal Choice in Unit 4 and Unit 5

REHABILITATION ENGINEERING

10ME6GE3RE

Objective:

To describe the role of occupational/physical/speech therapy, rehabilitation psychology and the multidisciplinary rehabilitation team in treating disabled patients in acute and chronic care settings. To comprehend rehabilitation framework of disease, functional impairment, activity limitation and barriers to social participation in approaching neurologic problems.

UNIT I

[12 hours]

Introduction to Rehabilitation & Rehabilitation Team: What is Rehabilitation, Epidemiology of Rehabilitation, Health, Levels of Prevention, Preventive Rehabilitation, Diagnosis of Disability, Functional Diagnosis, Importance of Psychiatry in Functional diagnosis, Impairment disability handicap, Primary & secondary Disabilities, Effects of prolonged inactivity & Bed rest on body system.

Rehabilitation Team: Classification of members, The Role of Physiatrist, Occupational therapist, Physical therapist, Recreation therapist, Prosthetist-Orthotist, Speech pathologist, Rehabilitation nurse, Social worker, Corrective therapist, Psychologist, Music therapist, Dance therapist & Biomedical engineer.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT II [10 hours]

Therapeutic Exercise Technique

Co-ordination exercises, Frenkels exercises, Gait analyses-Pathological Gaits, Gait Training, Relaxation exercises-Methods for training Relaxation, Strengthening exercises-Strength training, Types of Contraction, Mobilisation exercises, Endurance exercises.

UNIT III [10 hours]

Principles in Management of Communication

Impairment-introduction to communication, Aphasia, Types of aphasia, Treatment of aphasic patient, Augmentative communication-general form of communication, types of visual aids, Hearing aids, Types of conventional hearing aid, Writing aids.

UNIT IV [10 hours]

Orthotic Devices in Rehabilitation Engineering

General orthotics, Classification of orthotics-functional & regional, General principles of Orthosis, Biomechanics of orthoses, merits & demerits of orthotics, Material design consideration in orthotics, Calipers-FO, AFO, KAFO, HKAFO. Spinal Orthosis, Cervical, Head cervical thoracic orthosis, Thoraco lumbar sacral orthosis, Lumbosacroorthosis, Splints-its functions & types.

UNIT V [10 hours]

Prosthetic Devices

Introduction, Partial Foot Prostheses- Foot-ankle assembly, Trans femoral Prostheses – Knee unit, Axis system, Friction Mechanisms, Extension aid, Stabilizers, Socket. Disarticulation Prostheses-Knee Disarticulation Prostheses, Hip Disarticulation Prostheses.

TEXT BOOK:

Rehabilitation Medicine By Dr. S. Sunder (Jaypee medical publications, New Delhi)

Physical Rehabilitation by Susan B O'Sullivan, Thomas J Schmitz. 5th edition



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

ROBOTICS

10IT6GE3RB

UNIT I [07 hours]

Introduction

Objectives, Classification of robots, Major components of robot, definitions: Kinematics, Controls, and actuators. Robot history, types and applications current and future with examples. Fixed and flexible automation

UNIT II [10 hours]

Robot Arm Kinematics

Introduction, The direct kinematics problem, Rotation Matrices, Composite rotation Matrix, Rotation matrix about arbitrary axis, Rotation matrix with Euler angle representation, Geometric interpretation of rotation matrix, Homogenous coordinates and transformation matrix, Geometric interpretation of Homogenous transformation matrices, Composite homogenous transformation matrices, Links, Joints, and their parameters, The Denavit - Hartenberg representation, Kinematic equation for manipulator, Other specifications of the locations of the end effectors, Inverse kinematics problem.

UNIT III [08 hours]

Control of Actuators

Objective, Motivation, Closed loop control in position servo, Effect of friction and gravity, Adaptive control, Optimal control, Computed torque technique, Transfer function of single joint, Position control for single joint, Brief discussion on performance and stability criteria.

UNIT IV [10 hours]

Sensors

Sensor characteristics, Position sensors- potentiometers, Encoders, LVDT, Resolvers, Displacement sensor, Velocity sensor- encoders, tachometers, Acceleration sensors, Force and Pressure sensors – piezoelectric, force sensing resistor, Torque sensors, Touch and tactile sensor, Proximity sensors-magnetic, optical, ultrasonic, inductive, capacitive, eddy-current proximity sensors.

Hall Effect sensors, Binary sensors, Analog sensors, Force and Torque sensing, Elements of a Wrist sensor.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT V

[10 hours]

Vision and Processing :

Image acquisition, illumination Techniques, imaging geometry, some basic transformations, perspective transformations.

Camera model, camera calibration, stereo imaging, Higher-Level Vision: Segmentation, Edge Linking and Boundary detection, Thresholding. Region-oriented segmentation, Use of motion, Description, Boundary descriptors, Regional descriptors.

Mini project:

[7 Hours]

Discussion on DC motors with gears, Stepper motor, Servo motor, Mini projects using Basic sensors, 555 timers, Motors (DC motors with gears, Stepper motor, Servo motor)

A batch of TWO students are required to undertake a mini project to showcase the knowledge acquired during the course of this study.

Example topics :

2. Line follower robot
3. Obstacle avoiding robot
4. Face reorganization algorithm
5. MATLAB simulation or Use of robo sim
6. PCB design workshop (Using PCB design software)

Note: Carrying out small models / prototypes of projects are mandated which will carry a 20 percent weight in CIE

Project report has to be submitted with following chapters followed by a presentation

4. Abstract
5. Introduction
6. Block diagram
7. Materials used with detailed specification
8. Design and Design issues in detail
9. Model testing



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

TEXT BOOKS:

1. "Robotics – control, sensing, Vision and Intelligence", K.S.Fu, R.C.Gonzalez, C.S.G. Lee, McGraw Hill, 1987.
2. "Robotic Engineering" - Richard D Klafter, PHI

REFERENCE BOOKS:

1. "Introduction to Robotics Mechanics and control", John J. Craig, 2nd Edition, Pearson education, 2003

OPTICAL INSTRUMENTATION

12IT6GE3OI

Objectives:

This course will provide the student with a fundamental understanding of optical system design and instrumentation. The course begins with the foundations of geometrical optics, which includes the first-order properties of systems, and paraxial ray tracing, continues with a discussion of elementary optical systems, and concludes with an introduction to optical materials and dispersion. A special emphasis is placed upon the practical aspects of the design of optical systems.

Outcomes:

Upon the completion of this course student should be able to:

- o Describe the properties, characteristics and applications of lasers
- o Describe scientific and engineering applications of laser such as Doppler velocimeter, holography etc.
- o Understand the importance of integrated optics and its applications of fiber optical sensors.

UNIT I

[12 hours]

LASER TYPES AND CHARACTERISTICS : Principles, classification, construction of Ruby, He-Ne, Nd-YAG, semiconductor, Argon and Carbon dioxide lasers. Characteristics of stabilization, Q-switching and mode locking, frequency stabilization, line shape function, lasing threshold, application of lasers in engineering and medicine, safety with lasers.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT II [10 hours]

LASER INSTRUMENTS: laser interferometry, laser strain gauges, velocimetry, pulse echo technique, beam modulation telemetry and holography, application of holography, laser welding, laser machining and laser spectroscopy

UNIT III [08 hours]

OPTOELECTRONIC DEVICES AND COMPONENTS: Photo diodes, PIN diodes, solar cells, LED's phototransistors, opto-isolators, photocouplers.

UNIT IV [12 hours]

FIBER OPTICS: light Modulation schemes, optical fibers, intermodal dispersion, graded index fiber, low dispersive fibers Fiber losses, fiber materials, integrated optics, optical bistability, laser printing, optical multiplexers

UNIT V [10 hours]

OPTICAL FIBER SENSORS: Multimode passive and active fiber sensors, phasemodulated sensors, fiber optic gyroscope, Polarization: polarimetric sensors, polarization, and rotation sensors

TEXT BOOK:

1. "Optoelectronics", Wilson & Hawkes, Prentice Hall of India.
2. "Laser principles and applications", Wilson and Hawkes, Prentice Hall of India

REFERENCE BOOKS:

1. "Essentials of Opto Electronics with Applications", A.J. Rogers, CRC Press.
2. "Principles of Optical Communication & Opto Electronics", L Ravikumar, Bala N.Saraswathi, Lakshmi Publications.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

POWER SYSTEM OPERATION AND CONTROL

11EE7GE4PS

UNIT I [12 hours]

Power system Control And operating states, digital computer configuration, automatic generation control, area control error, Automatic load frequency control, Automatic load frequency control of single area systems, Speed governing systems Hydraulic valve actuator, Turbine generator response, Static performance of speed governor, Closing of ALFC loop, Concept of Control Area, Static response of primary ALFC loop.

UNIT II [10 hours]

ALFC of multi-area systems (POOL operation), the two-area system, modeling of the tie-line, Block diagram representation of Two-Area Systems, Static response of two area system and Tie line Bias Control, Automatic Voltage regulator: Basic generator control loops, Cross-coupling between control loops, Exciter types, Exciter modeling, Generator modeling, Static performance of AVR loop

UNIT III [10 hours]

Control of voltage and reactive power: Introduction, Generation and Absorption of reactive power, Relation between voltage, power and reactive power at a node, single machine infinite bus systems, methods of voltage control, sub synchronous resonance, voltage stability, voltage collapse, Overview of Economic Operation of Power systems without losses.

UNIT IV [10 hours]

Unit Commitment: Statement of the unit commitment problem, need and importance of unit commitment, Constraints in unit commitment, Unit commitment solution methods- Priority lists method, Forward Dynamic Programming method, Spinning reserve.

Power system security: Introduction, factors affecting power system security, an overview of security analysis, linear sensitivity factors, AC power flow methods, contingency evaluation, techniques for contingency evaluation



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT V

[05 hours]

System monitoring and control: Introduction, Energy Management systems, the basis of power system state estimation (PSSE), mathematical description of PSSE process, minimization technique for PSSE, Least square estimation, Error and detection in PSSE, System security and emergency control.

TEXT BOOKS:

1. Modern Power System Analysis- I J Nagarath and D P Kothari, TMH, 3rd Edition, 2003
2. Electrical Energy Systems Theory, O.J Elgerd, TMH,2008.
3. Power generation, operation and control- Allen J Wood & Woollenberg. John Wiley and Sons, Second Edition, 2009.
4. Electric Power Systems- B.M.Weedy and B.J. Cory, Wiley student edition, 1999
5. Computer Aided Power System Operation and Analysis- R.N. Dhar, Tata McGraw-Hill, 1987.

REFERENCE BOOKS:

1. Computer Aided Power System Analysis- G.L.Kusic, PHI,2010.
2. Power System Analysis, Operation and Control, Abhijit Chakrabarti and Sunita Halder, PHI, Second Edition, 2009



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

INDUSTRIAL DRIVES & APPLICATIONS

11EE7GE4ID

Objective

With the increase in the demand for power electronics devices, the use of power electronics, in drive circuits have increased. DC & AC motors can be controlled using highly compact power electronics circuits. This subject is versatile and can be useful to all students of the electrical cluster stream.

UNIT I [08 hours]

AN INTRODUCTION TO ELECTRICAL DRIVES & ITS DYNAMICS: Electrical drives. Advantages of electrical drives, Parts of electrical drives, choice of electrical drives, status of dc and ac drives, Dynamics of electrical drives, Fundamental torque equation, speed torque conventions and multiquadrant operation. Equivalent values of drive parameters, components of low torques, nature and classification of load torques, calculation of time and energy loss in transient operations, steady state stability, load equalization.

UNIT II [10 hours]

SELECTION OF MOTOR POWER RATING: Thermal model of motor for heating and cooling, Classes of motor duty, determination of motor rating.

INDUSTRIAL DRIVES: Rolling mill drives, cement mill drives, paper mill drives and textile mill drives.

UNIT III [12 hours]

DC MOTOR DRIVES:

(a) Starting braking, transient analysis, single phase fully controlled rectifier, control of dc separately excited motor, Single-phase half controlled rectifier control of dc separately excited motor.

(b) Three phase fully controlled rectifier control of dc separately excited motor, three phase half controlled controlled rectifier control of dc separately excited motor, multiquadrant operation of dc separately excited motor fed from fully controlled rectifier. Rectifier control of dc series motor, chopper controlled dc drives, chopper chopper control of separately excited dc motor. Chopper control of series motor.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT IV

[12 hours]

INDUCTION MOTOR DRIVES:

(a) Operation with unbalanced source voltage and single phasing, operation with unbalanced rotor impedances, analysis of induction motor fed from non-sinusoidal voltage supply, starting braking, transient analysis.

(b) Stator voltage control variable voltage frequency control from voltage sources, voltage source inverter control, closed loop control, current source inverter control, current regulated voltage source inverter control, rotor resistance control, slip power recovery, speed control of single phase induction motors.

UNIT V

[10 hours]

SYNCHRONOUS MOTOR DRIVES: Operation from fixed frequency supply, synchronous motor variable speed drives, and variable frequency control of multiple synchronous motors. Self- controlled synchronous motor drive employing load commutated thyristor inverter.

TEXT BOOK:

Fundamentals of Electrical Drives, G.K Dubey , Narosa publishing house, 2nd Edition,2002.

REFERENCE BOOKS:

1. Electrical Drives, N.K De and P.K. Sen- PHI, 2009.
2. A First Course On Electric Drives, S.K Pillai-Wiley Eastern Ltd 1990.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

LOW POWER MICROCONTROLLER

11TC7GE4MC

Objectives

- Ability to design, build, and debug simple microcontroller based systems by applying the knowledge of Mathematics and Engineering
- Ability to use a development environment that includes simulators, debuggers, cross compilers etc in the development of low power applications.
- Ability to identify, formulate and develop assembly and C code for mixed signal applications .
- Ability to work in a team and thereby learn how to cooperate in teams

UNIT I [08 hours]

Introduction - Motivation for MSP430 microcontrollers – Low Power embedded systems Main characteristics of a MSP430 microcontroller, Main features of the MSP430X RISC CPU architecture, Address space, Interrupt vector table, Flash/ROM, Information memory (Flash devices only), Boot memory (Flash devices only), RAM, Peripheral Modules, Special Function Registers (SFRs), Central Processing Unit (MSP430 CPU), Arithmetic Logic Unit (ALU), MSP430 CPU registers, Central Processing Unit (MSP430X CPU), MSP430X CPU registers.

UNIT II [08 hours]

Addressing modes & Instruction set- Double operand instructions, Single operand instructions, Program flow control – Jumps, Emulated instructions and programming.

UNIT III [08 hours]

Device Systems and Operating Modes- system reset, system clock, interrupt management, WDT, WDT+, Basic Timer, Capture/Compare blocks, Timer_A Interrupts, Timer_B special features, Real Time Clock (RTC).

UNIT IV [08 hours]

On-Chip Peripherals and General Purpose I/O- Hardware multiplier, ADC, DAC, SD16, LCD, DMA, Registers, Interruptible ports, Flashing LED, Blinking the LED, toggle the LED state by pressing the push button, Enable / disable LED blinking by push button.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT V

[07 hours]

Communications: Communications system model, Transmission mode, Synchronous and asynchronous serial communications, Serial Peripheral Interface (SPI) communication protocol, MSP430 communications interfaces, Case Studies of applications of MSP430

Lab Experiments

Basic debug introduction using CCE, eZ430-RF2500 Flashing LED, Memory clock with Basic Timer 1, Real Time Clock with Basic Timer 1, LCD message Display , Sample Temperature using SAR ADC10, Temperature data logger using ADC10 , Data acquisition using ADC12

MSP430-EXP430FG4618 Flashing LED, Voltage ramp generator, Data Memory transfer triggered by software , Multiplication without hardware multiplier, Flash memory programming with the CPU executing the code from flash memory.

REFERENCE BOOKS:

1. John H Davies, MSP430 Microcontroller Basics, Newnes Publications, 2008
2. Teaching MSP430, CD provided by Texas Instruments
3. Chris Nagy, Embedded systems Design using TI MSP430 Series, Newnes Publications, 2003



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

SOFTWARE DEFINED RADIO

11TC7GE4SR (Only EC, TC)

Objective:

This subject knowledge is helpful because in today's radio communication system where components that have been typically implemented in hardware such as mixers, filters, amplifiers, modulators/demodulators, detectors, etc. are instead implemented by means of software on a personal computer or embedded computing devices.

UNIT I

[10 hours]

Introduction

Software Based Radio, A Multi-Dimensional Model Sets the Stage, What is Software Based Radio , Software Defined Radio and Software Radio , Adaptive Intelligent Software Radio and Other Definitions , Functionality, Capability and SBR Evolution , Architectural Perspectives for a Software Based Radio , The Radio Implementer plane , The Network Operator plane, Software Radio Concepts , Adoption Timeframes for Software Based Radio, Realization of Software Based Radio Requires New Technology, Power/Performance/Price Limitations of Handsets Dictates Inflexible Networks, Regulatory Concepts Facilitate SBR Introduction

UNIT II

[12 hours]

Radio Frequency Translation for Software Defined Radio

Requirements and Specifications , Transmitter Specifications , Receiver Specifications, Operating Frequency Bands ,Receiver Design Considerations , Basic Considerations , Receiver Architectures , Dynamic Range Issues and Calculation , Adjacent Channel Power Ratio (ACPR) and Noise Power Ratio (NPR), Receiver Signal Budget , Image Rejection , Filter Functions within the Receiver , Transmitter Design Considerations , Filtering Analogies between Receiver and Transmitter ,Transmitter Architectures, Transmitter Efficiency and Linearity ,Candidate Architectures for SDR , Zero IF Receivers, Quadrature Local Oscillator, Variable Preselect Filters , Low IF Receivers

UNIT III

[10 hours]

Radio Frequency Front End Implementations for Multimode SDRs

Evolution of Radio Systems , Evolution of RF Front Ends – Superheterodyne Architecture , The AN2/6 Product Family – Dual Band, Six Mode , The AN2/6 Architecture , Lessons



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

Learned From the AN2/6 , Alternative RF Front End Architectures , Direct Conversion RF Front Ends , Pure Digital RF Front Ends , Analog Digital Combination Solutions , Directions for a Completely Successful SDR RF Front End

UNIT IV

[10 hours]

Data Conversion in Software Defined Radios

The Importance of Data Converters in Software Defined Radios , ADCs for SDR Base Stations ,

ADCs for SDR Handsets , DACs for SDR Applications , Converter Architectures ,Flash Converters , Multistage Converters , Sigma-Delta Converters , Digital-to-Analog Converters ,

Converter Performance Impact on SDR , Noise Sources – Impact on SDR Sensitivity , SNR of

Data Converter , Spurious Impact on Performance , Digital-to-Analog Converter Specification

UNIT V

[10 hours]

The Digital Front End: Bridge Between RF and Baseband Processing

The Front End of a Digital Transceiver, Signal Characteristics , Implementation Issues , The Digital Front End , Functionalities of the Digital Front End , The Digital Front End in Mobile Terminals and Base Stations , Digital Up- and Down-Conversion , Initial Thoughts, Theoretical Aspects , Implementation Aspects , The CORDIC Algorithm , Digital Down-Conversion with the CORDIC Algorithm , Digital Down-Conversion by Subsampling, Channel Filtering , Low-Pass Filtering after Digital Down-Conversion , Band-Pass Filtering before Digital Down-Conversion, Filterbank Channelizers , Sample Rate Conversion, Resampling after Reconstruction , Rational Factor SRC , Integer Factor SRC ,Concepts for SRC ,Systems for SRC ,Example ,Design Parameters ,Digital Down-Conversion ,Sample Rate Conversion , Channel Filtering

Mini Project:

At the end of the course students are expected to submit a miniproject on SDR implementation using Matlab /C/ LabVIEW /FPGA/DSP Processor/ARM Processor



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

TEXT BOOK:

Software Defined Radio: Dr Walter Tuttlebee,

Wiley REFERENCE BOOKS:

1. Bruce Fett, 'Cognitive Radio Technology', Newnes
2. 'Huseyin Arslan, 'Cognitive radio, software defined radio and adaptive wireless systems', Springer

BIOMETRICS

11ML7GE4BM

UNIT I [10 hours]

Introduction – Benefits of biometric security – Verification and identification – Basic working of biometric matching – Accuracy – False match rate – False non-match rate – Failure to enroll rate – Derived metrics – Layered biometric solutions.

UNIT II [10 hours]

Finger scan – Features – Components – Operation (Steps) – Competing finger Scan technologies – Strength and weakness. Types of algorithms used for interpretation. Voice Scan - Features – Components – Operation (Steps) – Competing voice Scan (facial) technologies – Strength and weakness.

UNIT III [10 hours]

Iris Scan - Features – Components – Operation (Steps) – Competing iris Scan technologies – Strength and weakness. Facial Scan - Features – Components – Operation (Steps) – Competing facial Scan technologies – Strength and weakness.

UNIT IV [10 hours]

Other physiological biometrics – Hand scan – Retina scan – AFIS (Automatic Finger Print Identification Systems) – Behavioral Biometrics – Signature scan- keystroke scan.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT V

[12 hours]

Biometrics Application – Biometric Solution Matrix – Bio privacy – Comparison of privacy factor in different biometrics technologies – Designing privacy sympathetic biometric systems. Biometric standards – (BioAPI , BAPI) – Biometric middleware.

Biometrics for Network Security: Statistical measures of Biometrics. Biometric Transactions.

TEXT BOOKS:

1. Biometrics – Identity Verification in a Networked World – Samir Nanavati, Michael Thieme, Raj Nanavati, Wiley India Pvt Ltd, 2002
2. Biometrics for Network Security- Paul Reid, Pearson Education, 2004.

REFERENCE BOOKS:

1. Biometrics- The Ultimate Reference- John D. Woodward, Jr. Wiley Dreamtech.
2. Biometric Systems Technology, Design and Performance Evaluation, James Wayman, Anil Jain, Davide Maltoni and Dario Maio, Springer Publications.
3. Personal Identification in Networked Society, Jain, A.K.; R Bolle, Ruud M.; S Pankanti, Sharath, 1st ed. 1999. 2nd printing, 2006, Springer Publications.
4. Handbook of Biometrics, Jain, Anil K.; Flynn, Patrick; Ross, Arun A, Springer, 2008.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

INTRODUCTION TO AUDIO & VIDEO PROCESSING

11ML7GE4AV

UNIT I [12 hours]

Audio Engineering: Sound waves, Complex sounds, Audio frequency range, loudness, pitch, and decibels. Sound pick up devices (microphones): types: - condenser- carbon, piezoelectric – direction pattern-parameters of microphones: - frequency range-sensitivity-impedance- noise. Sound reproduction devices: types: - horn, cone –typical specifications- Acoustics of speech production and hearing. Recording of Sound: Magnetic recordingsystems –optical storage systems-Coding and decoding applied to CD – CD-R.

UNIT II [10 hours]

Video Engineering: Elements of Television System:- Basic Block Schematic of Monochrome TV Transmitter and receiver, Gross structure, flicker& interlaced scanning ,number of scanning lines. Horizontal and Vertical resolution, Resolution and Bandwidth. Composite video signal- Vertical and horizontal synchronization,Vestigial Sideband Transmission, transmission of Sound signal. Modulation Positive and Negative Modulationand its comparison - Picture tubes. Television Cameras, Working Principle and operation of CCD cameras.

UNIT III [12 hours]

Colour Television: Compatibility considerations, Colour response of human eye, three colour theory, additivemixing of colours, chromaticity diagram, Luminance and chrominance, Block schematic explanation of ColourTV Cameras. Colour difference signal and its generation. Colour signal transmission, Modulation of colourDifference signals and colour burst signal. Basic Colour Television Systems: PAL, NTSC and SECAM.-BlockSchematic, explanation and Comparison. Colour TV picture tubes: CRTs, LCD and Plasma displays.

UNIT IV [08 hours]

Audio and Video coding: Introduction to Audio Coding, Audio compression, MPEG – Block diagram of audio encoder and decoder, Digital Audio Broadcasting- Block schematic explanation.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT V

[10 hours]

Video coding and compression: Need for compression- video image representation – quantization of image data. Intra frame compression techniques: DPCM–DCT based transform coding, Motion Compensation –H261 videoconference coding standard-MPEG video compression- HDTV- DVB-T

TEXT BOOK:

1. RR Gulati, Monochrome and Colour Television, New Asian Age
2. Fred Halsal , Multimedia Communications ,Pearson Education

REFERENCE BOOKS:

Thomas Quatieri , Discrete Time Speech Signal Processing: Principles and practice , Pearson Education

MULTI MEDIA COMMUNICATION

12ES7GE4MC

Objective:

- Ability to analyze the classifications and applications of Multimedia and identify various communication modes and media types used in Multimedia.
- Ability to analyze various communication networks such as LANs, Ethernet, Token ring and Bridges.
- Ability to analyze various text representations used in Multimedia and design various text compression techniques.
- Ability to identify various image models used in Multimedia and design various image compression techniques such as GIF, TIFF and JPEG.
- Ability to analyze various audio processing methods such as synthesized audio and MIDI and video compression techniques such as MPEG and H.261.
- Ability to apply technical knowledge and use engineering tools necessary for engineering practice.
- Impart education to develop Engineering solutions with an awareness of industry concerns through implementation of a project.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT I [07 hours]

Fundamentals of Multimedia Communication: Introduction, multimedia information representation, multimedia networks: telephone networks, data networks, broadcast television networks, ISDNs, broadband multiservice networks, multimedia applications: interpersonal communications, interactive applications over internet, entertainment applications.

UNIT II [08 hours]

Multimedia Information Representation: Media types, communication modes, network types, multipoint conferencing: centralized, decentralized and hybrid modes, network QoS, basic digital principles for multimedia.

Multimedia Networks: Introduction to networks in multimedia domain, Local Area Networks, concept of Ethernet, Token ring.

UNIT III [08 hours]

Text Representation and Compression: Text representation, Unformatted text, Formatted text, Hypertext, Introduction to compression techniques in multimedia, Text compression principles, Entropy encoding, Source encoding, Transform encoding, Text compression principles: Static Huffman coding, Arithmetic coding, Basics of LZW coding, Brief overview of other text compression standards.

UNIT IV [08 hours]

Image Representation and Compression: Image representation, Graphics, Digitized documents, Digitized Pictures, Raster scan principles, Three color image capture methods, Image compression principles, Image compression techniques: Graphics Interchange Format, JPEG: Image Preparation, Block Preparation, DCT, Quantization, Entropy encoding, Frame builder, Basics of JPEG decoder, Introduction to TIFF and JPEG 2000.

UNIT V [08 hours]

Audio Compression: Introduction to audio compression, PCM Speech, CD quality audio, Synthesized audio, MIDI, Brief overview of various audio compression standards.

Video Compression: Introduction to Video compression: Broadcast TV, Color signals, Luminance and Chrominance, Signal bandwidth, digital video: 4:2:2 format, 4:2:0 format, HDTV format, Video compression techniques: Introduction to MPEG and Brief overview of other MPEG standards.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

MINI PROJECT:

Mini project would be preceded by lab sessions that would include discussion of prerequisites to undertake the project. The 2 lab sessions would include experiments based on image processing / speech processing using tools such as MATLAB / LABVIEW.

A batch of THREE students is required to undertake a mini project to showcase the knowledge acquired during the course of this study. The project may be pursued with respect to the following sub – domains:

1. Image processing techniques such as enhancement, restoration, segmentation etc.
2. Image compression techniques such as JPEG, JPEG 2000, TIFF etc.
3. Text processing techniques like Huffman coding etc.
4. Text Compression techniques such as LZW coding, ZIP, RAR etc.
5. Audio / Speech compression techniques.
6. Video processing / compression techniques such as MPEG etc.

Implementation of the project including the project report would carry 50% (i.e. 25 out of 50) of the CIE marks.

Project Report has to be submitted with the following chapters followed by demonstration:

1. Abstract
2. Contents
3. Introduction
4. Description of the Project
5. Source Code of the Project
6. Results (Simulation / Snapshots)
7. Conclusion and Future Enhancements
8. Bibliography.

TEXT BOOK:

Multimedia Communications: Applications, Networks, Protocols, and Standards – Fred Halsall, Pearson Education, Second Indian reprint 2002.

REFERENCE BOOK:

Data Compression: The Complete Reference – David Salomon, Springer, Fourth Edition, 2007.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

WIRELESS COMMUNICATION

11EC7GE4WC

Objectives

- This course introduces the student to the concepts of cellular communication.
- To enable the students to understand the various modulation techniques, propagation methods, coding and multiple access techniques used in wireless communication.
- Study the second generation digital cellular networks in detail.

UNIT I [08 hours]

Introduction: Application and requirements of wireless services, History, types of services, requirements for services, Economical and social aspects. Spectrum limitations, limited energy, user mobility.

UNIT II [12 hours]

The Cellular concept: System design fundamentals: Frequency reuse, Channel assignment strategies, Handoff strategies, Interference and system capacity, Trunking and Grade of service, Improving coverage and capacity in cellular system.

UNIT III [12 hours]

Mobile radio propagation: Large scale path loss – Introduction to Radio wave propagation, freespace propagation model, relating power to electric field, Reflection, Ground Reflection model, Diffraction, Scattering.

Small scale fading- small-scale multipath propagation, Impulse response model of a multipath channel, small scale multipath measurements, Parameters of mobile multipath channels, Types of small scale fading.

UNIT IV [10 hours]

Equalization and Diversity: Fundamentals of Equalization, Training a Generic Adaptive Equalizer, Equalizers in communication receiver, Survey of Equalization Techniques, Linear and non-linear equalization, Algorithms for Adaptive Equalization, Fractionally

Spaced equalizers, Diversity techniques, RAKE receivers.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

UNIT V

[10 hours]

Global System for Mobile communication: System overview, The air interface, Logical and physical channels, synchronization, coding , circuit switched data transmission, Establishing a communication and handoff, Services and billing.

TEXT BOOKS:

1. Wireless Communication- Andreas F Molish, Wiley Student, Second Edition (Units 1&5)
2. Wireless Communication- Principles and Practice, Theodore S Rappaport, Second Edition (Units 2, 3&4)

EMBEDDED SYSTEM DESIGN
11EC7GE4ES

Objectives

- Introduce to features that build an embedded system.
- To understand the interaction of the various components within embedded system and the techniques of interfacing between processors & peripheral device related to embedded processing.
- To understand the basic concepts of systems programming like operating system, assembler compilers etc and the management task needed for developing embedded system.

UNIT I

[10 hours]

Introduction to Embedded System:

Introduction to functional building blocks of embedded systems – Register, memory devices, ports, timer, and interrupt controllers using circuit block diagram representation for each category.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT II [08 hours]

Processor and Memory Organization:

Structural units in a processor; selection of processor & memory devices; shared memory; DMA; interfacing processor, memory and I/O units; memory management – Cache mapping techniques, dynamic allocation - Fragmentation.

UNIT III [10 hours]

Devices & Buses for Devices Network: I/O devices, timer & counting devices; serial communication using I2C, CAN, USB buses; parallel communication using ISA, PCI, PCI/X buses, arm bus; interfacing with devices/ports, device drivers in a system – Serial port & parallel port.

UNIT IV [12 hours]

I/O Programming Schedule Mechanism: Intel I/O instruction – Transfer rate, latency; interrupt driven I/O - Non-maskable interrupts; software interrupts, writing interrupt service routine in C & assembly languages; preventing interrupt overrun; disability interrupts. Multi threaded programming – Context switching, pre-emptive & non-pre-emptive

multitasking, semaphores. Scheduling – Thread states, pending threads, context switching, round robin scheduling, priority based scheduling, assigning priorities, deadlock, watch dog timers.

UNIT V [12 hours]

Real Time Operating System (RTOS):

Introduction to basic concepts of RTOS, Basics of real time & embedded system operating systems, RTOS – Interrupt handling, task scheduling.

TEXT BOOKS:

1. Rajkamal, 'Embedded System – Architecture, Programming, Design', Tata McGraw Hill, 2003.
2. Daniel W. Lewis 'Fundamentals of Embedded Software', Prentice Hall of India, 2004.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

REFERENCE BOOK:

- 1 David E. Simon, 'An Embedded Software Primer', Pearson Education, 2004.
- 2 Frank Vahid 'Embedded System Design – A Unified hardware & Software Introduction' John Wiley, 2002.
- 3 Sriram V. Iyer, Pankaj Gupte, 'Embedded Real Time Systems Programming', Tata McGrawHill,2004.
- 4 Steve Heath, 'Embedded System Design', II edition, Elsevier, 2003

DISTRIBUTED COMPUTING
11IT7GE4DC

Objectives:

This course is designed to provide clear understanding of fundamental concept and design principles that underlie a distributed computing system.

UNIT I [10 hours]

INTRODUCTION:

Scope, goals, motivation, historical development, architectural models, design issues.

NETWORKS & PROTOCOLS:

Computer network principles, local network technologies, protocols for distributed systems, asynchronous transfer mode network.

UNIT II [10 hours]

REMOTE PROCEDURE CALLING:

Introduction, characteristics of remote procedure calling, interface definitions, binding, the RPC software, and implementation of RPC with lightweight process.

UNIT III [11 hours]

SYNCHRONIZATION IN DISTRIBUTED SYSTEMS:

Clock synchronization, mutual exclusion, election Algorithm, dead lock in distributed systems.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

PROCESS AND PROCESSOR IN DISTRIBUTED SYSTEMS:

Threads, processor allocation, scheduling.

UNIT IV [11 hours]

DISTRIBUTED DATABASES:

Division of responsibilities, file service, access control, directory service, and implementation.

STRUCTURED DISTRIBUTED DATABASES:

Overview of client server, architecture, data fragmentation, replication and allocation techniques over processing.

UNIT V

[10 hours]

CASE STUDY:

Introduction, locus, sun network file system, Cambridge file server, Ameba, mach, Apollo domain.

TEXT BOOKS:

1. "Modern Operating Systems " , A S Tanenbaum PHI 1996
2. " Distributed systems, concepts and design " , George F Coulounis & Jeon dollimose

REFERENCE BOOK:

1. " Distributed computing systems, synchronization, control and communication" , Parkar & Venis J P; Academic press 1983
2. " Distributed data base principles and systems" , Ceri S & Pelagatt, Mc-Graw Hill 1984
3. " Distributed operating systems" , Pradeep K Sinha --PHI 1998.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

MEDICAL IMAGING SYSTEMS

11IT7GE4MI (EXCEPT ML)

Objective:

The course focuses in the area of Therapeutic instruments . The evolution of ultrasonic medical imaging, computerized tomography & NMR Scanners are introduced in the syllabus to provide an inner depth to these diagnostic equipments / instruments .

*development of prototype

*Applying this knowledge in the design of smart sensors with portable equipment.

UNIT I [10 hours]

X-RAYS: Interaction between X-Rays and matter, Intensity of an X-Ray, Attenuation, X-Ray Generation and Generators, Beam Restrictors and Grids, Intensifying screens, fluorescent screens and Image intensifiers.

UNIT II [09 hours]

COMPUTED TOMOGRAPHY: Conventional tomography, Computed tomography principle, Projection function Generations of CT machines, Electron beam CT, Reconstruction algorithms, Helical CT.

UNIT III [09 hours]

ULTRASOUND IMAGING: Acoustic propagation, Attenuation, Absorption and Scattering, Ultrasonic transducers, Arrays, A mode, B mode, M mode scanners, Tissue characterization, Color Doppler flow imaging.

UNIT IV [11 hours]

MAGNETIC RESONANCE IMAGING: Angular momentum, Magnetic dipole moment, Magnetization, Larmor frequency, Rotating frame of reference, Free induction decay, Relaxation times, Pulse sequences. Introduction to functional MRI.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT V

[13 hours]

THERMAL IMAGING: Medical thermography, Infrared detectors, Thermographic equipment, Pyroelectric vidicon camera.

RADIONUCLIDE IMAGING: Interaction of nuclear particles and matter, Nuclear sources, Radionuclide generators, Nuclear radiation detectors, Rectilinear scanner, scintillation camera, SPECT, PET.

TEXT BOOKS:

1. Principles of Medical Imaging- Kirk shung, Academic Press.
2. Handbook of Biomedical Instrumentation- Khandpur, Tata McGraw-Hill Publishing Company Ltd., 2nd Edition, 2003.

REFERENCE BOOK:

- 1, Medical Imaging Signals and Systems- Jerry L Prince and Jonathan M Links, Prenti Hall of India/Pearson Education.
2. Fundamentals of medical Imaging- Zhong Hicho and Manbir singh, John Wiley.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

ELECTRICAL POWER QUALITY

12EE7GE5PQ

UNIT I [09 hours]

Introduction to Power Quality; Definition Of Power Quality; Causes Of Disturbances In Power Systems; Need For Power Quality, Power Quality Evaluation Procedure.

UNIT II [09 hours]

Classification Of Power Quality Issues; Transients, Short Duration Voltage Variations, Long Duration Voltage Variations, Voltage Imbalance, Waveform Distortions, Voltage Fluctuations And Flicker, Power Frequency Variations.

UNIT III [12 hours]

Measures used for power quality; harmonics, average value of non sinusoidal waveform, RMS value of non sinusoidal waveform, form factor(FF), ripple factor(RF), harmonic factor(HF), lowest order harmonic(LOH), total harmonic distortion(THD), total inter harmonic distortion (TIHD), total sub harmonic distortion(TSHD), total demand distortion (TDD), distortion power (D).

UNIT IV [10 hours]

Power Quality Measurement Equipment; Types Of Instruments, Wiring And Grounding Testers, Multi-Meters, Digital Cameras, Oscilloscopes, Disturbance Analyzers, Spectrum Analyzers And Harmonic Analyzers, Flicker Meters, Smart Power Quality Meters, Transducer Requirements.

UNIT V [12 hours]

Overview Of Mitigation Methods; From Fault To Trip, Reducing The Number Of Faults, Reducing The Fault-Clearing Time, Changing The Power System, Installing Mitigation Equipment , Improving Equipment Immunity, Different Events And Mitigation Methods, Summary and future direction.

TEXT BOOKS:

1) Power quality in power systems and electrical machines- Ewald F Fuchs: Mohammad A S Masoum; First Indian Reprint 2009, Indian reprint ISBN: 978-81-312-2350-5; Academic Press-An imprint of Elsevier



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

2) Electrical power systems quality, Second Edition, Roger, C Dugan/Mark F
McGranaghan/Surya Santosa/H Wayne Beaty; Tata McGraw Hill Edition.

REFERENCE BOOK:

Understanding Power quality problem: voltage fags and interruptions by Mat H Bollen,
First Edition, IEEE Press

SWITCH MODE POWER SUPPLIES
11EE7GE5SP

UNIT I [10 hours]

INTRODUCTION TO DC-DC SWITCHED MODE CONVERTERS: Basic Topologies,
Buck, boost, buck-boost, and Cuk converters.

UNIT II [08 hours]

FULL BRIDGE DC-DC CONVERTER: Detailed theory, working principles, modes of
operation, with detailed circuits and wave forms, applications, merits and demerits
(Operation of the above converters is CCM mode only)

UNIT III [10 hours]

DC-AC SWITCHED MODE INVERTERS: Single-phase inverter, three phase inverters.
SPWM inverter, detailed theory, working principles, modes of operation with circuit
analysis, applications, merits and demerits, problems based on input output voltage
relationship.

UNIT IV [14 hours]

RESONANT CONVERTERS: Zero voltage and zero current switching, resonant switch
converters, and comparison with hard switching, switching locus diagrams, and
working principle.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

HIGH FREQUENCY INDUCTOR AND TRANSFORMERS: Design principles, definitions, comparison with conventional design and problems.(Examples of Inductor and Transformer design for forward and flyback converter)

UNIT V

[10 hours]

POWER SUPPLIES: Introduction, DC power supplies: fly back converter, forward converter, push-pull converter, half bridge converter, full bridge converter, AC power supplies: switched mode ac power supplies, resonant ac power supplies, bidirectional ac power supplies.

TEXT BOOKS:

1. Power Electronics- converters, application & design- Mohan N, Undeland T.M., Robins, W.P-John Wiley 1989
2. Power Electronics-Circuits, Devices, Applications- Rashid M.H.-3rd Edition, Prentice Hall India, 2008.
3. Power Electronics and A.C. Drives- Bose B.K.-Prentice Hall 1986.
4. Digital Power Electronics And Applications- Muhammad Rashid. first edition, 2005, Elsevier



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

EMC –EMI

11TC7GE5EM

Objective:

- ability to apply the knowledge of EMC/EMI to examples
- ability to apply the knowledge of electromagnetics and measurements to instruments
- ability to apply the knowledge of EMC standards and regulations
- ability to apply the knowledge of EMI control methods
- ability to design PCB considering trace routing, impedance control, decoupling, zoning and grounding

UNIT I [11 hours]

BASIC CONCEPTS:

Definition of EMC, EMI with examples, SMPS, UPS, Classification of EMC/EMI-CE,RE,CS,RS, Units of parameters, Sources of EMI, EMI coupling modes-CM,DM,ESD phenomena and effects, Transient phenomena and suppression

UNIT II [11 hours]

EMI MEASUREMENTS :

Basic principles of RE,CE, RS, CS measurements, EMI measuring instruments-Antennas, LISN, feed through capacitor, current probe, EMC analyzer, and detection technique, open area site, shielded anechoic chamber, TEM cell

UNIT III [10 hours]

EMC STANDARD AND REGULATIONS: National and international standardizing organizations,-FCC, CE, and RE standards, frequency assignment-spectrum conversation

UNIT IV [10 hours]

EMI CONTROL METHODS AND FIXES:

Shielding, grounding, bonding, filtering, EMI gasket, isolation transformer, optical isolator



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT V

[10 hours]

EMC DESIGN AND INTERCONNECTION TECHNIQUES:

Cable routing and connection, component selection and mounting, PCB design-Trace routing, impedance control, decoupling, zoning and grounding

TEXT BOOK:

1. Prasad Kodali.V - Engineering Electromagnetic Compatibility - S.Chand&Co - New Delhi - 2000
2. Clayton R.Paul - Introduction to Electromagnetic compatibility - Wiley & Sons - 1992

REFERENCE BOOKS:

1. Keiser - Principles of Electromagnetic Compatibility - Artech House - 3rd Edition - 1994
2. Donwhite Consultant Incorporate - Handbook of EMI / EMC - Vol I - 1985

SATELLITE COMMUNICATION

11TC7GE5SC

Objective:

- ability to apply the knowledge of Kepler's laws to satellite orbits
- ability to apply the knowledge of communication and control in satellite subsystems
- ability to design communication modules considering power, bandwidth, cost, environment and safety
- ability to identify, formulate and solve problems in satellite link

UNIT I

[11 hours]

OVER VIEW OF SATELLITE SYSTEMS: Introduction, frequency allocation, INTEL Sat, India in space. ORBITS: Kepler laws, orbital elements, orbit perturbations, inclined orbits, calendars, orbital plane and sun synchronous orbits, Geostationary orbit: antenna look angles, limits of visibility, earth eclipse of satellite, sun transit outage, launching orbits



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT II [11 hours]

PROPAGATION IMPAIRMENTS AND SPACE LINK: Introduction, atmospheric loss, ionospheric effects, rain attenuation, other impairments.

SPACE LINK: Introduction, EIRP, transmission losses, link power budget, system noise, CNR, uplink, down link, effects of rain, combined CNR

UNIT III [10 hours]

SPACE SEGMENT: Introduction, Power supply units, Attitude control, Station keeping, Thermal control, Telemetry tracking and command, Transponders, Antenna subsystem

UNIT IV [10 hours]

SATELLITE ACCESS: Pre-assigned FDMA, SCPC (spade system), TDMA, pre-assigned TDMA, demand assigned TDMA, down link analysis, comparison of uplink power requirements for TDMA & FDMA, On board signal processing, satellite switched TDMA.

UNIT V [10 hours]

SATELLITE SERVICES: DBS, orbital spacing, power ratio, frequency and polarization, transponder capacity, bit rates for digital TV, satellite mobile services, VSAT, RadarSat, GPS, orbcomm

TEXT BOOK:

Satellite Communications, Dennis Roddy, 4th Edition, McGraw-Hill International edition, 2006.

REFERENCE BOOKS:

1. Satellite Communications, Timothy Pratt, Charles Bostian and Jeremy Allnutt, 2nd Edition, John Wiley & Sons, 2003.
2. Satellite Communication Systems Engineering, W. L. Pitchand, H. L. Suyderhoud, R.A. Nelson, 2nd Ed., Pearson Education., 2007.
3. Satellite Communication Systems Engineering-Louis J. Ippolito Jr, Wiley Publishers.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

ASIC DESIGN

11ES7GE5AD

Objective:

The course deals with the study of the hardware structure, synthesis methods, design methodology and design flow from the application to ASIC chip.

Note : All Designs Will Be Based On VHDL

UNIT I [10 hours]

Introduction: Full Custom with ASIC, Semi custom ASICS, Standard Cell based ASIC, Gate array based ASIC, Channeled gate array, Channel less gate array, structured get array, Programmable logic device, FPGA design flow, ASIC cell libraries

UNIT II [10 hours]

Data Logic Cells: Data Path Elements, Adders, Multiplier, Arithmetic Operator, I/O cell, Cell Compilers

ASIC Library Design: Logical effort: practicing delay, logical area and logical efficiency logical paths, multi stage cells, optimum delay, optimum no. of stages, library cell design.

UNIT III [11 hours]

Low-level Design Entry: Schematic Entry: Hierarchical design. The cell library, Names, Schematic, Icons & Symbols, Nets, schematic entry for ASIC.S, connections, vectored instances and buses, Edit in place attributes, Netlist, screener, Back annotation

ASIC Construction

UNIT IV [11 hours]

Floor Planning : Physical Design, CAD Tools, System Partitioning, Estimating ASIC size, partitioning methods. Floor planning tools, I/O and power planning, clock planning,

UNIT V [10 hours]

Placement and Routing placement algorithms, iterative placement improvement, Time driven placement methods. Physical Design flow global Routing, Local Routing, Detail Routing, Special Routing, Circuit Extraction and DRC.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

TEXT BOOK:

M.J.S .Smith, - "Application - Specific Integrated Circuits" – Pearson Education, 2003

REFERENCE BOOKS:

1. Jose E.France, Yannis Tsividis, "Design of Analog-Digital VLSI Circuits for Telecommunication and Signal Processing", Prentice Hall, 1994.
2. Malcolm R.Haskard; Lan. C. May, "Analog VLSI Design - NMOS and CMOS" Prentice Hall, 1998.
3. Mohammed Ismail and Terri Fiez, "Analog VLSI Signal and Information Processing", McGraw Hill, 1994.

ADVANCED MEDICAL IMAGE PROCESSING

11ML7GE5IP

Objective:

The Subject aims to introduce advanced concepts and methodologies for digital image processing and implementing the various techniques of image processing to make the results (output images) more suitable than the original Bio-medical images.

UNIT I [12 hours]
MORPHOLOGICAL IMAGE PROCESSING: Preliminaries, Erosion, Dilation, Duality, Opening and Closing, The Hit-or-Miss Transformation, Some Basic Morphological Algorithms, Boundary Extraction, Hole Filling, Extraction of Connected Components, Convex Hull, Thinning, Thickening, Skeletons, Pruning, Morphological Reconstruction, Summary of Morphological Operations of Binary Images, Gray-Scale Morphology, Erosion and Dilation, Opening and Closing, Some Basic Gray-Scale Morphological Algorithms, Gray-Scale Morphological Reconstruction.

UNIT II [08 hours]
IMAGE SEGMENTATION: Fundamentals, Point, Line, and Edge Detection, Background, Detection of Isolated Points, Line Detection, Edge Models, Basic Edge Detection, More Advanced Techniques for Edge Detection, Edge Linking and Boundary Detection,



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

Thresholding, Foundation, Basic Global Thresholding, Optimum Global Thresholding Using Otsu's Method, Using Image Smoothing to improve Global Thresholding, Using Edges to improve Global Thresholding, Multiple Thresholds, Variable Thresholding, Multivariable Thresholding, Region- Based Segmentation, Region Growing, Region Splitting and Merging, Segmentation Using Morphological watersheds, Background, Dam Construction, watershed segmentation Algorithm, The Use of Markers, The Use of Motion in Segmentation, Spatial Techniques, Frequency Domain Techniques.

UNIT III

[10 hours]

REPRESENTATION AND DESCRIPTION: Representation, Boundary (Border) Following, Chain Codes, Polygonal Approximations Using Minimum-Perimeter Polygons, Other Polygonal Approximation Approaches, Signatures, Boundary Segments, Skeletons, Boundary Descriptors, Shape Numbers, Fourier Descriptors, Statistical Moments, Regional Descriptors, Some Simple Descriptors, Topological Descriptors, Texture, Moment Invariants, Use of Principal Components for Description Relational Descriptors.

UNIT IV

[10 hours]

OBJECT RECOGNITION: Patterns and Pattern Classes, Recognition Based on Decision-Theoretic Methods, Matching, Optimum Statistical Classifiers, Neural Networks, Structural Methods, Matching Shape Numbers, String Matching.

UNIT V

[12 hours]

WAVELETS AND MULTIREOLUTION PROCESSING: Image Pyramids, Sub band Coding, The Haar Transform, Multi resolution Expansions, Series Expansions, Scaling Functions, Wavelet Functions, Wavelet Transforms in One Dimension, The Wavelet Series Expansions, The Discrete Wavelet Transform, The Continuous Wavelet Transform. The Fast Wavelet Transform. Wavelet Transforms in Two Dimensions. Wavelet Packets

TEXT BOOK:

Digital Image Processing by RafaelC. Gonzalez & Richard E. Woods, Third Edition.
Pearson Education Inc.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

REFERENCE BOOKS:

1. Digital Image Processing using MATLAB by RafaelC. Gonzalez & Richard E. Woods, Second Edition.Pearson Education Inc.
2. Image Processing, Analysis andMachine-Vision by Milan Sonka,Vaclav Hlavac& Roger Boyle, Second Edition
3. Digital Image Processing by S Jayakumaran, S Esakkirajan, T Veerakumar, Tata McGraw Hill Education Private Ltd.

ADVANCED BIOMEDICAL DIGITAL SIGNAL PROCESSING 11ML7GE5SP

Objectives:

Understand the concepts of Discrete and continuous Random Variables, Probability Density Function and its types. To be able to understand the various measurement parameters based on signal processing concepts. Such as power spectral analysis on ECG,EMG,EEG signals.

UNIT I

[10 hours]

INTRODUCTION: Discrete and continuous Random variables, Probability distribution and density functions. Gaussian and Raleigh density functions, Correlation between random variables.

Stationary random process, Ergodicity, Power spectral density and autocorrelation function of random processes. Noise power spectral density analysis, Noise bandwidth, noise figure of systems.

UNIT II

[10 hours]

TIME SERIES ANALYSIS: Introduction to time series analysis, AR, MA and ARMA models, Parameter estimation of ARMA models (Maximum likelihood method), Process order estimation, Adaptive segmentation, autocorrelation measure (ACM) method, spectral error measure(SEM) method.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT III [08 hours]

SPECTRAL ANALYSIS: Introduction to spectral analysis, the PSD, Cross – Spectral Density and coherence functions, Linear filtering, the Wiener filter, Cepstral analysis. Homomorphic filtering

UNIT IV [12 hours]

SPECTRAL ESTIMATION: Introduction, estimation based on Fourier transform, the expected value of the Periodogram, weighted overlapped segment averaging (WOSA), smoothing of the Periodogram, estimation based on Maximum entropy method (MEM) and the AR method, the Moving average (MA) method, Autoregressive moving average (ARMA) methods, Prony's method, Maximum likely hood method (MLM), comparison of several methods.

UNIT V [12 hours]

WAVELETS: Introduction to Wavelets: Multi resolution, Formulation of Wavelet systems, The Scaling Functions, and scaling Coefficients, Wavelet and Wavelet Coefficients, Calculation of the Discrete Wavelet Transform, Wavelet-Based Signal Processing and Applications.

TEXT BOOKS:

1. Biomedical Signal Processing: Time & Frequency Analysis (Vol-1) by Arnon Cohen., CRC Press, 1986.
2. Introduction to Wavelets and Wavelet Transforms, Burrus, Gopinath and Gao, Prentice Hall, 1998.

REFERENCE BOOKS:

1. Biomedical Signal Analysis by Rangaraj M. Rangayyan –. IEEE Press, 2001.
2. Biomedical Signal Processing by MatinAkay, Academic, Press 1994
3. Wavelet Transforms by Raghuveer M. Rao and Ajit S. Bopardikar, Pearson, 1998.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

LOW POWER VLSI DESIGN

12EC7GE5LP (4-0-0)

Objectives

Low Power technology is the most needed technology of modern electronics. This course enables the student to understand the design challenges of low power techniques and its impact on low power technology.

UNIT 1

Introduction to Low power CMOS design:

Need for Low Power VLSI chips, Charging and discharging capacitance, Short circuit current in CMOS circuit, CMOS leakage current, Static current, Basic Principles of low power design, Low power figure of merit. 10 Hours

UNIT 2

Power Analysis:

Simulation Power Analysis: Spice circuit simulation, Discrete transistor modeling, Gate level logic simulation, architecture level analysis, Monte-Carlo simulation 10 Hours

UNIT 3

Probabilistic Power analysis: Random Logic signals, Probability and frequency, Probabilistic power analysis techniques, Signal entropy. 10Hours

UNIT 4

Low power circuit techniques:

Power consumption in circuits, Flip-flops and latches, logic, high capacitance nodes.

10 Hours

UNIT 5

Clock distribution and logic synthesis for low power:

Low power Clock distribution: Power dissipation in clock distribution, single driver Vs distributed buffers, Process variations in buffer and device sizing, Low power logic synthesis: Power estimation techniques, power minimization techniques. 12Hours



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19
(Autonomous College under VTU)

TEXT BOOK:

1. Practical Low Power Digital VLSI design ,Gary Yeap, Kluwer academic publishers, 1998.
2. Low Power design Methodologies ,Jan M Rabaey, Massoud Pedram, Kluwer academic publishers, 2002.

REFERENCE BOOKS:

1. Low Power CMOS VLSI circuit design, Kaushik Roy, Sharat C Prasad, Wiley Interscience publication, 2000.
2. Low Power Design in deep submicron Electronics, W. Nebel, J. Mermet, Kluwer academic publishers, 1997.

NETWORK SECURITY
11EC7GE5NS

Objective

This course focuses on communication security in computer systems and networks and aims at providing students with a comprehensive introduction to the field of network security and services that are most essential for secure communication over the net.

UNIT I

[12 hours]

Services, Mechanisms and Attacks, The OSI security Architecture, A model for network security. Symmetric Ciphers: Symmetric Cipher model, Substitution techniques, Transposition technique, Simplified DES, Data encryption Standard, The strength of DES, Differential and linear cryptanalysis, Block cipher design principles and modes of operation.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT II [10 hours]

Introduction to finite fields- Groups ,rings and fields, modular arithmetic, Euclid's Algorithm, Finite fields of the form $GF(p)$, Polynomial arithmetic, Finite Fields of the form $GF(2^n)$. Prime numbers, Fermat's and Euler's Theorems, Testing for primality, the Chinese Remainder Theorem, and Discrete logarithms.

UNIT III [10 hours]

Principles of Public key cryptosystems, The RSA algorithm, Key Management, Diffie-Hellman Key exchange, Elliptic Curve Arithmetic, Authentication functions, Digital signatures, Digital signature standard.

UNIT IV [10 hours]

Electronic Mail Security- Pretty Good Privacy, S/MIME Web security- Secure Electronic Transaction.

UNIT V [10 hours]

Intruders, Intruder detection, Password management, Viruses and related threats. Firewalls Design Principles, Trusted systems.

TEXT BOOK:

1. Cryptography and Network Security-Principles and Practice: William Stallings, Third Edition.

REFERENCE BOOKS:

1. Fundamentals of Network Security-Eric Maiwald, 2009 Edition, Information Security Series
2. Network Security-Private Communication in a public World:Charlie Kaufman, Radia Perlman, Mike Speciner, Second Edition



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

EMBEDDED SYSTEM & RTOS

11IT7GE5ES

UNIT I [10 hours]

INTRODUCTION: An Embedded System; Characteristics of Embedded Systems; Software embedded into a system; Real Time Definitions, Events and Determinism, Synchronous & Asynchronous Events, Determinism, Sequence Control, Loop control, Supervisory control,

Centralized computer control, Hierarchical and Distributed system, Human-computer interface, Benefits of computer control systems.

UNIT II [10 hours]

OPERATING SYSTEMS: Introduction, Real-time multi-tasking OS, Scheduling strategies, Priority Structures, Task management, Scheduler and real-time clock interrupt handles, Memory Management, Code sharing, Resource control, Task co-operation and communication, Mutual exclusion, Data transfer, Liveness, Minimum OS kernel, Examples.

UNIT III [12 hours]

REAL TIME SPECIFICATIONS AND DESIGN TECHNIQUE: Mathematical specifications, flow charts, structure charts, Finite state automata, data flow diagrams, Petri Nets, Warnier Orr Notation, State charts.

PROCESSOR AND MEMORY ORGANIZATION: Structural Units in a Processor; Memory Devices, Memory selection for an embedded system; Direct Memory Access, DMA controllers; Interfacing Processor, Memory and I/O Devices.

UNIT IV [10 hours]

INTERRUPT SERVICING (HANDLING) MECHANISM: Context and the periods for context switching; Deadline and interrupt latency. Language Features: Parameter passing, Recursion, Dynamic allocation, Typing, exception handling, abstract data typing.

REAL TIME KERNELS: Real Time and Embedded Operating Systems; Interrupt Routines in RTOS environment; co routines, Interrupt driven systems, Foreground/background systems, Full-featured Real Time Operating Systems.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT V

[10 hours]

INTER-PROCESS COMMUNICATION AND SYNCHRONIZATION OF PROCESSES:

Multiple processes in an application; Problem of sharing data by multiple tasks and routines; Inter Process Communication, Mailboxes, Critical Regions, Semaphores, Deadlock.

PROGRAMMING LANGUAGES AND TOOLS: DESIRED LANGUAGE CHARACTERISTICS: Data typing; Control Structures; Packages; Exception Handling; Overloading; Multitasking; Task Scheduling; Timing specification; Programming environments; Runtime support.

Lab Experiments will be conducted using low power Microcontroller MSP 430

TEXT BOOKS:

1. Embedded Systems Architecture; Programming and Design-Rajkamal; Tata McGraw Hill Publications.
2. Real-Time Systems Design and Analysis--3rd Edition, Phillip A. Laplante. Apr 2004. Wiley-IEEE Press.
3. Real - Time Computer Control- An Introduction – Stuart Bennet,, 2nd Edn. Pearson Education. 2005.

REFERENCE BOOKS:

1. Real Time Systems- C.M. Krishna, Kang G.Shin McGraw-Hill, 1997.
2. An Embedded software primer-David E Simon; Addison Wesley; 2000.
3. An Introduction to Real Time Systems-Raymond J.A. Buhr; Donald L. Bailey; Prentice Hall International; 1999.
4. Embedded Real Time system-Concepts, Design and Programming, Dr. K. V. K. K. Prasad Dream Tech Pres, New Delhi 2003.



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

COMPUTER COMMUNICATION NETWORKS

11IT7GE5CN (Except TC, EC)

Objective:

- To understand the state-of-the-art in network protocols, architectures, and applications.
- To understand network functional components and their interaction.

UNIT I [10 hours]

INTRODUCTION: Uses of computer networks, Data communication, Circuit Switching, Packet Switching, Network Models, Example Networks, Network standardization. Theoretical basics of data communication, Layered tasks, OSI Model, Layers in OSI model, Functions, TCP/IP Suite, Addressing.

UNIT II [12 hours]

DATA LINK CONTROL: Framing, Flow and error control, Protocols, Noiseless channels: Simplest protocol, Stop and wait protocol, Noisy channels: Stop and wait protocol ARQ, piggy backing, Go-Back-N ARQ, sliding window protocol, Selective repeat ARQ, HDLC, Point to point protocol.

Multiple accesses control: Random Access: ALOHA, CSMA, CSMA/CD, CSMA/CA, Controlled access: Reservation, Polling, and Token passing

UNIT III [10 hours]

MEDIUM ACCESS SUB LAYER:

Static and dynamic channel allocation, multiple access protocols, LAN/MAN technology, Bus/Tree, Star and Ring topologies, The ring topology, Medium access control protocols, MAC performance, LAN/MAN standards, IEEE 802.2, 802.3, 802.4, IEEE802.5, 802.6, 802.11, and 802.16, Blue tooth

UNIT IV [10 hours]

NETWORK LAYER: Unicast Routing Protocols, Multicast Routing protocols, Logical addressing, Ipv4, Ipv6 format & addressing, Transition from Ipv4 to Ipv6, Delivery, Forwarding,



B.M.S COLLEGE OF ENGINEERING, BANGALORE-19

(Autonomous College under VTU)

UNIT V

[10 hours]

TRANSPORT LAYER: Transport layer Process to process Delivery, UDP, TCP, SCTP, Congestion, Congestion Control, Examples, QOS, and Techniques to improve QOS.

APPLICATION LAYER: Client Server Model, Domain Name Space (DNS), Electronic mail, HTTP, world wide web (www)

TEXT BOOK:

1. Data communication and networking– Behrouz A. Forouzan, 4th Ed, TMH 2006.
2. William Stallings, Data and Computer Communications, Fifth edition, PHI, 1998.
3. Computer networks – Andrew. S. Tannenbaum

REFERENCE BOOKS:

1. Data communication and networking– Behrouz A. Forouzan, 3rd Ed, TMH 2006

