



B.M.S. COLLEGE OF ENGINEERING

(Estd 1946, Autonomous Institute under VTU)

Bull Temple Road, Bangalore - 560 019, Karnataka, India.

No. BMSCE/Ph.D. Admission /2024-25/July Session

Date: 23.07.2024

Admission to Ph.D. Programme (Full-Time/Part-Time) under Visvesvaraya Ph.D. Scheme

Digital India Corporation, Ministry of Electronics and Information Technology, Govt. of India
Implementation Order No: PhD-PT02/2024-01/51 & PhD-02/2024-02/72

Applications are invited from eligible and interested research candidates for the Ph.D programme (Full-Time/Part-Time) under “Visvesvaraya PhD Scheme for Electronics & IT- Phase-II”, by Digital India Corporation (DIC), MeitY, Govt. of India. B.M.S. College of Engineering will be the research centre and the degree will be awarded by Visvesvaraya Technological University, Belgaum, Karnataka.

Details of the Scheme: <https://www.myscheme.gov.in/schemes/vpseit>

VTU Ph.D Regulations: jnanashodha.vtu.ac.in/Regulations/PhD2023Regulation.pdf

Vacancy Table

Full-Time Ph.D.	02
Part-Time Ph.D.	01

The details of the scheme can be found at the end of this advertisement. The summary of the same is as below

Eligibility:

- The candidates shall possess a master’s degree in engineering/ technology or equivalent degree by the University, with a minimum CGPA of 6.75 out of 10 or 60% aggregate marks at the Master’s degree.
- GATE candidates will be given preference.
- For the part-time registration, the candidate should be working in an organization (Academic institution or industry).

Financial Assistance:

Full time Candidates:

- **Stipend/Fellowship (Full-time Ph.D.)** @ Rs. 38,750/- per month for first and second years
@ Rs. 43,750/- per month for Third, Fourth and Fifth Year
- **Reimbursement of Rent (RoR):** 24% (As per the prevailing norms & approval from DIC)
- **Research Contingency Grant:** Rs.1,20,000/Year
- **Support for attending International Conference:** Support up to 1.5 Lakhs from third year onwards
- **Visit to labs abroad:** From third year of PhD based on approval from DIC

Part time Candidates

- A one-time incentive of ₹ 3,00,000/- would be provided on successful completion of the Ph.D.

Important Dates

Online application registration process start date	29/07/2024
Last date for the submission of online Application form	15/08/2024
Web notification of the short-listed candidates for written test and/or Interview	16/08/2024
Dates of interview (for short-listed candidates)	24/08/2024
Announcement of Results	25/08/2024

Application Procedure:

- Interested and eligible candidates shall submit an online application using the form <https://tinyurl.com/phd-bmsce-application-dic> along with the following
 - Application Form: <https://tinyurl.com/phd-dic-application>
 - Enclosures to be submitted along with the application
 - Date of Birth (DOB) Certificate or High School (Class 10th) Certificate with DOB
 - SC/ST/OBC(NCL) Certificate, if applicable
 - Certificate of Physical Disability, if applicable
 - A valid GATE/CSIR/UGC score card, if applicable
 - Copies of mark/grade sheets of qualifying degree or higher
 - No Objection Certificate from employer (only for part-time). Full-time students currently working must produce NOC or Relieving Certificate from the employer at the time of admission.
 - Details of two referees (Name, Designation, Affiliation, Email ID)

Selection Procedure: Based on norms of the University and DIC, MeitY, GOI

Tentative Broad Research Areas: (Under Visvesvaraya Fellowship)

Department	Full-Time and Part-Time
ECE & allied	VLSI (ASIC, FPGA, Chip design, etc), Embedded Systems, Communications, Digital Signal Processing, Digital Image Processing, Instrumentation, Antennas, Power Electronics, Photonics, Photovoltaics, optical fibre communication and other related areas of Electronics and Communication Engineering

For any queries, the applicants can contact

Dr. Abhishek Appaji,

Head R&D

and

Nodal Officer (Visvesvaraya PhD Scheme for Electronics and IT),

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