

**B.M.S. COLLEGE OF ENGINEERING, BANGALORE-19**

(AUTONOMOUS COLLEGE UNDER VTU)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CLASSROOM: SEMINAR HALL

W.E.F. :16/12/2024

**1<sup>ST</sup> SEM PG- VLSI DESIGN & EMBEDDED SYSTEM**

|           | 8.55 AM<br>TO<br>9.50 AM                     | 9.50 AM<br>TO<br>10.45 AM | 10.45 AM<br>TO<br>11.15 AM | 11.15 AM<br>TO<br>12.10 PM                   | 12.10 PM<br>TO<br>1.05 PM | 1.05 PM<br>TO<br>2.00 PM | 2.00 PM<br>TO<br>2.55 PM        | 2.55 PM<br>TO<br>3.50 PM |
|-----------|--|---------------------------|----------------------------|--|---------------------------|--------------------------|---------------------------------|--------------------------|
| MONDAY    | AML & DL                                     | AML & DL                  | B<br>R<br>E<br>A<br>K      | DCLD   | PE 1                      | B<br>R<br>E<br>A<br>K    | DSD LAB (KSVP-L-303, SHH-L-301) |                          |
| TUESDAY   |  | PE2                       |                            | AES  | AML & DL                  |                          | DSD LAB (VMB-L-309, SHH-L-303)  |                          |
| WEDNESDAY |  | PE2                       |                            | AES  | PE1                       |                          | AES LAB KPL-L-301               |                          |
| THURSDAY  |  | PE2                       |                            | DCLD   | AES                       |                          |                                 |                          |
| FRIDAY    |  |                           |                            | PE1  | DCLD                      |                          |                                 |                          |
| SATURDAY  | INDUSTRY INTERACTION<br>/INVITED TALKS/ AATs |                           |                            | INDUSTRY INTERACTION<br>/INVITED TALKS/ AATs |                           |                          |                                 |                          |

| COURSE NAME                                     | CREDITS | FACULTY                                 |
|---|---------|---|
| Advanced Machine Learning and Deep Learning     | 3       | Dr. GEETISHREE MISHRA                   |
| Advanced Embedded Systems                       | 4       | Dr. K P LAKSHMI                         |
| Digital Circuits and Logic Design               | 3       | Dr. VEENA M B                           |
| Digital IC Design-PE1                           | 3       | Dr. VASUNDHARA PATEL K S                |
| Static Timing Analysis-PE2                      | 3       | Dr. R. JAYAGOWRI                        |
| Advanced Machine Learning and Deep Learning Lab | 2       | Dr. H H SURENDRA                        |
| Digital System Design Lab                       | 2       | Dr. VASUNDHARA PATEL K S/ Dr. VEENA M B |

*Hemavathi*  
TTO

*K.P.Lakshmi*  
**Professor & Head**  
Dept. of Electronics & Communication Engineering  
B.M.S. College of Engineering  
Bengaluru - 560 019.

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W.E.F. :16/12/2024

**1<sup>ST</sup> SEM PG- DIGITAL COMMUNICATION ENGINEERING**

|           | 8.55 AM<br>TO<br>9.50 AM                     | 9.50 AM<br>TO<br>10.45 AM | 10.45 AM<br>TO<br>11.15 AM | 11.15 AM<br>TO<br>12.10 PM                   | 12.10 PM<br>TO<br>1.05 PM | 1.05 PM<br>TO<br>2.00 PM | 2.00 PM<br>TO<br>2.55 PM        | 2.55 PM<br>TO<br>3.50 PM |
|-----------|--|---------------------------|----------------------------|--|---------------------------|--------------------------|---------------------------------|--------------------------|
| MONDAY    | AML & DL                                     | AML & DL                  | B<br>R<br>E<br>A<br>K      | DCLD   | PE 1(PG-CR-2)             | B<br>R<br>E<br>A<br>K    | DSD LAB (KSVP-L-303, SHH-L-301) |                          |
| TUESDAY   |  | PE2(PG-CR-2)              |                            | AES  | AML & DL                  |                          | DSD LAB (VMB-L-309, SHH-L-303)  |                          |
| WEDNESDAY |  | PE2(PG-CR-2)              |                            | AES  | PE1(PG-CR-2)              |                          | AES LAB SHH-L-309               |                          |
| THURSDAY  |  | PE2(PG-CR-2)              |                            | DCLD   | AES                       |                          |                                 |                          |
| FRIDAY    |  |                           |                            | PE1(PG-CR-2)                                 | DCLD                      |                          |                                 |                          |
| SATURDAY  | INDUSTRY INTERACTION<br>/INVITED TALKS/ AATs |                           |                            | INDUSTRY INTERACTION<br>/INVITED TALKS/ AATs |                           |                          |                                 |                          |

| COURSE NAME                                     | CREDITS | FACULTY                                 |
|---|---------|---|
| Advanced Machine Learning and Deep Learning     | 3       | Dr. GEETISHREE MISHRA                   |
| Advanced Embedded Systems                       | 4       | Dr. K P LAKSHMI                         |
| Digital Circuits and Logic Design               | 3       | Dr. VEENA M B                           |
| Data Networks -PE1                              | 3       | Dr. AKHILA S                            |
| Advanced Digital Communications-PE2             | 3       | Dr. KARTHIKEYA G S                      |
| Advanced Machine Learning and Deep Learning Lab | 2       | Dr. H H SURENDRA                        |
| Digital System Design Lab                       | 2       | Dr. VASUNDHARA PATEL K S/ Dr. VEENA M B |

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CLASSROOM: SEMINAR HALL

W.E.F. :16/12/2024

**1<sup>ST</sup> SEM PG- ELECTRONICS**

|           | 8.55 AM TO<br>9.50 AM                        | 9.50 AM TO<br>10.45 AM | 10.45 AM TO<br>11.15 AM | 11.15 AM TO<br>12.10 PM                      | 12.10 PM TO<br>1.05 PM | 1.05 PM TO<br>2.00 PM | 2.00 PM TO<br>2.55 PM           | 2.55 PM TO<br>3.50 PM |
|-----------|--|------------------------|-------------------------|--|------------------------|-----------------------|---------------------------------|-----------------------|
| MONDAY    | AML & DL                                     | AML & DL               | B<br>R<br>E<br>A<br>K   | DCLD   | PE 1                   | B<br>R<br>E<br>A<br>K | DSD LAB (KSVP-L-303, SHH-L-301) |                       |
| TUESDAY   |  | PE2                    |                         | AES  | AML & DL               |                       | DSD LAB (VMB-L-309, SHH-L-303)  |                       |
| WEDNESDAY |  | PE2                    |                         | AES  | PE1                    |                       | AES LAB SHH-L-309               |                       |
| THURSDAY  |  | PE2                    |                         | DCLD   | AES                    |                       |                                 |                       |
| FRIDAY    |  |                        |                         | PE1  | DCLD                   |                       |                                 |                       |
| SATURDAY  | INDUSTRY INTERACTION<br>/INVITED TALKS/ AATs |                        |                         | INDUSTRY INTERACTION<br>/INVITED TALKS/ AATs |                        |                       |                                 |                       |

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| Advanced Embedded Systems                       | 4       | Dr. K P LAKSHMI                         |
| Digital Circuits and Logic Design               | 3       | Dr. VEENA M B                           |
| Digital IC Design-PE1                           | 3       | Dr. VASUNDHARA PATEL K S                |
| Static Timing Analysis-PE2                      | 3       | Dr. R. JAYAGOWRI                        |
| Advanced Machine Learning and Deep Learning Lab | 2       | Dr. H H SURENDRA                        |
| Digital System Design Lab                       | 2       | Dr. VASUNDHARA PATEL K S/ Dr. VEENA M B |

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