B.M.S. COLLEGE OF ENGINEERING, BANGALORE-19

(AUTONOMOUS COLLEGE UNDER VTU)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VLSI Design and Embedded systems

PG FIRST SEMESTER CLASSROOM : PJ BLOCK 3RD FLOOR, T-303 W.E.F. 15/10/2026

	8.55 AM 9.50 AM	9.50 AM 10.45 AM	10.45 AM 11.15 AM	11.15AM 12.10 PM	12.10 PM 1.05 PM	1.05 PM 2.00PM	2.00 PM 2.55 PM	2.55 PM 3.50 PM	3.50PM 4.45PM
Monday		AD	BREAK	AE	DS	BREAK	DS LAB [L-301]		
Tuesday	AL-TUT	AL-TUT		AD	AD				
Wednesday		LP-VLSI		AE	DS		AE LAB [L-301]		
Thursday		AL		AE	LP-VLSI		LP-VLSI LAB [L-301]		
Friday	STA	STA		DS	AL		STA LAB [L-301]		
Saturday	INDUSTRY INTERACTION			REMEDIAL WORK					

CORE/ELECTIVE	Course code	Subjects	Coordinator	L	T	P
T/PCC	26MAT1PCAL	Applied Linear Algebra and Random Processes	Dr. Chandrashekar	2	1	0
PCC	26LVS1PCAD	Advanced Deep Learning for VLSI Design	Dr. GEETISHREE MISHRA	3	0	0
PCC	26LVS1PCDS	Digital System Design using FPGA	Dr. Veena M B	3	0	0
IPCC	26LVS1IPAE	Advanced Embedded System	Dr. Veena N. Hegde	3	0	1
PEC	26LVS1PEXX	Professional Elective -I (LP-VLSI)	Dr.Nikita Kar Chowdhury & Skillsoc LLP.	2	0	1
PEC	26LVS1PEXX	Professional Elective -II (STA)	Dr. R.Jayagowri	2	0	0
PEC	26LVS1PEXX	Professional Elective -II (STA Lab)	Skillsoc LLP.	0	0	1
PCCL	26LVS1PCLDS	Digital System Design Lab	Dr. Veena M B	0	0	1
NCMC		Research Methodology and IPR (Online)		0	0	0

COORDINATOR

HoD/ECE

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