


**B.M.S. COLLEGE OF ENGINEERING, BANGALORE-19**  
 (AUTONOMOUS COLLEGE UNDER VTU)  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**PG TIMETABLE**

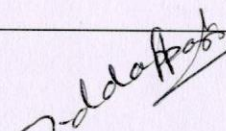
CLASSROOM: PG CR-1 SEMESTER:3RD

SECTION: PG-ELECTRONICS

W.E. F: 04/11/2024

	8.55 AM 9.50 AM	9.50 AM 10.45AM	10.45 11.15	11.15 AM 12.10 PM	12.10 PM 1.05 PM	1.05 PM 2.00 PM	2.00 PM 2.55 PM		
Monday			B R E A K			L U N C H  B R E A K			
Tuesday									
Wednesday									
Thursday									
Friday									AFPGA (6.00 PM TO 7.30 PM ONLINE)
Saturday		AFPGA (9.50AM TO 11.20AM)					AFPGA (11.40AM TO 1.10PM)		
<b>Advanced FPGA design-(3-0-0) JDR</b>									

  
 Hemavathi  
 PTO

  
 HOD  
**Professor & Head**  
 Dept. of Electronics & Communication Engineering  
 B.M.S. College of Engineering  
 Bengaluru - 560 019.




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CLASSROOM: PG CR-2 SEMESTER:3RD

SECTION: PG-DIGITAL COMMUNICATIONS

W.E.F: 04/11/2024

	8.55 AM 9.50 AM	9.50 AM 10.45AM	10.45 11.15	11.15 AM 12.10 PM	12.10 PM 1.05 PM	1.05 PM 2.00 PM	2.00 PM 2.55 PM		
Monday			B R E A K			L U N C H  B R E A K			
Tuesday									
Wednesday									
Thursday									
Friday									CB (6.00 PM TO 7.30 PM ONLINE)
Saturday		CB (9.50AM TO 11.20AM)					CB (11.40AM TO 1.10PM)		
An Insight to crypto system and Block chain CB (3-0-0)-SA									

 *Heavenly*  
TTO

*Ridda*  
HOD  
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**PG TIMETABLE**


CLASSROOM: PG CR-3

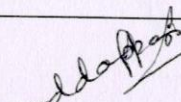
SEMESTER:3RD

SECTION: PG-VLSI

W.E.F: 04/11/2024

	8.55 AM 9.50 AM	9.50 AM 10.45AM	10.45 11.15	11.15 AM 12.10 PM	12.10 PM 1.05 PM	1.05 PM 2.00 PM	2.00 PM 2.55 PM		
Monday			B R E A K			L U N C H  B R E A K			
Tuesday									
Wednesday									
Thursday									
Friday									
Saturday		AFPGA (9.50AM TO 11.20AM)					AFPGA (11.40AM TO 1.10PM)		
<b>Advanced FPGA design-(3-0-0) JDR</b>									

  
 TPO *Heemavathi*

  
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