

B.M.S. COLLEGE OF ENGINEERING, BANGALORE-19
(AUTONOMOUS COLLEGE UNDER VTU)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
2ND SEM PG-ELECTRONICS

W.E.F. : 25/03/2026

CLASSROOM: SEMINAR HALL

MONDAY	8.55AM TO 9.50 AM	9.50 AM TO 10.45 AM	10.45A TO 11.15 AM	11.15 AM TO 12.10 PM	12.10PM TO 1.05 PM	1.05 PM TO 2.00 PM	2.00 PM TO 2.55 PM	2.55 PM TO 3.50 PM	
		ATD							
TUESDAY			B	PE-3-SCRIPTING LANGUAGE ATD	RTOS	B	PE-3-SCRIPTING LANGUAGE	PE-4-Automotive Electronics	
WEDNESDAY	VTV	LPVLSI	R	VTV LAB (L-301)		R			
THURSDAY		VTV	E	RTOS		E			
FRIDAY		PE-4-Automotive Electronics (T-302)	A	VTV LAB (L-301)		A			
SATURDAY		Ability/Skill Enhancement Course	K	RTOS LAB (L-301)		K			
Course code		COURSES (ELECTRONICS)			FACULTY				
MLEL201	Antenna Theory and Design			Dr. KARTHIKEYA GS					
MLEL204	Real Time Operating Systems			Dr. Veena N. Hegde					
MLEL205	Low power VLSI Design			Dr. VEENA.M B					
MLEL206	VLSI Testing and Verification			Dr. R. JAYAGOWRI					
MLEL215E	Scripting Language			Dr. M S Suma					
MLEL216E	Automotive Electronics			Dr. Veena N Hegde					
MLEL209	VLSI Testing and verification Lab			Dr. R. JAYAGOWRI					
MLEL258x	Ability/Skill Enhancement Course (Offline/Online)			Prof. Radha R C					

Heaven
TTO

Professor
Dept. of Electronics & Communication Engineering
B.M.S. College of Engineering
Bangalore - 560 019.

B.M.S. COLLEGE OF ENGINEERING, BANGALORE-19
(AUTONOMOUS COLLEGE UNDER VTU)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
2ND SEM PG- DIGITAL COMMUNICATION ENGINEERING

CLASSROOM: SEMINAR HALL

W.E.F. : 25/03/2026

	8.55AM TO 9.50 AM	9.50 AM TO 10.45 AM	10.45A TO 11.15 AM	11.15 AM TO 12.10 PM	12.10PM TO 1.05 PM	1.05 TO 2.00 PM	2.00 PM TO 2.55 PM	2.55 PM TO 3.50 PM
MONDAY	ATD			PE-3-SCRIPTING LANGUAGE		Multimedia (T-302)		
TUESDAY		Multimedia (T-302)	B	ATD	RTOS	B	PE-3-SCRIPTING LANGUAGE	
WEDNESDAY		Multimedia (T-302)	R	HIGH FREQ. LAB (L-302)		R	Cryptograpy and Network Security	
THURSDAY		Multimedia (T-302)	E	RTOS		E	Cryptograpy and Network Security	
FRIDAY	PE-4-Automative Electronics (T-302)		A	HIGH FREQ. LAB (L-302)		A		
SATURDAY	Ability/Skill Enhancement Course			RTOS LAB (L-301)		K		
Course code	COURSES (DIGITAL COMMUNICATION ENGINEERING)				FACULTY			
MLDC201	Antenna Theory and Design				Dr. KARTHIKEYA G S			
MLDC203	Cryptograpy and Network Security				Dr. MADHUSUDHAN K N			
MLDC205	Multimedia Communication				Dr. H H SURENDRA			
MLDC206	Real Time Operating Systems				Dr. Veena N. Hegde			
MLDC215E	Scripting Language				Dr. M S Suma			
MLDC216E	Automotive Electronics				Dr. Veena N. Hegde			
MLDCL207	High frequency component design Lab				Dr. KARTHIKEYA G S			
MLDCL257X	Ability / Skill Enhancement Course (Offline/Online)				Prof. Radha R C			

TTO

Hemanth

K.P.D.
PROFESSOR
HOD

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B.M.S. COLLEGE OF ENGINEERING, BANGALORE-19
(AUTONOMOUS COLLEGE UNDER VTU)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
2ND SEM PG- VLSI DESIGN & EMBEDDED SYSTEM

CLASSROOM: SEMINAR HALL

W.E.F. : 25/03/2026

DAY	TIME	COURSE	LAB	FACULTY
MONDAY	8.55AM TO 9.50 AM	Ability/Skill Enhancement Course		
	10.45A TO 11.15 AM			
TUESDAY	8.55AM TO 9.50 AM	Design of Analog and Mixed Mode VLSI Circuits	VTV LAB (L-301)	Dr. ARCHANA H R
				Dr. R. JAYAGOWRI
	10.45A TO 11.15 AM	Design of Analog and Mixed Mode VLSI Circuits (T-303)	RTOS	Dr. Veena N. Hegde
				Dr. VEENA M B
11.15 AM TO 12.10 PM	PE-3-SCRIPTING LANGUAGE	RTOS	Dr. M S Suma	
			Dr. MADHUSUDHAN K N	
WEDNESDAY	9.50 AM TO 10.45 AM	Design of Analog and Mixed Mode VLSI Circuits (T-303)	VTV LAB (L-301)	Dr. R. JAYAGOWRI
				Dr. MADHUSUDHAN K N
THURSDAY	10.45A TO 11.15 AM	Ability/Skill Enhancement Course		
FRIDAY	8.55AM TO 9.50 AM	Design of Analog and Mixed Mode VLSI Circuits	VTV LAB (L-301)	Dr. ARCHANA H R
				Dr. R. JAYAGOWRI
SATURDAY	10.45A TO 11.15 AM	Design of Analog and Mixed Mode VLSI Circuits	VTV LAB (L-301)	Dr. Veena N. Hegde
				Dr. VEENA M B
COURSES (VLSI DESIGN AND EMBEDDED SYSTEMS)	11.15 AM TO 12.10 PM	Scripting Language		Dr. M S Suma
				Dr. MADHUSUDHAN K N
Course code	12.10 PM TO 1.05 PM	Cryptograpy and Network Security		Dr. R. JAYAGOWRI
				Dr. R. JAYAGOWRI
MLVS201	1.05 TO 2.00	Design of Analog and Mixed Mode VLSI Circuits	RTOS LAB (L-301)	Dr. ARCHANA H R
				Dr. R. JAYAGOWRI
MLVS202	2.00 PM TO 2.55 PM	VLSI Testing and verification		Dr. Veena N. Hegde
				Dr. VEENA M B
MLVS204	2.55 PM TO 3.50 PM	Real Time Operating Systems		Dr. M S Suma
				Dr. MADHUSUDHAN K N
MLVS 205		LP VLSI Design		Dr. R. JAYAGOWRI
				Dr. R. JAYAGOWRI
MLVS215E		Scripting Language		Dr. M S Suma
				Dr. MADHUSUDHAN K N
MLVS216F		Cryptograpy and Network Security		Dr. R. JAYAGOWRI
				Dr. R. JAYAGOWRI
MLVS L209		VLSI Testing and verification Lab		Dr. M S Suma
				Dr. MADHUSUDHAN K N
MLVS 258x		Ability/Skill Enhancement Course (Offline/Online)		Dr. R. JAYAGOWRI
				Dr. R. JAYAGOWRI

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Dept of Electronics & C&I
B.M.S. College of Engineering
Bangalore

Professor in Charge