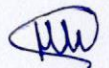


B.M.S. COLLEGE OF ENGINEERING, BANGALORE-19 (AUTONOMOUS COLLEGE UNDER VTU)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
CLASSROOM: EC304 CLASS: 6TH SEM SECTION: 'A' SEC W.E.F. 14-03-2024

DAY	8.00 AM 8.55 AM	8.55 AM 9.50 AM	9.50 AM 10.45 AM	10.45 11.15	11.15AM 12.10 PM	12.10 PM 1.05 PM	1.05 PM 2.00PM	2.00 PM 2.55 PM	2.55 PM 3.50 PM	3.50PM 4.45PM	4.45PM 5.40PM		
MON	PE-SV-KB-304 PE-SV-JDR-305 PE-ML-HNL-306 PE-ML-APP-SH PE-DS-KPL-307	MSD	CCN	B R E A K	A1 CCN LAB KS A2 MSD LAB MSD		B R E A K						
TUE					OE-IR-RKN-304 OE-IR-LS-305 OE-IR-HRA-307			IPL	MSD	A2 CCN LAB KB A3 MSD LAB PJ			
WED			ECS D		CCN	PE-SV-KB-304 PE-SV-JDR-305 PE-ML-HNL-306 PE-ML-APP-SH PE-DS-KPL-307							
THU	PE-SV-KB-304 PE-SV-JDR-305 PE-ML-HNL-306 PE-ML-APP-SH PE-DS-KPL-307	OE-IR-RKN-304 OE-IR-LS-305 OE-IR-HRA-307	ECS D		A3 CCN LAB KS A4 MSD LAB PJ								
FRI		IPL	MSD		CCN	ECS D		A4 CCN LAB BH A1 MSD LAB PJ					
SAT	PROJECT WORK/ INTERNSHIP WORK				PROJECT WORK/ INTERNSHIP WORK			PROJECT WORK/ INTERNSHIP WORK					
COMPUTER COMMUNICATION NETWORKS			Prof. K SUJATHA		ELECTRONICS AND COMMUNICATION FOR SUSTAINABLE DEVELOPMENT				Dr. JAYANTHI K MURTHY				
MIXED SIGNAL DESIGN			Prof. B HARSHITHA		INTELLECTUAL PROPERTY RIGHTS AND CYBER LAW				Dr. HEMAVATHI				
MACHINE LEARNING (PE)			Dr. H N LATHA/Prof. A P POOJA		SYSTEM VERILOG AND VERIFICATION (PE)			Dr. KIRAN BAILEY /Prof. JEERU DINESH REDDY					
DATA STRUCTURES USING C++ (PE)			Dr. K P LAKSHMI	INTRODUCTION TO ROBOTICS(OE)			Dr. RAJANIKANTH K N /Dr. LALITHA S/Dr. ARCHANA H R						

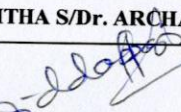

TTO


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CLASSROOM: EC304 CLASS: 6TH SEM SECTION: 'B' SEC W.E.F. 14-03-2024

DAY	8.00 AM 8.55 AM	8.55 AM 9.50 AM	9.50 AM 10.45 AM	10.45 11.15	11.15AM 12.10 PM	12.10 PM 1.05 PM	1.05PM 2.00PM	2.00 PM 2.55 PM	2.55 PM 3.50 PM	3.50PM 4.45PM	4.45PM 5.40PM	
MON	PE-SV-KB-304 PE-SV-JDR-305 PE-ML-HNL-306 PE-ML-APP-SH PE-DS-KPL-307	B1 CCN LAB KNM B2 MSD LAB RVM		B R E A K	IPL	CCN	B R E A K					
TUE		MSD	CCN		OE-IR-RKN-304 OE-IR-LS-305 OE-IR-HRA-307							
WED		B3 CCN LAB KNM B4 MSD LAB RVM			ECSD-307	PE-SV-KB-304 PE-SV-JDR-305 PE-ML-HNL-306 PE-ML-APP-SH PE-DS-KPL-307			MSD			
THU	PE-SV-KB-304 PE-SV-JDR-305 PE-ML-HNL-306 PE-ML-APP-SH PE-DS-KPL-307	OE-IR-RKN-304 OE-IR-LS-305 OE-IR-HRA-307	MSD-307		CCN	ECSD		B4 CCN LAB KB B1 MSD LAB RVM				
FRI		ECSD-307	IPL-307		B2 CCN LAB KNM B3 MSD LAB RVM							
SAT	PROJECT WORK/ INTERNSHIP WORK				PROJECT WORK/ INTERNSHIP WORK			PROJECT WORK/ INTERNSHIP WORK				
COMPUTER COMMUNICATION NETWORKS		Dr. MADHUSUDHAN K N			ELECTRONICS AND COMMUNICATION FOR SUSTAINABLE DEVELOPMENT					Dr. LALITHA S		
MIXED SIGNAL DESIGN		Dr. RAJATH VASUDEVAMURTHY		INTELLECTUAL PROPERTY RIGHTS AND CYBER LAW					Prof. SHAILA V HEGDE			
MACHINE LEARNING (PE)		Dr. H N LATHA/Prof. A P POOJA		SYSTEM VERILOG AND VERIFICATION (PE)			Dr. KIRAN BAILEY/Prof. JEERU DINESH REDDY					
DATA STRUCTURES USING C++ (PE)		Dr. K P LAKSHMI		INTRODUCTION TO ROBOTICS(OE)			Dr. RAJANIKANTH K N/Dr. LALITHA S/Dr. ARCHANA H R					

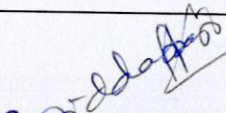

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
CLASSROOM: EC305 CLASS: 6TH SEM SECTION: 'C' SEC W.E.F. 14-03-2024

DAY	8.00 AM 8.55 AM	8.55 AM 9.50 AM	9.50 AM 10.45 AM	10.45 11.15	11.15AM 12.10 PM	12.10 PM 1.05 PM	1.05 2.00	2.00 PM 2.55 PM	2.55 PM 3.50 PM	3.50PM 4.45PM	4.45PM 5.40PM	
MON	PE-SV-KB-304 PE-SV-JDR-305 PE-ML-HNL-306 PE-ML-APP-SH PE-DS-KPL-307	ECSD	CCN	B R E A K	MSD	IPL	B R E A K	C2 CCN LAB HNL C3 MSD LAB HRA				
TUE		C3 CCN LAB BH C4 MSD LAB HRA			OE-IR-RKN-304 OE-IR-LS-305 OE-IR-HRA-307							
WED			MSD		CCN	PE-SV-KB-304 PE-SV-JDR-305 PE-ML-HNL-306 PE-ML-APP-SH PE-DS-KPL-307			C4 CCN LAB KB C1 MSD LAB HRA			
THU	PE-SV-KB-304 PE-SV-JDR-305 PE-ML-HNL-306 PE-ML-APP-SH PE-DS-KPL-307	OE-IR-RKN-304 OE-IR-LS-305 OE-IR-HRA-307	CCN		ECSD	MSD						
FRI		IPL	ECSD		C1 CCN LAB HNL C2 MSD LAB HRA							
SAT	PROJECT WORK/ INTERNSHIP WORK				PROJECT WORK/ INTERNSHIP WORK			PROJECT WORK/ INTERNSHIP WORK				
COMPUTER COMMUNICATION NETWORKS			Dr. POORNIMA G		ELECTRONICS AND COMMUNICATION FOR SUSTAINABLE DEVELOPMENT					Dr. H H SURENDRA		
MIXED SIGNAL DESIGN			Dr. ARCHANA H R		INTELLECTUAL PROPERTY RIGHTS AND CYBER LAW					Prof. ASHWINI V		
MACHINE LEARNING (PE)			Dr. H N LATHA/Prof. A P POOJA		SYSTEM VERILOG AND VERIFICATION (PE)			Dr. KIRAN BAILEY/Prof. JEERU DINESH REDDY				
DATA STRUCTURES USING C++ (PE)			Dr. K P LAKSHMI		INTRODUCTION TO ROBOTICS(OE)			Dr. RAJANIKANTH K N/Dr. LALITHA S/Dr. ARCHANA H R				

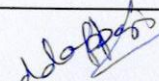
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CLASSROOM: EC305 CLASS: 6TH SEM SECTION: 'D' SEC W.E.F. 14-03-2024

DAY	8.00 AM 8.55 AM	8.55 AM 9.50 AM	9.50 AM 10.45 AM	10.45 11.15	11.15AM 12.10 PM	12.10 PM 1.05 PM	1.05 2.00	2.00 PM 2.55 PM	2.55 PM 3.50 PM	3.50PM 4.45PM	4.45PM 5.40PM	
MON	PE-SV-KB-304 PE-SV-JDR-305 PE-ML-HNL-306 PE-ML-APP-SH PE-DS-KPL-307	MSD-307	IPL-307	B R E A K	ECSD-307	CCN-307	B R E A K					
TUE			IPL		OE-IR-RKN-304 OE-IR-LS-305 OE-IR-HRA-307			D1 CCN LAB KNM D2 MSD LAB MSD				
WED					CCN-SH	PE-SV-KB-304 PE-SV-JDR-305 PE-ML-HNL-306 PE-ML-APP-SH PE-DS-KPL-307			ECSD	MSD	D2 CCN LAB BH D3 MSD LAB PJ	
THU	PE-SV-KB-304 PE-SV-JDR-305 PE-ML-HNL-306 PE-ML-APP-SH PE-DS-KPL-307	OE-IR-RKN-304 OE-IR-LS-305 OE-IR-HRA-307	ECSD-SH		D3 CCN LAB BH D4 MSD LAB MSD							
FRI		D4 CCN LAB KB D1 MSD LAB MSD			MSD	CCN						
SAT	PROJECT WORK/ INTERNSHIP WORK				PROJECT WORK/ INTERNSHIP WORK			PROJECT WORK/ INTERNSHIP WORK				
COMPUTER COMMUNICATION NETWORKS			Prof. B HARSHITHA		ELECTRONICS AND COMMUNICATION FOR SUSTAINABLE DEVELOPMENT			Prof. ASHWINI V				
MIXED SIGNAL DESIGN			Prof. MONICA SHARMA D		INTELLECTUAL PROPERTY RIGHTS AND CYBER LAW			Dr. SIDDAPPAJI				
MACHINE LEARNING (PE)		Dr. H N LATHA/Prof. A P POOJA			SYSTEM VERILOG AND VERIFICATION (PE)			Dr. KIRAN BAILEY/Prof. JEERU DINESH REDDY				
DATA STRUCTURES USING C++ (PE)		Dr. K P LAKSHMI	INTRODUCTION TO ROBOTICS(OE)			Dr. RAJANIKANTH K N/Dr. LALITHA S/Dr. ARCHANA H R						


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