

B.M.S. COLLEGE OF ENGINEERING, BANGALORE-19
(AUTONOMOUS COLLEGE UNDER VTU)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
TIMETABLE-W.E.F. 07/09/2026

Section	Day	Slot 1 8:00 AM - 8:55 AM	Slot 2 8:55 AM - 9:50 AM	9:50 - 10:20 AM	Slot 3 10:20 AM - 11:15 AM	Slot 4 11:15 AM - 12:10 PM	Slot 5 12:10 PM - 1:05 PM	1:05 - 2:00 PM	Slot 6 2:00 PM - 2:55 PM	Slot 7 2:55 PM - 3:50 PM	Slot 8 3:50 PM - 4:45 PM
ECE-3A	Monday	CEN [C-304]	MAT [C-304]	BREAK	DSD [C-301]	AEC LAB-A1-KV,A2-SKU,A3-APP A1,A2,A3-VENUE--FIRST FLOOR, PG BLOCK, EIE DEPT DSD LAB-A4-SB,A5-SRD A4, A5-VENUE-FIRST FLOOR, PG BLOCK, EEE DEPT.		LUNCH BREAK	SKILL LAB DS,KPL,SS,ST,SS-OPTIMUS VENUE – LAB1,SECOND FLOOR, PG BLOCK		
	Tuesday	DSD [C-303]	NAL [C-303]		CEN [C-306]	MAT TUT [C-306]	MAT TUT [C-306]		★★		DIP MATHS
	Wednesday	NAL [C-304]	MAT [C-304]		AEC [C-306]	NAL [C-306]	CEN [C-306]		SKILL LAB ST,SMR,SMR,MAS,FM -L AND T VENUE – LAB1,SECOND FLOOR, PG BLOCK		
	Thursday		AEC [C-306]		DSD [C-302]	AEC LAB-A4-ST,A5-ARK A4, A5-VENUE--LAB-305 DSD LAB-A1-KPK,A2-JMS,A3-MPS A1, A2,A3 -VENUE-LAB-306			TCE-A1-APP, A2-AK,A3-SKU,A4-ED,A5-LS VENUE-LAB2, SECOND FLOOR, PG BLOCK		
	Friday		AEC [C-306]		MAT [C-302]	VERILOG LAB-A1-SK,A2-RCR,A3-KPK,A4-KB,A5-NSR VENUE-LAB1, SECOND FLOOR, PG BLOCK					
	Saturday	SKILL LAB DS,BU,MSD,PJ,PJ-L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK				SKILL LAB DS,BU,NKC,KPK,HRA -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK DIP MATHS (11.30 AM TO 1.30 PM)			SKILL LAB MAS,ED,MAS,NKC,ED -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK		
	Course Code	26EC3ASMAT	26EC3IPAEC	26EC3IPDSD	26EC3PCNAL	26EC3PCCEN	26EC3AETCE	26EC3PLHDL	26EC3SDSL1		
	Course Name	Program Specific Mathematics	Analog Electronic Circuits	Digital System Design	Network Analysis	Control Engineering	Technical Communication for Engineers	Verilog for System Design	Skill Lab-1	DIP MATHS	
	Faculty	Prof. Rahul R	Dr. K VIJAYALAKSHMI	Dr. HARISH V MEKALI	Prof. K Poornima Kamath	Dr. Maligi Anantha Sunil				Dr. AMBIKA	

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Prof. P. D. & Head
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TIMETABLE-W.E.F. 07/09/2026

TIME TABLE-W.E.F. 07/09/2026

Section	Day	Slot 1 8:00 AM - 8:55 AM	Slot 2 8:55 AM - 9:50 AM	9:50 - 10:20 AM	Slot 3 10:20 AM - 11:15 AM	Slot 4 11:15 AM - 12:10 PM	Slot 5 12:10 PM - 1:05 PM	1:05 - 2:00 PM	Slot 6 2:00 PM - 2:55 PM	Slot 7 2:55 PM - 3:50 PM	Slot 8 3:50 PM - 4:45 PM
ECE-3B	Monday	DSD [C-307]	CEN [C-307]	BREAK	MAT [C-303]	VERILOG LAB-B1-JP, B2-KPK, B3-SK, B4-NSR, B5-KB VENUE – LAB1, SECOND FLOOR, PG BLOCK		LUNCH BREAK	SKILL LAB DS,KPL,SS,ST,SS-OPTIMUS VENUE – LAB1,SECOND FLOOR, PG BLOCK ★★★DIP MATHS		
	Tuesday	AEC [C-306]	DSD [C-306]		NAL [C-305]	MAT(TUT) [C-305]	MAT(TUT) [C-305]		SKILL LAB ST,SMR,SMR,MAS,FM -L AND T VENUE – LAB1,SECOND FLOOR, PG BLOCK DIP MATHS		
	Wednesday	AEC [C-306]	MAT [C-306]		NAL [C-303]	AEC LAB-B1-KV,B2-HRA,B3-JRK VENUE-B1,B2,B3 FIRST FLOOR, PG BLOCK, EIE DEPT. DSD LAB-B4-SB,B5-PMK VENUE-B4,B5FIRST FLOOR, PG BLOCK, EEE DEPT.			SKILL LAB DS,KPL,ST,SS,ST-OPTIMUS APP,GSK,APP-WEARABLE TECH ★★★ VENUE – LAB1 & LAB2, SECOND FLOOR, PG BLOCK		
	Thursday	NAL [C-304]	DSD [C-304]		CEN [C-303]	AEC LAB-B4-HRA,B5-JRK B4, B5- VENUE-FIRST FLOOR, PG BLOCK, EIE DEPT. DSD LAB-B1-SB,B2-PMK,B3-FM B1, B2-VENUE-FIRST FLOOR, PG BLOCK , EEE DEPT B3 - VENUE-FIRST FLOOR, PG BLOCK , EIE DEPT					
	Friday	CEN [C-302]	AEC [C-302]		MAT [C-307]	TCE-B1-AK, B2-ARS, B3-FM, B4-NKC, B5-APP VENUE – LAB2, SECOND FLOOR, PG BLOCK					
	Saturday	SKILL LAB DS,BU,MSD,PJ,PJ-L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK				SKILL LAB DS,BU,NKC,KPK,HRA -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK DIP MATHS (11.30 AM TO 1.30 PM)			SKILL LAB MAS,ED,MAS,NKC,ED -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK		
	Course Code	26EC3ASMAT	26EC3IPAEC	26EC3IPDS	26EC3PCNAL	26EC3PCCEN	26EC3AETCE	26EC3PLHDL	26EC3SDSL1		
	Course Name	Program Specific Mathematics	Analog Electronic Circuits	Digital System Design	Network Analysis	Control Engineering	Technical Communication for Engineers	Verilog for System Design	Skill Lab-1	DIP MATHS	
	Faculty	Dr. Rangaswamy	Dr. S KUMUDA	Prof. Shrinivas B	Prof. Shrisha M R	Dr. Beena Ullala Mata B N				Dr. AMBIKA	

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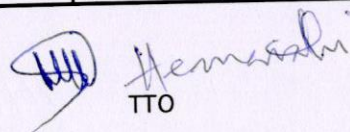
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ECE-3C	Monday			BREAK	DSD [C-ISE-503]	MAT (TUT) [C-ISE-503]	MAT (TUT) [C-ISE-503]	LUNCH BREAK	SKILL LAB DS,KPL,SS,ST,SS-OPTIMUS VENUE – LAB1,SECOND FLOOR, PG BLOCK * * DIP MATHS		
	Tuesday	AEC LAB-C1-SKS,C2-APP,C3-MSD C1,C2,C3- VENUE-FIRST FLOOR, PG BLOCK, EIE DEPT. DSD LAB-C4-PMK,C5-MTN C4,C5- VENUE-FIRST FLOOR, PG BLOCK , EEE DEPT.			NAL [C-304]	MAT [C-304]	AEC [C-304]		SKILL LAB ST,SMR,SMR,MAS,FM -L AND T VENUE – LAB1,SECOND FLOOR, PG BLOCK DIP MATHS		
	Wednesday	CEN [C-302]	NAL [C-302]		DSD [C-302]	AEC [C-302]	MAT [C-302]		SKILL LAB DS,KPL,ST,SS,ST-OPTIMUS APP,GSK,APP-WEARABLE TECH* * VENUE – LAB1 & LAB2, SECOND FLOOR, PG BLOCK		
	Thursday	TCE-C1-FM,C2-NKC,C3-GSK,C4-ED,C5-APP VENUE –LAB2, SECOND FLOOR, PG BLOCK			CEN [C-305]	DSD [C-305]	NAL [C-305]				
	Friday	MAT [C-305]	AEC [C-305]		CEN [PG Block C-5010]	AEC LAB-C4-KV,C5-ARK C4, C5-VENUE-LAB-305 DSD LAB-C1-MPS,C2-PMK,C3-MTN C1, C2,C3 -VENUE- LAB-306			VERILOG LAB-C1-GT,C2-KPK,C3-NSR,C4-KB,C5-SK VENUE –LAB1 SECOND FLOOR, PG BLOCK		
	Saturday	SKILL LAB DS,BU,MSD,PJ,PJ-L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK				SKILL LAB DS,BU,NKC,KPK,HRA -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK DIP MATHS (11.30 AM TO 1.30 PM)			SKILL LAB MAS,ED,MAS,NKC,ED -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK		
	Course Code	26EC3ASMAT	26EC3IPAEC	26EC3IPDSD	26EC3PCNAL	26EC3PCCEN	26EC3AETCE	26EC3PLHDL	26EC3SDSL1		
	Course Name	Program Specific Mathematics	Analog Electronic Circuits	Digital System Design	Network Analysis	Control Engineering	Technical Communication for Engineers	Verilog for System Design	Skill Lab-1	DIP MATHS	
	Faculty	Dr. Deepthi M R	Prof. POOJA A P	Dr.VEENA M B	Prof. Geeta	Prof. R C RADHA				Dr. AMBIKA	

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ECE-3D	Monday	MAT (TUT) [C-301]	MAT (TUT) [C-301]	BREAK	CEN [C-307]	AEC [C-307]	DSD [C-307]	LUNCH BREAK	SKILL LAB DS,KPL,SS,ST,SS-OPTIMUS VENUE – LAB1,SECOND FLOOR, PG BLOCK ★ ★		
	Tuesday	CEN [C-307]	CEN [C-307]		MAT [C-301]	AEC LAB- D4-MSD,D5-ARS D4, D5- VENUE-FIRST FLOOR, PG BLOCK, EIE DEPT. DSD LAB-D1-JMS,D2-GSK,D3-KTK D1, D2 - VENUE-FIRST FLOOR, PG BLOCK , EEE DEPT. D3- VENUE-FIRST FLOOR, PG BLOCK , EIE DEPT.			SKILL LAB ST,SMR,SMR,MAS,FM -L AND T VENUE – LAB1,SECOND FLOOR, PG BLOCK		
	Wednesday	NAL [C-305]	DSD [C-305]		AEC [C-305]	TCE-D1-FM,D2-LS,D3-ED,D4-SMR,D5-ARK VENUE –LAB2, SECOND FLOOR, PG BLOCK			DIP MATHS		
	Thursday	VERILOG LAB-D1-GT,D2-SK,D3-PJ,D4-JP,D5-RCR VENUE –LAB1, SECOND FLOOR, PG BLOCK			MAT [C-ISE-503]	NAL [C-ISE-503]	DSD [C-ISE-503]		SKILL LAB DS,KPL,ST,SS,ST-OPTIMUS APP,GSK,APP-WEARABLE TECH ★★ VENUE – LAB1 & LAB2, SECOND FLOOR, PG BLOCK		
	Friday	AEC[C-304]	NAL [C-304]		MAT [C-303]				AEC LAB-D1-ST,D2-SRD,D3-SHH D1,D2,D3- -LAB-305 DSD LAB-D4-JMS,D5-KTK D4,D5- VENUE-LAB-306		
	Saturday	SKILL LAB DS,BU,MSD,PJ,PJ-L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK				SKILL LAB DS,BU,NKC,KPK,HRA -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK DIP MATHS (11.30 AM TO 1.30 PM)			SKILL LAB MAS,ED,MAS,NKC,ED -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK		
	Course Code	26EC3ASMAT	26EC3IPAEC	26EC3IPDS	26EC3PCNAL	26EC3PCCEN	26EC3AETCE	26EC3PLHDL	26EC3SDSL1		
	Course Name	Program Specific Mathematics	Analog Electronic Circuits	Digital System Design	Network Analysis	Control Engineering	Technical Communication for Engineers	Verilog for System Design	Skill Lab-1	DIP MATHS	
	Faculty	Dr. LAKshmi B N	Prof. SANJANA T	Dr. JAYANTHI K MURTHY	Prof. EESHA D	Dr. SURENDRA H H				Dr. AMBIKA	


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ECE-3E	Monday		MAT [C-306]	BREAK	AEC [C-305]	NAL [C-305]	CEN [C-305]	LUNCH BREAK	SKILL LAB DS,KPL,SS,ST,SS-OPTIMUS VENUE – LAB I,SECOND FLOOR, PG BLOCK ★ ★			DIP MATHS
	Tuesday	TCE-E1-AK,E2-FM,E3-NKC,E4-ED,E5-ARK VENUE –LAB2, SECOND FLOOR, PG BLOCK			AEC [C-303]	MAT (TUT) [C-303]	MAT (TUT) [C-303]		SKILL LAB ST,SMR,SMR,MAS,FM -L AND T VENUE – LAB I,SECOND FLOOR, PG BLOCK			DIP MATHS
	Wednesday	NAL [C-307]	DSD [C-307]		MAT [C-307]	AEC LAB-E1-SHH,E2-SKS,E3-ST E1,E2,E3- -VENUE-LAB-305 DSD LAB-E4-JMS,E5-MPS E4,E5- VENUE-LAB-306			SKILL LAB DS,KPL,ST,SS,ST-OPTIMUS APP,GSK,APP-WEARABLE TECH ★★ VENUE – LAB I & LAB2, SECOND FLOOR, PG BLOCK			
	Thursday		DSD [C-305]		CEN [C-307]	VERILOG LAB-E1-SK,E2-RCR,E3-RJG,E4-PJ,E5-JP VENUE – LAB1, SECOND FLOOR, PG BLOCK			AEC LAB- E4-KV,E5-JRK E4, E5- VENUE-FIRST FLOOR, PG BLOCK, EIE DEPT. DSD LAB-E1-PMK,E2-GSK,E3-MPS E1, E2,- VENUE-FIRST FLOOR, PG BLOCK , EEE DEPT. E3 - VENUE-FIRST FLOOR, PG BLOCK , EIE DEPT.			
	Friday	DSD [C-303]	NAL [C-303]		AEC [C-301]	CEN [C-301]	MAT [C-301]		-	-		
	Saturday	SKILL LAB DS,BU,MSD,PJ,PJ-L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK				SKILL LAB DS,BU,NKC,KPK,HRA -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK DIP MATHS (11.30 AM TO 1.30 PM)			SKILL LAB MAS,ED,MAS,NKC,ED -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK			
	Course Code	26EC3ASMAT	26EC3IPAEC	26EC3IPDSD	26EC3PCNAL	26EC3PCCEN	26EC3AETCE	26EC3PLHDL	26EC3SDSL1			
	Course Name	Program Specific Mathematics	Analog Electronic Circuits	Digital System Design	Network Analysis	Control Engineering	Technical Communication for Engineers	Verilog for System Design	Skill Lab-1	DIP MATHS		
	Faculty	Dr. Gayathri M S	Dr. SESHACHALAM D	Dr. HARISH V MEKALI	Dr. P S MANJUNATH	Prof. Geeta				Dr. AMBIKA		

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ECE-3F	Monday	TCE-F1-AK,F2-GSK,F3-FM,F4-PMK,F5-SKU VENUE -LAB2, SECOND FLOOR, PG BLOCK		BREAK	AEC [C-304]	MAT TUT [C-304]	MAT TUT [C-304]	LUNCH BREAK	SKILL LAB DS,KPL,SS,ST,SS-OPTIMUS VENUE – LAB1,SECOND FLOOR, PG BLOCK ★ ★		
	Tuesday		DSD [C-302]		MAT [PG Block C-5010]	AEC [PG Block C-5010]	NAL [PG Block C-5010]		SKILL LAB ST,SMR,SMR,MAS,FM -L AND T VENUE – LAB1,SECOND FLOOR, PG BLOCK DIP MATHS		
	Wednesday		MAT [C-301]		AEC [C-301]	CEN [C-301]	DSD [C-301]		SKILL LAB DS,KPL,ST,SS,ST-OPTIMUS APP,GSK,APP-WEARABLE TECH ★★ VENUE – LAB1 & LAB2, SECOND FLOOR, PG BLOCK		
	Thursday	AEC LAB-F1-ARK,F2-MSD,F3-AK VENUE F1,F2,F3- VENUE-FIRST FLOOR, PG BLOCK , EIE DEPT. DSD LAB-F4-KPK,F5-MTN F4,F5- VENUE-ECE L-306, PJ BLOCK			NAL [C-301]	DSD [C-301]	CEN [C-301]		VERILOG LAB-F1-GT,F2-KPK,F3-PJ,F4-NSR,F5-JP VENUE –LAB1, SECOND FLOOR, PG BLOCK		
	Friday				CEN [C-304]	MAT [C-304]	NAL [C-304]		AEC LAB- F4-ARK,F5-SHH F4, F5- VENUE-FIRST FLOOR, PG BLOCK, EIE DEPT. DSD LAB-F1-SB,F2-MTN,F3-PMK VENUE F1, F2 - VENUE-FIRST FLOOR, PG BLOCK , EEE DEPT. F3 - VENUE-FIRST FLOOR, PG BLOCK , EIE DEPT.		
	Saturday	SKILL LAB DS,BU,MSD,PJ,PJ-L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK					SKILL LAB DS,BU,NKC,KPK,HRA -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK DIP MATHS (11.30 AM TO 1.30 PM)		SKILL LAB MAS,ED,MAS,NKC,ED -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK		
	Course Code	26EC3ASMAT	26EC3IPAEC	26EC3IPDSD	26EC3PCNAL	26EC3PCCEN	26EC3AETCE	26EC3PLH DL	26EC3SDSL1		
	Course Name	Program Specific Mathematics	Analog Electronic Circuits	Digital System Design	Network Analysis	Control Engineering	Technical Communication for Engineers	Verilog for System Design	Skill Lab-1	DIP MATHS	
	Faculty	Dr. H Y Shrivalli	Dr. H R ARCHANA	Dr. N SRINIVAS RAO	Dr. Surendra H H	Prof. Krishnamurthy K T				Dr. AMBIKA	

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ECE-3G	Monday	MAT [C-303]	CEN [C-303]	BREAK	AEC [C-302]	TCE-G1-AK,G2-ED,G3-LS,G4-ARK,G5-MTN VENUE – LAB2, SECOND FLOOR, PG BLOCK		LUNCH BREAK	SKILL LAB DS,KPL,SS,ST,SS-OPTIMUS VENUE – LAB1,SECOND FLOOR, PG BLOCK		
	Tuesday	AEC LAB- G1-HRA,G2-JRK, G3-KV G1,G2,G3 - - VENUE-LAB 305 DSD LAB-G4-KTK,G5-JMS G4,G5- VENUE-LAB 306			DSD [C-302]	VERILOG LAB-G1-NSR-,G2-PJ,G3-RCR,G4-KPK,G5-GT VENUE –LAB1, SECOND FLOOR, PG BLOCK			★ ★ DIP MATHS		
	Wednesday	MAT [C-303]	DSD [C-303]		AEC [C-304]	NAL [C-303]	CEN [C-303]		SKILL LAB DS,KPL,ST,SS,ST-OPTIMUS APP,GSK,APP-WEARABLE TECH★ ★ VENUE – LAB1 & LAB2, SECOND FLOOR, PG BLOCK		
	Thursday	NAL [C-302]	DSD [C-302]		AEC [C-306]	MAT (TUT) [C-306]	MAT (TUT) [C-306]				
	Friday	CEN [C-301]	NAL [C-301]		MAT [C-305]	AEC LAB-G4-JRK,G5-MSD G4, G5- VENUE-FIRST FLOOR, PG BLOCK, EIE DEPT. DSD LAB-G1-KTK,G2-SB,G3-GSK G1, G2 - VENUE-FIRST FLOOR, PG BLOCK , EEE DEPT. G3 - VENUE-FIRST FLOOR, PG BLOCK , EIE DEPT					
	Saturday	SKILL LAB DS,BU,MSD,PJ,PJ-L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK				SKILL LAB DS,BU,NKC,KPK,HRA -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK DIP MATHS (11.30 AM TO 1.30 PM)			SKILL LAB MAS,ED,MAS,NKC,ED -L AND T VENUE – LAB1, SECOND FLOOR, PG BLOCK		
	Course Code	26EC3ASMAT	26EC3IPAEC	26EC3IPD SD	26EC3PCNAL	26EC3PCCEN	26EC3AETCE	26EC3PLHD L	26EC3SDSL1		
	Course Name	Program Specific Mathematics	Analog Electronic Circuits	Digital System Design	Network Analysis	Control Engineering	Technical Communication for Engineers	Verilog for System Design	Skill Lab-1	DIP MATHS	
	Faculty	Dr. Madhukar	Prof. Archana K	D AJAY KUMAR	Dr. M VASANTHA LAKSHMI	Prof. R C RADHA				Dr. AMBIKA	

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