

B.M.S.COLLEGE OF ENGINEERING, BANGALORE-19  
(AUTONOMOUS COLLEGE UNDER VTU)  
DEPARTMENT OF ELECTRONICS AND COMMUNICATION  
PG TIMETABLE

CLASS ROOM: PG CR-1

SECTION: PG-ELECTRONICS

W.E.F. 27-06-22

	8.55 AM TO 9.50 AM	9.50 AM TO 10.45 AM	10.45 AM TO 11.15 AM	11.15 AM TO 12.10 PM	12.10 PM TO 1.05 PM	1.05 PM TO 2.00P M	2.00 PM TO 2.55 PM	2.55 PM TO 3.50 PM
MONDAY	ELE1-LPVLSI	RTOS	B R E A K	OPEN ELE		B R E A K		
TUESDAY	SODC	ELE2-PP		ELE1-LPVLSI	RTOS			
WEDNESDAY	RTOS	ELE2-PP		SODC	VLSI V		OPEN ELE	
THURSDAY	ELE2-PP	VLSI V		ELE1-LPVLSI	SODC			
FRIDAY	SODC TUT			VLSI V				
SATURDAY	VLSI V TUT			RTOS LAB				
REAL TIME OPERATING SYSTEMS-JKM				SYNTHESIS & OPTIMIZATION OF DIGITAL CIRCUITS-JKM			VLSI VERIFICATION & TESTING-VMB	
ELE1-Low Power VLSI -VMB			ELE2-PROGRAMMING IN PYTHON-GM			OPEN ELE-		

TTO

HOD

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CLASS ROOM: PG CR-2

SECTION: PG-DIGITAL COMMUNICATIONS

W.E.F. 27-06-22

	8.55 AM TO 9.50 AM	9.50 AM TO 10.45 AM	10.45 AM TO 11.15 AM	11.15 AM TO 12.10 PM	12.10 PM TO 1.05 PM	1.05 PM TO 2.00PM	2.00 PM TO 2.55 PM	2.55 PM TO 3.50 PM
MONDAY	AWC	WCNA	B  R  E  A  K	OPEN ELE-QRE		B  R  E  A  K		
TUESDAY	ELE1-CNS	ELE2-CSS		ADSP TUT				
WEDNESDAY	WCNA	ADSP		ELE1-CNS	AWC		OPEN ELE-QRE	
THURSDAY	ADSP			ELE2-CSS	WCNA			
FRIDAY	ELE2-CSS	ELE1-CNS		AWC				
SATURDAY	AWC LAB			WCNA TUT				
ADVANCED WIRELESS COMMUNICATION-ARS				ADVANCED DSP-PGP			Wireless Communication Networks Analytics- SA	
ELE1- Communication Networking Systems SA			ELE2- Communication System Security-ARS			OPEN ELE- QUALITY AND RELIABILITY OF ENGINEERING SYSTEMS -SHH		

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PG TIMETABLE

CLASS ROOM: PG CR-3

SECTION: PG-VLSI

W.E.F. 27-06-22

	8.55 AM TO 9.50 AM	9.50 AM TO 10.45 AM	10.45 AM TO 11.15 AM	11.15 AM TO 12.10 PM	12.10 PM TO 1.05 PM	1.05 PM TO 2.00PM	2.00 PM TO 2.55 PM	2.55 PM TO 3.50 PM	
MONDAY	RTOS	ELE1-SV		OPEN ELE-SL					
TUESDAY	MSCD	RTOS		DFT					
WEDNESDAY	DFT	ELE2-PD		ELE1-SV	MSCD			OPEN ELE-SL	
THURSDAY	ELE1-SV	ELE2-PD		MSCD LAB					
FRIDAY	ELE2-PD	RTOS		DFT	MSCD				
SATURDAY	RTOS LAB			DFT TUT					
MIXED SIGNAL CIRCUIT DESIGN -KSVP		DESIGN FOR TESTABILITY -RJG					REAL TIME OPERATING SYSTEMS-KPL		
ELE1- SYSTEM VERILOG AND VERIFICATION -RJG			ELE2- PHYSICAL DESIGN -KSVP		OPEN ELE- Introduction to scripting languages- JDR				

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