



B.M.S. COLLEGE OF ENGINEERING, BENGALURU-560 019

(Autonomous Institute, Affiliated to VTU)

II SEMESTER M.TECH. THEORY SEMESTER END MAIN EXAMINATION -JULY/AUGUST-2025 (Re-registered)

TIME TABLE

DATE 29.07.2025

Date & Day	M.Tech - Computer Network Engineering		M.Tech - VLSI Design and Embedded System	
	9.30 am to 12.30 pm		9.30 am to 12.30 pm	
	Course Code	Course Title	Course Code	Course Title
04-08-2025 Monday	22ISCNPCCL	Cyber Security and Law	22ECVEPCMS	Analog and mixed signal IC Design
06-08-2025 Wednesday	22ISCNPCBC	Block Chain Technology	22ECVEPCRT	Real Time Operating Systems
08-08-2025 Friday	22ISCNPCNM	Network Management	22ECVEPELP	Low power VLSI
11-08-2025 Monday	22ISCNPE5G	5G Network	22ECVEPCDT	Design for Testability
13-08-2025 Wednesday	22ISCNPEMD	Mobile Application Development	22ECVEENS	Nano Sensors and devices
16-08-2025 Saturday	22SCS2OEBD	Introduction to Big Data	22ECVEPEPD	Physical Design

Devin Singh C. 30/7/25
Controller of Examinations

CONTROLLER OF EXAMINATIONS
B.M.S. College of Engineering
Bengaluru-560 019

Ramesh
Principal
Principal
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