IEEE Antennas and Propagation Society
ACTIVITY REPORT

1. **Name of the Department**: Electronics and Communication Engineering

2. **Name of the Activity**: Value added course on "High frequency Transmission media design for sophomores - A DIY Approach"

3. **Objectives:**

   - To create an exposure to commercial state of the art software on high frequency design
   - To enhance skill in the design of high frequency transmission media with emphasis on industry standard problems

4. **Date, Time and Venue of the Activity:**

   21 May to 4 June 2026, L-302, 3 to 6pm

5. **Name and details of collaborating agency (if it is collaborative/joint activity):** IEEE Antennas and Propagation Society

6. **Brief summary of the Programme:**

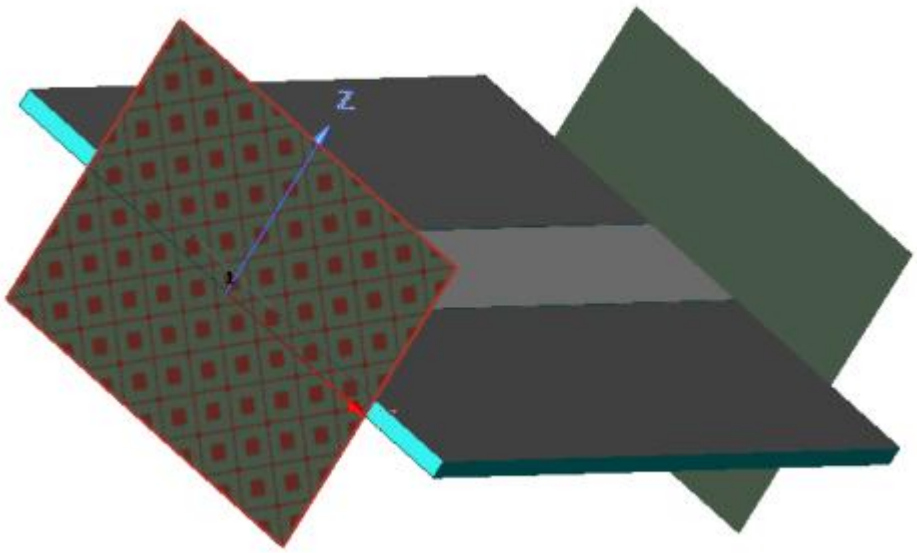
   Students were given multiple industry standard problems. These were executed on commercial full-wave solver by different groups of students. The results were reported and evaluated.

7. **Outcomes:**
   - Exposure to the fundamentals of high frequency transmission media design
   - Exposure to industry standard software to design high frequency transmission media
   - Design and analysis of numerous problems related to high frequency transmission media

8. **Number of participants:**

Particulars	Number of Faculty & Students	Non-Teaching Staff	External Participants
Total Number of Participants 46	IEEE Members : 19 Non-IEEE Members : 27	-	**-**

9. **Photographs of the activity:**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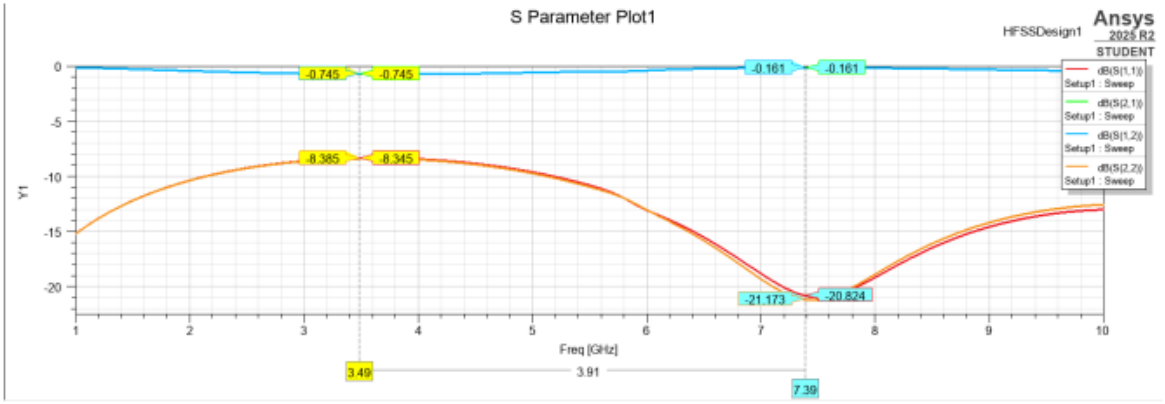
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HFSSDesign1
Ansys
2025 R2
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Setup1 : Sweep
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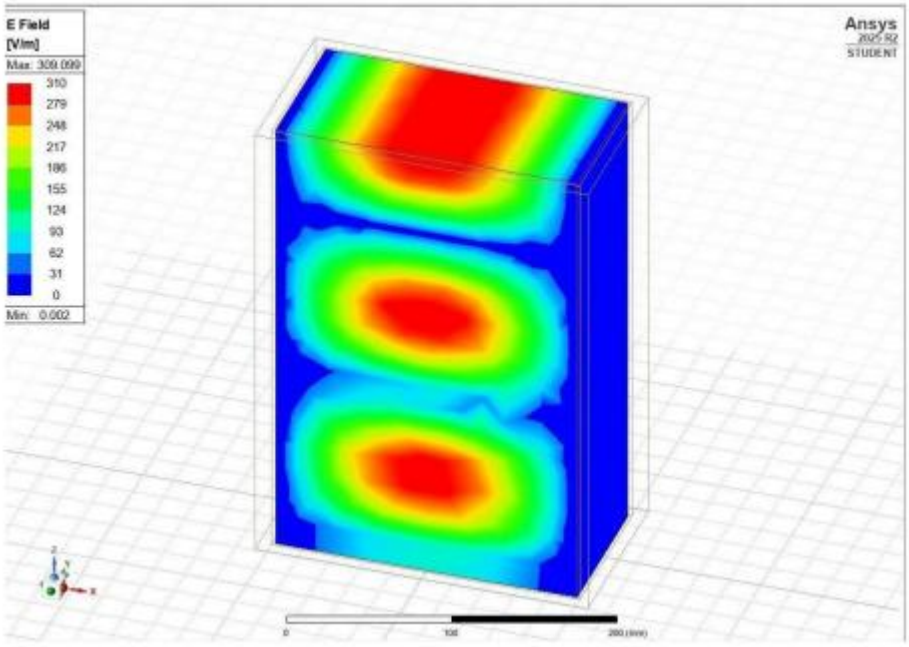
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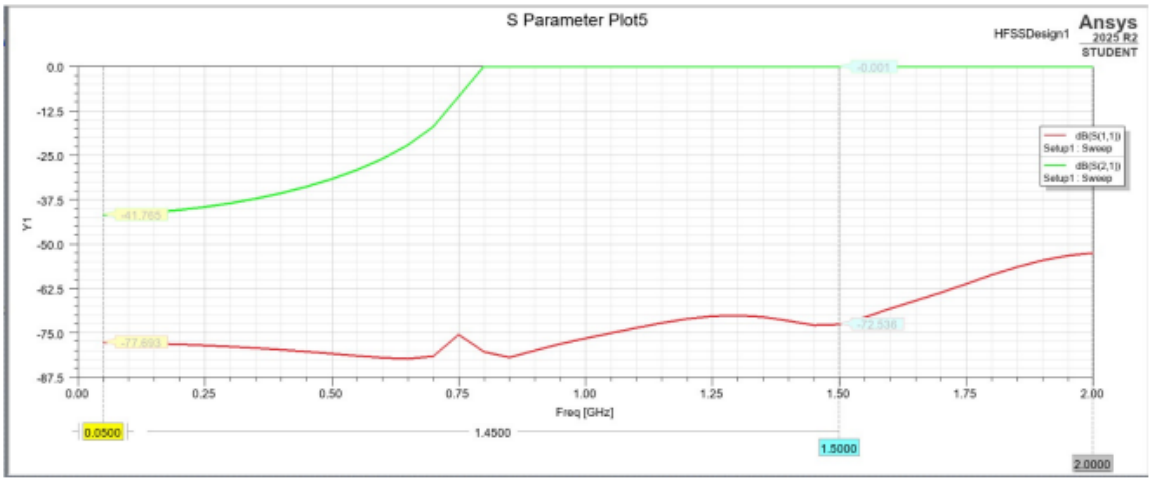

10. **Supporting Documents:**
    a) Attendance sheet
    b) Circulars/Brochures:

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Ansys

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Bull Temple Road, Basavanagudi, Bengaluru 560019
(Autonomous Institute under VTU)

Accredited by NAAC with A++ grade

Organised by

Department of Electronics and Communication Engineering

in association with

Advanced RF and Wireless Communication Lab & IEEE APS SBC

present Value added course on

High Frequency Transmission Media design for sophomores

A DIY Approach

21 May to 4 Jun 2026

About the institution

B.M.S. College of Engineering, Bengaluru has the unique distinction of being the first private engineering college established in the country in 1946. The institution owes its existence to the foresight and vision of its beloved founders, Late Sri B. M. Sreenivasaiah and his illustrious son Sri B. S. Narayan. Imparting quality education and training was the founder's vision for the development of skilled and competent engineers who will go on to become the workforce for the benefit of national prosperity. The college initially started with three undergraduate programmes in 1946 and currently offers 18 undergraduate and 13 postgraduate programmes in conventional and emerging fields. In addition, the college also offers PhD programmes in fifteen of its departments which are recognized as research centers by the University, and is also approved as QIP Centre in Engineering and Technology by AICTE. The college maintains high academic quality standards, the certification by the National Assessment and Accreditation Council (NAAC) and National Board of Accreditation (NBA) bearing testimony for the same. In fact it is the first few institutions in India to be bestowed with NBA in Tier-I Format (Washington Accord) in the year 2013. The institute is accredited by National Assessment and Accreditation Council (NAAC) with the highest grade of A++ in the second Cycle with a CGPA of 3.83 on a scale of four. The institution is a proud recipient of TEQIPIII (World Bank Funded Project) after successful participation in TEQIP-I and II Projects. BMSCE is the only partner institution in the country with the Melton Foundation, USA which promotes cross-cultural learning for selected students along with peers from five other countries.

About the Department

The department of Electronics and Communication Engineering (ECE) of BMSCE was established in 1971 with an initial intake of 60 students to the Undergraduate (UG) program and enhanced to an intake of 120 students from 1983 and 180 students from 2018. The intake has been raised to 420 since 2024. The department offers One Postgraduate Programme: "M. Tech. VLSI Design & Embedded Systems" from 2014 with an intake of 24 students. The department is also a recognized Research Centre (RC) by VTU from 2002 and is a recognized Quality Improvement Programme (QIP) center by the AICTE from 2011. With these activities on-hand, the overall objective of the department is to contribute significantly to the realization of the vision of BMSCE. The department is accredited by NBA for three years under TIER-I from 2023 to 2026. The department has a long tradition of excellence in educating, mentoring, and inspiring future technology leaders and researchers in the area of Electronics and Communication Engineering.

ABOUT THE PROGRAM:

1. The program is open to IV Semester UG students.
2. It includes certification along with hand-on-training.
3. Students eager to join hardware companies are encouraged to apply this internship
4. The number of candidates are limited to eighty.

EXPECTED OUTCOMES:

1. Learn the fundamentals of transmission Parameters, industry perspective
2. Exposure to real-world high frequency transmission media and its applications
3. Exposure to industry-standard software
4. Design fundamental transmission media with real-world specifications
5. Exposure to fabrication and experimental characterization of high frequency transmission media
6. Opportunity to work on a trending research project
7. Exposure to technical writing on microwave engineering

DATES:
21 May to 4 Jun 2026
VENUE: L-302, PJ Block

Register by scanning the **QR Code**

Registration fee:
Rs. 300/-
(to Mr. Abhiram Kashyap)
30% discount for IEEE APS members
**Seats limited to 40*