

**WORKSHOPS-2016**

<b>Sl. No</b>	<b>Event</b>	<b>Resource Person</b>	<b>No. of Participants</b>	<b>No. of days</b>	<b>Venue</b>	<b>Date</b>
<b>1</b>	<b>Workshop on "Technical Writing"</b>		<b>88</b>			<b>08.01.2016 &amp; 09.01.2016</b>
<b>2</b>	<b>Workshop on "Intellectual Property Rights"</b>		<b>94 students 54 faculties</b>			<b>04.02.2016 &amp; 05.02.2016</b>
<b>3</b>	<b>Workshop on "Advances in Verification Methodologies"</b>		<b>21</b>	<b>3 days</b>		<b>13th to 15th June 2016</b>
<b>4</b>	<b>One week Faculty Development Programme on "EMI and EMC in Electronic Systems"</b>	<b>Dr. Gunashekar, Retd. IISc., &amp; Dr. N.C. Joshi, STQC</b>	<b>20</b>	<b>5 Days</b>	<b>BMSCE</b>	<b>25th to 29th July, 2016</b>
<b>5</b>	<b>Finishing School on "VLSI Physical Design"</b>	<b>Dr. R. Jayagowri &amp; Dr. Kiran Bailey</b>	<b>11</b>	<b>6 Days</b>	<b>Centre of Excellence, BMSCE</b>	<b>1-6 August, 2016</b>

