



ChipEdge

Partnership Memorandum of Understanding

between

BMS College of Engineering

and

ChipEdge Technologies Pvt Ltd

Scope

BMS and ChipEdge have agreed to partner and collaborate to offer industry oriented VLSI training programs to BMS students and outside participants, in BMS campus.

Goals and objectives:

1. To improve the quality of VLSI teaching & technical expertise at BMS to a level, where academic community and industry recognize BMS as the best in India for VLSI Education.
2. To offer industry oriented quality VLSI Training programs to BMS students and outside participants, including working professionals, faculties & students from other colleges

Planned Programs under this partnership:

1. Weekend Courses to working professionals.
2. Student Courses during semester holidays to BMS and outside students.
3. Faculty development programs to BMS & outside faculties.
4. Industry professionals teaching selected hours in BMS academic subjects

Resources:

ChipEdge will be using BMS class rooms & VLSI lab during weekends, with EDA tools installed.