B.M.S COLLEGE OF ENGINEERING, BENGALURU-19
(Autonomous Institute, Affiliated To VTU)

Department of Electrical and Electronics Engineering

EVENT REPORT

Title	**VLSI, VERILOG, ROBOTICS AND EMBEDDED SYSTEMS**	
Date	3rd June 2024	7th June 2024
Time	3:30 PM	6:30 PM
Duration	1 week	
Event Type	Short Term Training Program	
Brief Description (4-5 Lines Max)	This offline-mode STTP focuses on recent developments and future trends in "VLSI, Verilog, Robotics, and Embedded Systems." It covers essential topics such as VLSI design and HDLs, CMOS logic design, and testing, along with combinational and sequential logic circuits. The program also delves into Verilog basics, including syntax, data types, and operators, as well as behavioral and structural modeling for circuits. Additionally, it offers an introduction to embedded systems, embedded C programming, and memory management and design in embedded systems, blending theoretical and practical insights.	
Poster		

	1991 with a sanctioned intake of 18 which is also NBA Accredited. Additionally, it operates a research center under VTU since 2003, gaining recognition as an AICTE QIP and NDF Research Center in 2012 and in 2018 respectively. **About IEEE Society on Social Implications of Technology** The IEEE SSIT (Society on Social Implications of Technology) is a professional society within the Institute of Electrical and Electronics Engineers (IEEE) that focuses on understanding and addressing the societal implications of technology. SSIT members come from various disciplines, including engineering, social sciences, and humanities, and they work together to explore the ethical, legal, and social impacts of technology on individuals, communities, and society as a whole. The society promotes responsible and sustainable technology development, aiming to foster dialogue and collaboration among technologists, policymakers, and the public to ensure that technology serves the common good. **About this Short Term Training Program** This STTP is organized in an offline mode and introduces the recent developments and future outcomes addressing the related theoretical and practical aspects of **"VLSI, Verilog, Robotics and Embedded Systems"** The major course contents, include, but not limited to • Introduction to VLSI Design and HDLs • CMOS Logic Design and Testing • Combinational and Sequential Logics • Verilog Basics: Syntax, Data Types, Operators • Behavioral and Structural Modeling • Verilog for Combinational and Sequential Circuits • Introduction to Embedded Systems • Embedded C Programming • Memory Management and Design in Embedded Systems **Outcomes of Short Term Training Program** At the end of the course, the participant would be able to • Identify the emerging areas engineering domains to carry out research work. • Enhance their understanding of VLSI, Verilog and Embedded Systems. • Implement the skillset gained in allied areas develop efficient hardware solutions. **Who can register?** This STTP is useful to the students (preferably third and final year students), postgraduation students, and research scholars. **Note:** • This Short Term Training Program is purely offline. No online requests are entertained. • Candidates must bring their laptops for smooth participation. • Certificates will be issued upon successful completion. • Minimum 85% attendance is mandatory. • An AAT activity follows; passing it, alongside meeting attendance criteria, earns certificates. **For More Details, Please Contact** **Dr. Prakash. D. B** Assistant Professor, EEE, BMSCE Email: prakashdb.ece@bmscc.ac.in Contact Number: +91-9480235493 **Dr. Venkata Madhava Ram Tatabhatla** Assistant Professor, Dept. of EEE, Email: madhavaram.ece@bmscc.ac.in Contact Number: +91-7893222486
Event Photos	
No. of Participants	50

Faculty Co-ordinators
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