BMS College of Engineering Bangalore

Refresher Course
On
FUNDAMENTALS OF HDL

29th January 2020 to 31st January 2020

(Between 2pm and 4pm)

Organized

By

Dept. of Electrical & Electronics Engg

Patrons

Dr. B.S Ragini Narayan, Donor Trustee

Dr.P.Dayananda Pai, Chairman, BMSCE

Advisors

Dr.B.V.Ravishankar,
Principal, BMSCE

Dr.S. Muralidhara,
Vice Principal, BMSCE

Dr.P.Meena, Prof. & HOD,EEE, BMSCE

Convenors

Dr. A.N.Nagashree, Associate Prof., EEE, BMSCE

Resource Person

Prof. Divya.S, Dept. of EEE, BMSCE

About BMS College of Engineering

BMS College of Engineering (BMSCE) was founded by a great visionary and philanthropist Late Sri. B. M. Sreenivasaiah (BMS) in the year 1946. After the demise of the founder, Sri.B.S.Narayan the illustrious son of the Founder took over the reins of the College. Under his able leadership, the college grew from strength to strength. BMSCE is the first engineering college established in the country (pre independent India) by a private enterprise. The college is an aided institution (by Government of Karnataka) and affiliated to Visvesvaraya Technological University (VTU). BMSCE offers 13 UG, 16 PG & 15 Research programs. The College became an autonomous institution under VTU in the year 2008-09. In the year 2011, BMSCE was recognized as a QIP Centre in Engineering & Technology by All India Council for Technical Education (AICTE). The College is one among the 14 Engineering Colleges in the State qualified for Phase-2 of the Technical Education Quality Improvement Programme (TEQIP), a world bank sponsored project. BMSCE is the only partner institution from India along with the other universities located in Chile, China, Germany and USA for the Melton Foundation, USA. The Melton Foundation advocates Global citizenship programmes for the selected graduating students in its campuses.

About the Department

The Department of Electrical Engineering was established in 1946 and subsequently emerged as Department of Electrical and Electronics Engineering. The Department has the distinction of having its graduates known worldwide for their contributions and accomplishments in varied domains. It nurtures a tradition of providing a free, at the same time an encouraging and motivating environment conducive to an effective learning experience beyond the curriculum. The focus of the department is to equip students with capabilities through a wide exposure to all aspects facilitating their development as professionals. This enables them to adapt to the societal needs and the progressive technological requirements.

The department receives grants from Govt. of Karnataka and offers an undergraduate program B.E, Electrical and Electronics, with a sanctioned intake of 60. The department is NBA Accredited under Tier I of Washington Accord and has a Student Faculty Ratio of 14.8:1.The department offers M.Tech, program in Power Electronics which started in 1991 with a sanctioned intake of 18 which is also NBA Accredited. It has a research Centre under VTU since 2003. It is a recognized AICTE QIP Research Centre since 2012, AICTE NDF (National Doctoral Fellowship Scheme) Research centre since 2018. The number of awarded Ph.Ds till date is 11 and currently 15 research scholars are pursuing their research in various domains.

About the Refresher Course

The course would deal with the study of programming language for the implementation of Digital Electronic Systems. Two major styles, namely, VHDL and Verilog will be dealt with.

Objectives of the Course

- Understand the structure of a HDL module, its operators etc.,
- Brief discussion on the styles/types of descriptions.

Outcomes

- To apply digital electronics engineering fundamentals to identify, formulate and analyze various digital systems.
- To test and interpret the data through experiments using modern IT tools.
- This course is a pre requisite for the course, Circuit Design using VLSI.

Target Participants

- Sixth Semester EEE Dept. students who have opted Circuit Design using VLSI Department elective course.

About Registration

- Students are required to fill the registration form and submit it to Prof. Divya S on the day of even semester course registration, i.e. on 27th January 2020.

For More Details, You May Please Contact the coordinator:
Prof. Divya S
Assistant Professor, Dept of EEE, BMSCE
Tel: 7795080678
Email: divyas.eee@bmsce.ac.in

Registration Form

Refresher Course

On

FUNDAMENTALS OF HDL

29th January 2020 to 31st January 2020

(Between 2pm and 4pm)

Venue: B.S.N.Hall

1. Name:
2. USN:
3. Department:
4. Email Id:
5. Mobile No:
6. Signature & date:

Signature of the Head of the Department
(With Seal)