### DEPARTMENT OF ELECTRONICS AND COMMUNICATION

### DRAFT PG TIMETABLE

CLASS ROOM: PG CR-1 SECTION: PG-ELECTRONICS

	8.55 AM TO 9.50 AM	9.50 AM TO 10.45 AM	10.45 AM TO 11.15 AM	ТО	12.10 PM TO 1.05 PM	1.05 PM TO 2.00PM	2.00 PM TO 2.55 PM	2.55 PM TO 3.50 PM
MONDAY		SD		AM	CN		AES	
TUESDAY		ELE1-AE	В	CN	SD	В	AES	LAB
WEDNESDAY		CN	R E	SD	SD AES		OPEN I	ELE-RM
THURSDAY		ELE2-SN	А	SE	SD LAB		AM	AES
FRIDAY		AM	К	ELI	ELE2-SN		ELE	1-AE
SATURDAY								
Advanced Embedded Applied Mather Systems -NF PGP		natics-	natics- Digital System Design -VMB		Advanced Computer Networks -JKM			
ELE1- Automotive Electronics-BSN			ELE2- Wireless Sensor Network -JKM			OPEN ELE- Research Methodology -LS		

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION

### DRAFT PG TIMETABLE

CLASS ROOM: PG CR-2 SECTION: PG-DIGITAL COMMUNICATIONS

	8.55 AM TO 9.50 AM	9.50 AM TO 10.45 AM	10.45 AM TO 11.15 AM	11.15 AM TO 12.10 PM	12.10 PM TO 1.05 PM	1.05 PM TO 2.00PM	2.00 PM TO 2.55 PM	2.55 PM TO 3.50 PM
MONDAY		RS		AM	WN		RS L	AB
TUESDAY	ELE1-OC	RS	B R	DC	ELE2-FG	B R	WNT	ГИТ
WEDNESDAY		DC	E	WN	RS	E	OPEN E	LE-RM
THURSDAY	WN	DC	A	ELE2-FG		A	AM	
FRIDAY		AM	К	ELE1-OC ELE1-OC		К	DC T	UT
SATURDAY								
Radiating Systems-ARS Applied M PGP		Applied Mat	hematics-	Advanced D	igital Communio	cation-MNS ELE2-Fifth Generation Networks- ARS		
Wireless Networks-SA			ELE1- Optical C Networks -SA	communicatio	n and	OPEN ELE- Research Methodology -LS		

# DEPARTMENT OF ELECTRONICS AND COMMUNICATION DRAFT PG TIMETABLE

CLASS ROOM: PG CR-3

SECTION: PG-VLSI

	8.55 AM TO 9.50 AM	9.50 AM TO 10.45 AM	10.45 AM TO 11.15 AM	11.15 AM TO 12.10 PM	12.10 PM TO 1.05 PM	1.05 PM TO 2.00PM	2.00 PM TO 2.55 PM	2.55 PM TO 3.50 PM
MONDAY	ELE1-ST	Al		AM	DI		ES	LAB
TUESDAY	ELE1-ST	ELE1-ST		Al	AM			
WEDNESDAY	ELE2-LP	ELE2-LP		DI	ES		OPEN	ELE-RM
THURSDAY	ES	DI		АМ	Al			
FRIDAY	ELE2-LP	ES		DI LAB				
SATURDAY								
Digital IC design- KSVP A		Applied Mathematics-BSN		Embedded Software and Hardware Architecture-KPL		Analog IC design-KSVP		
ELE1- Static Timing Analysis-RJG ELE			ow Power VI	LSI Design-RJG		OPEN ELE- Research Methodology –LS		

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION

### DRAFT PG TIMETABLE

CLASS ROOM: PG CR-1 SECTION: PG-ELECTRONICS

	8.55 AM TO 9.50 AM	9.50 AM TO 10.45 AM	10.45 AM TO 11.15 AM	11.15 AM TO 12.10 PM	12.10 PM TO 1.05 PM	1.05 PM TO 2.00PM	2.00 PM TO 2.55 PM	2.55 PM TO 3.50 PM
MONDAY								
TUESDAY			В			В		
WEDNESDAY			R E			R E		
THURSDAY			А			А		
FRIDAY			К			К		
SATURDAY	SoC/ML	SoC/ML		SoC TUT	/ML TUT			
System on Chi	System on Chip- VMB							

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION

#### DRAFT PG TIMETABLE

CLASS ROOM: PG CR-2 SECTION: PG-DIGITAL COMMUNICATIONS

	8.55 AM TO 9.50 AM	9.50 AM TO 10.45 AM	10.45 AM TO 11.15 AM	11.15 AM TO 12.10 PM	12.10 PM TO 1.05 PM	1.05 PM TO 2.00PM	2.00 PM TO 2.55 PM	2.55 PM TO 3.50 PM
MONDAY								
MONDAI								
TUESDAY			В			В		
			R			R		
WEDNESDAY			E			E		
THURSDAY			А			А		
			К			К		
FRIDAY							NOC on	line class
SATURDAY					NOC		NOC	TUT
Network on Chip-JDR				•				

### DEPARTMENT OF ELECTRONICS AND COMMUNICATION

### DRAFT PG TIMETABLE

CLASS ROOM: PG CR-3 SECTION: PG-VLSI

	8.55 AM TO 9.50 AM	9.50 AM TO 10.45 AM	10.45 AM TO 11.15 AM	11.15 AM TO 12.10 PM	12.10 PM TO 1.05 PM	1.05 PM TO 2.00PM	2.00 PM TO 2.55 PM	2.55 PM TO 3.50 PM
MONDAY								
TUESDAY								
WEDNESDAY								
THURSDAY								
FRIDAY				SL on	line class			
SATURDAY	SL	TUT						
Introduction	to scripting lang	guages-JDR			•			